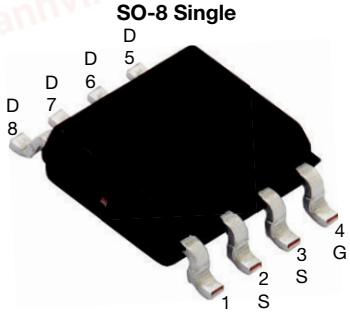
chanhvinh.com

P-Channel 30 V (D-S) MOSFET

SO-8 Single



Top View

FEATURES

- TrenchFET® power MOSFET
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

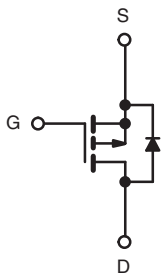
Pb-free

RoHS COMPLIANT

HALOGEN FREE Available

APPLICATIONS

- Load switches
  - Notebook PCs
  - Desktop PCs



P-Channel MOSFET

PRODUCT SUMMARY	
V_{DS} (V)	-30
$R_{DS(on)}$ max. (Ω) at V_{GS} = -10 V	0.018
$R_{DS(on)}$ max. (Ω) at V_{GS} = -4.5 V	0.030
Q_g typ. (nC)	22
I_D (A) [d]	-13
Configuration	Single

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free	Si4835DDY-T1-E3
Lead (Pb)-free and halogen-free	Si4835DDY-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	-30	V
Gate-source voltage		V_{GS}	±25	
Continuous drain current (T_J = 150 °C)	T_C = 25 °C	I_D	-13	A
	T_C = 70 °C		-10.5	
	T_A = 25 °C		-8.7 [a, b]	
	T_A = 70 °C		-7.7 [a, b]	
Pulsed drain current		I_{DM}	-50	
Continuous source-drain diode current	T_C = 25 °C	I_S	-4.6	
	T_A = 25 °C		2 [a, b]	
Avalanche current	L = 0.1 mH	I_{AS}	-20	
Single-pulse avalanche energy		E_{AS}	20	mJ
Maximum power dissipation	T_C = 25 °C	P_D	5.6	W
	T_C = 70 °C		3.6	
	T_A = 25 °C		2.5 [a, b]	
	T_A = 70 °C		1.6 [a, b]	
Operating junction and storage temperature range		T_J, T_{stg}	-55 to +150	°C

THERMAL RESISTANCE RATINGS

PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient [a, c]	t ≤ 10 s	R_{thJA}	39	50	°C/W
Maximum junction-to-foot	Steady state	R_{thJF}	18	22	

Notes

a. Surface mounted on 1" x 1" FR4 board
b. t = 10 s
c. Maximum under steady state conditions is 85 °C/W
d. Based on T_C = 25 °C

For technical questions, contact: pmostechsupport@vishay.com

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

chanhvinh.com

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static						
Drain-source breakdown voltage	V_{DS}	V_{GS} = 0 V, I_D = -250 µA	-30	-	-	V
V_{DS} temperature coefficient	$\Delta V_{DS}/T_J$	I_D = -250 µA	-	-31	-	mV/°C
$V_{GS(th)}$ temperature coefficient	$\Delta V_{GS(th)}/T_J$		-	5.5	-	
Gate-source threshold voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = -250 µA	-1	-	-3	V
Gate-source leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 25 V	-	-	± 100	nA
Zero gate voltage drain current	I_{DSS}	V_{DS} = -30 V, V_{GS} = 0 V	-	-	-1	µA
		V_{DS} = -30 V, V_{GS} = 0 V, T_J = 55 °C	-	-	-5	
On-state drain current [a]	$I_{D(on)}$	$V_{DS} \geq$ -10 V, V_{GS} = -10 V	-30	-	-	A
Drain-source on-state resistance [a]	$R_{DS(on)}$	V_{GS} = -10 V, I_D = -10 A	-	0.0140	0.0180	Ω
		V_{GS} = -4.5 V, I_D = -7 A	-	0.0245	0.0300	
Forward transconductance [a]	g_{fs}	V_{DS} = -10 V, I_D = -10 A	-	23	-	S
Dynamic [b]						
Input capacitance	C_{iss}	V_{DS} = -15 V, V_{GS} = 0 V, f = 1 MHz	-	1960	-	pF
Output capacitance	C_{oss}		-	380	-	
Reverse transfer capacitance	C_{rss}		-	325	-	
Total gate charge	Q_g	V_{DS} = -15 V, V_{GS} = -10 V, I_D = -10 A	-	43	65	nC
		V_{DS} = -15 V, V_{GS} = -4.5 V, I_D = -10 A	-	22	33	
Gate-source charge	Q_{gs}		-	6	-	
Gate-drain charge	Q_{gd}		-	11	-	
Gate resistance	R_g	f = 1 MHz	0.3	1.3	2.5	Ω
Turn-on delay time	$t_{d(on)}$	V_{DD} = -15 V, R_L = 3 Ω $I_D \cong$ -5 A, V_{GEN} = -10 V, R_g = 1 Ω	-	11	22	ns
Rise time	t_r		-	13	25	
Turn-off delay time	$t_{d(off)}$		-	32	50	
Fall time	t_f		-	9	18	
Turn-on delay time	$t_{d(on)}$	V_{DD} = -15 V, R_L = 3 Ω $I_D \cong$ -5 A, V_{GEN} = -4.5 V, R_g = 1 Ω	-	44	70	
Rise time	t_r		-	100	160	
Turn-off delay time	$t_{d(off)}$		-	28	50	
Fall time	t_f		-	15	30	
Drain-Source Body Diode Characteristics						
Continuous source-drain diode current	I_S	T_C = 25 °C	-	-	-4.6	A
Pulse diode forward current	I_{SM}		-	-	-50	
Body diode voltage	V_{SD}	I_S = -2 A, V_{GS} = 0 V	-	-0.75	-1.2	V
Body diode reverse recovery time	t_{rr}	I_F = -2 A, di/dt = 100 A/µs, T_J = 25 °C	-	28	45	ns
Body diode reverse recovery charge	Q_{rr}		-	20	40	nC
Reverse recovery fall time	t_a		-	13	-	ns
Reverse recovery rise time	t_b		-	15	-	

Notes

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %

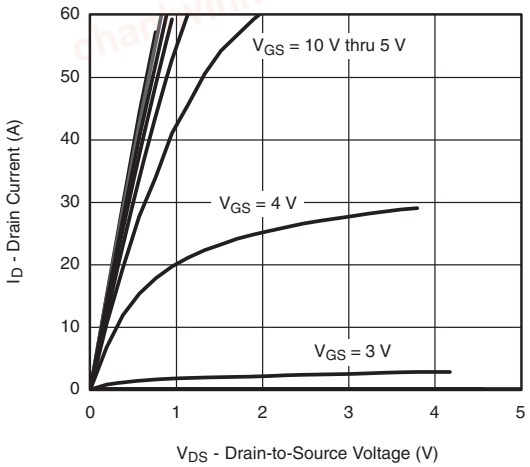
b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

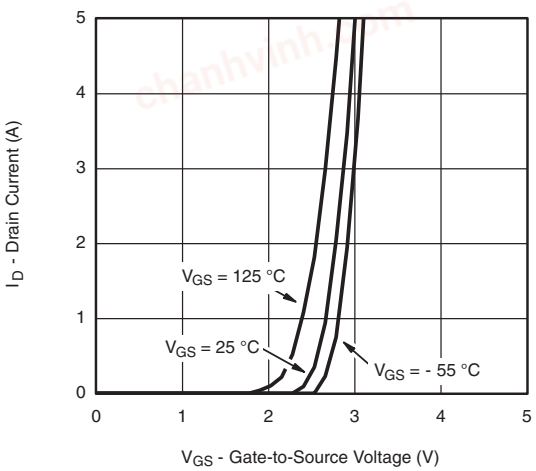
For technical questions, contact: pmostechsupport@vishay.com
THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

chanhvinh.com

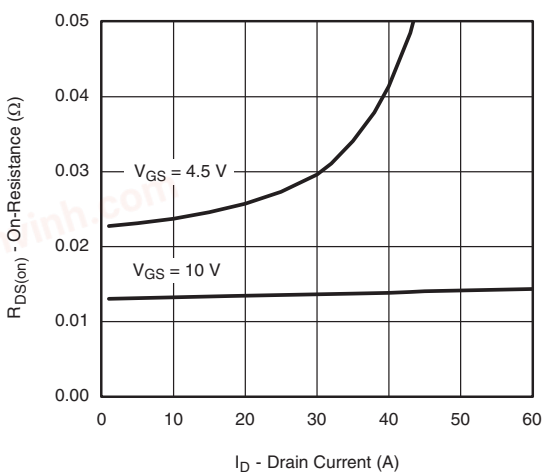
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



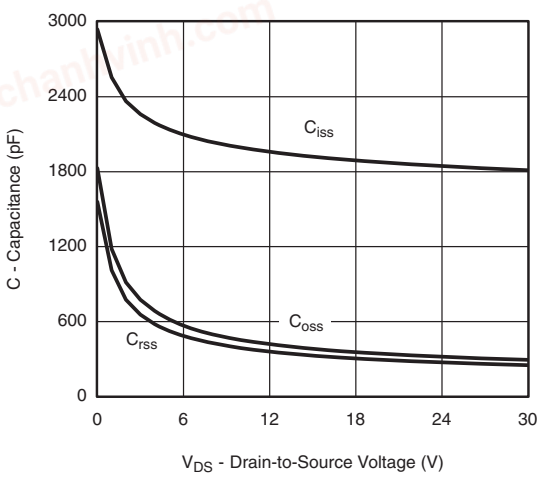
Output Characteristics



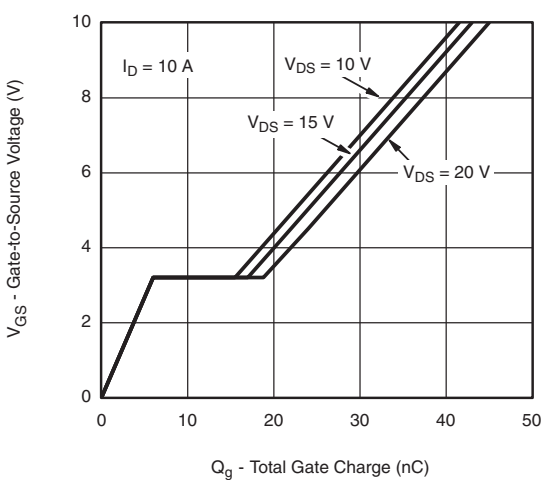
Transfer Characteristics



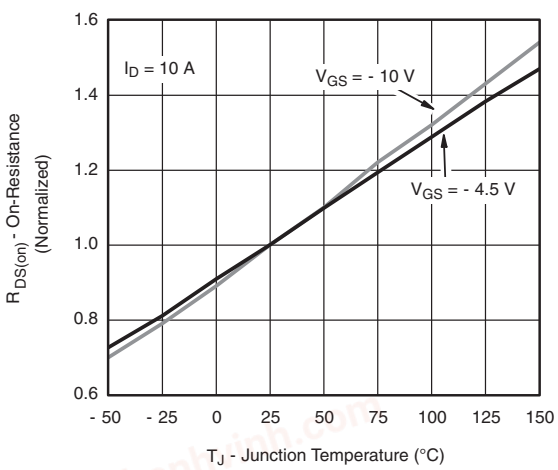
On-Resistance vs. Drain Current



Capacitance



Gate Charge

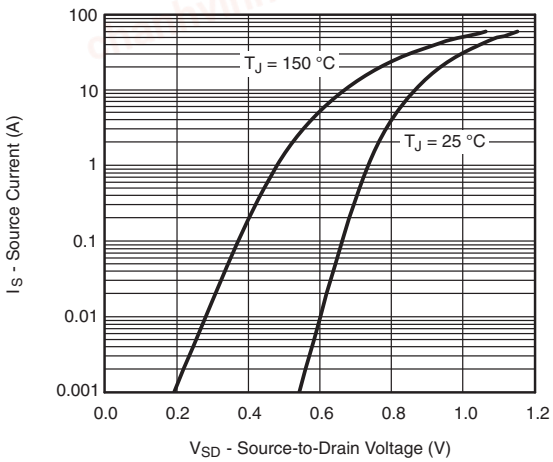


On-Resistance vs. Junction Temperature

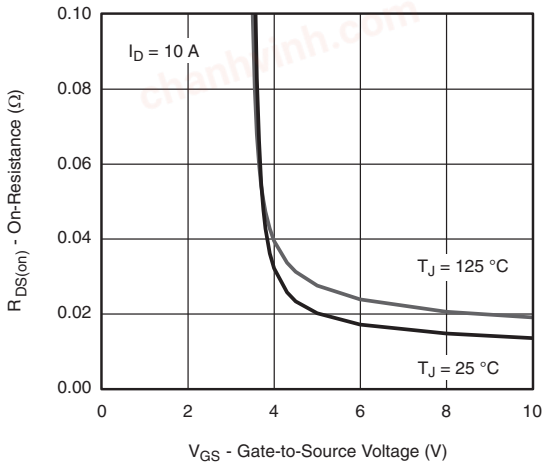
For technical questions, contact: pmostechsupport@vishay.com
THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

chanhvinh.com

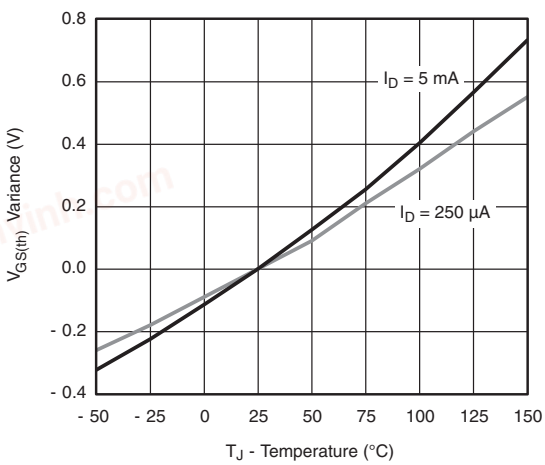
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



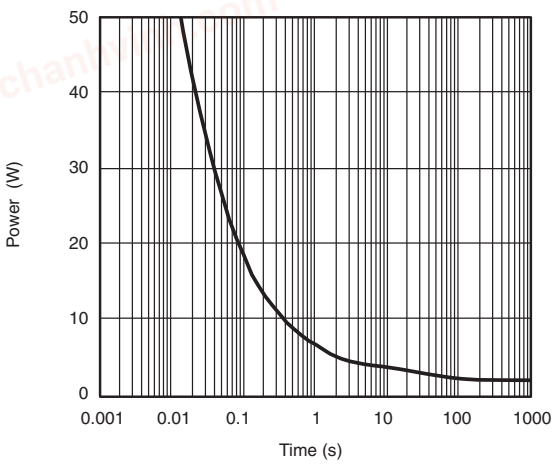
Source-Drain Diode Forward Voltage



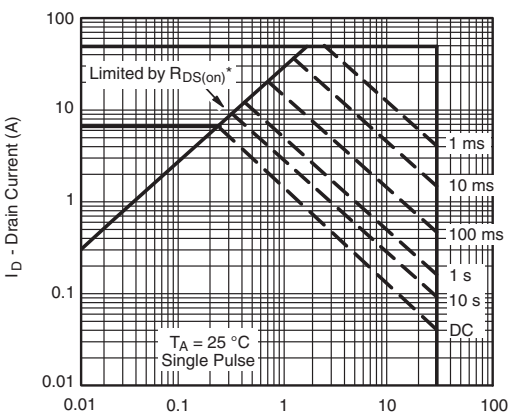
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient



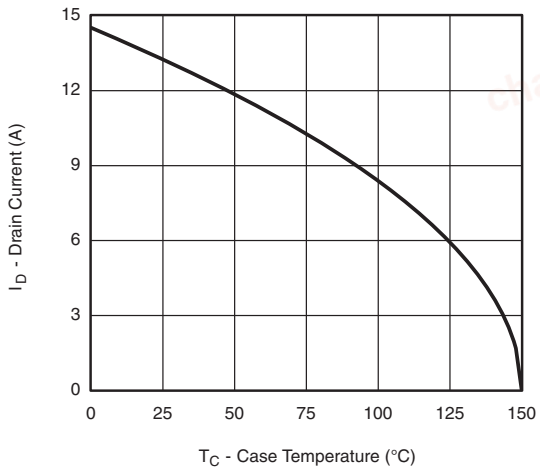
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area

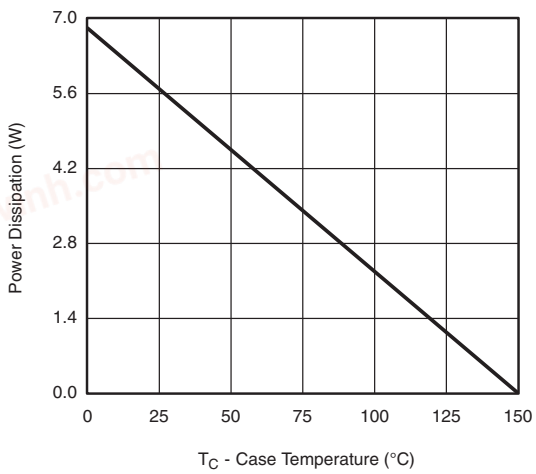
For technical questions, contact: pmostechsupport@vishay.com
THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

chanhvinh.com

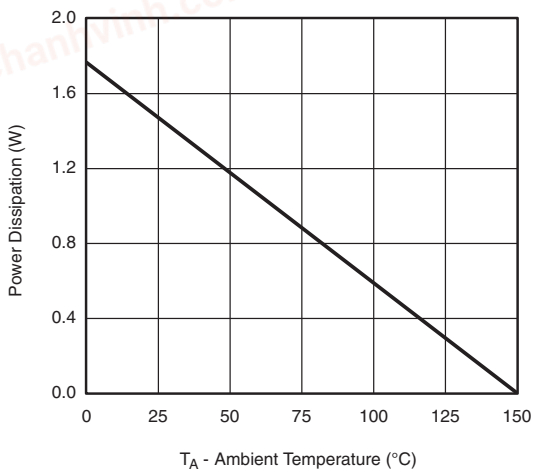
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating [a]



Power, Junction-to-Foot



Power Derating, Junction-to-Ambient

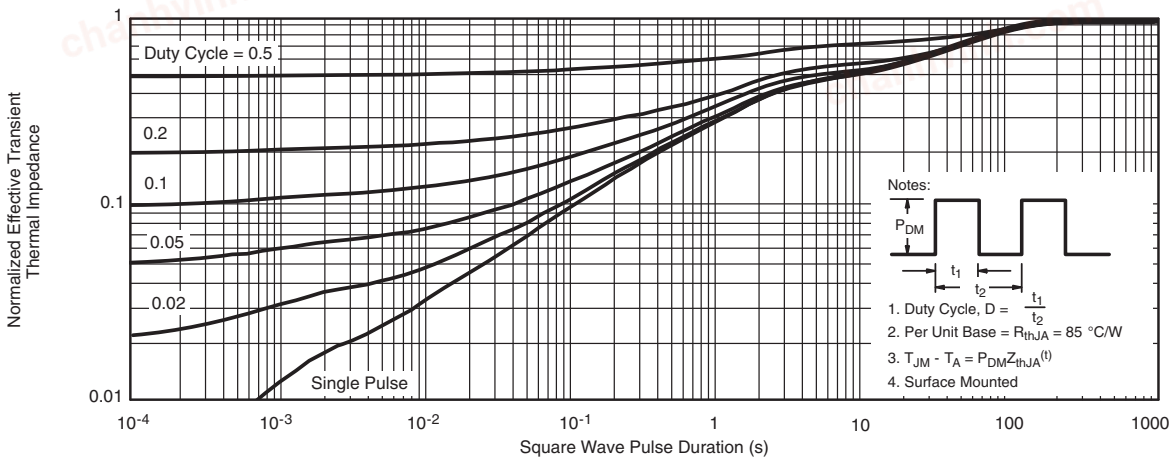
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

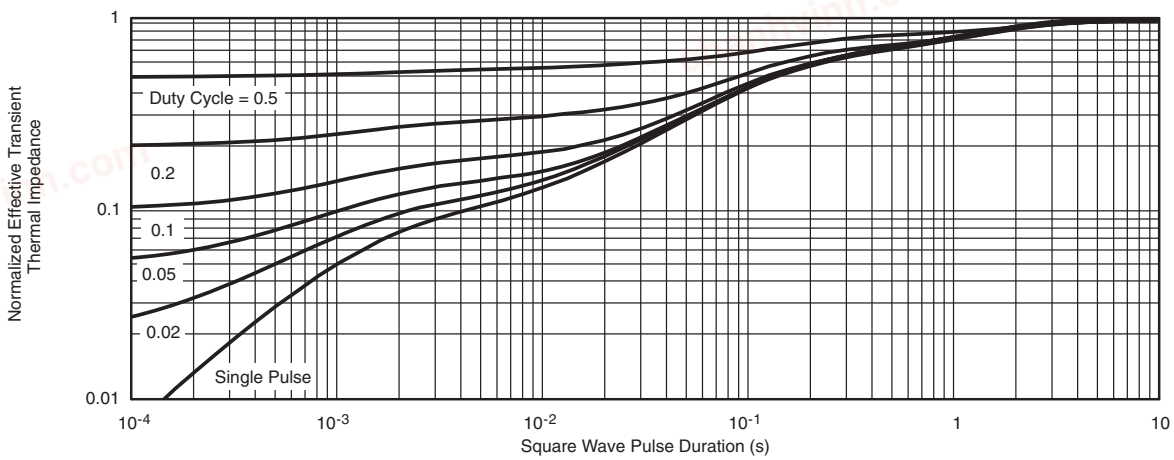
For technical questions, contact: pmostechsupport@vishay.com
THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

chanhvinh.com

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?69953.

For technical questions, contact: pmostechsupport@vishay.com
THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

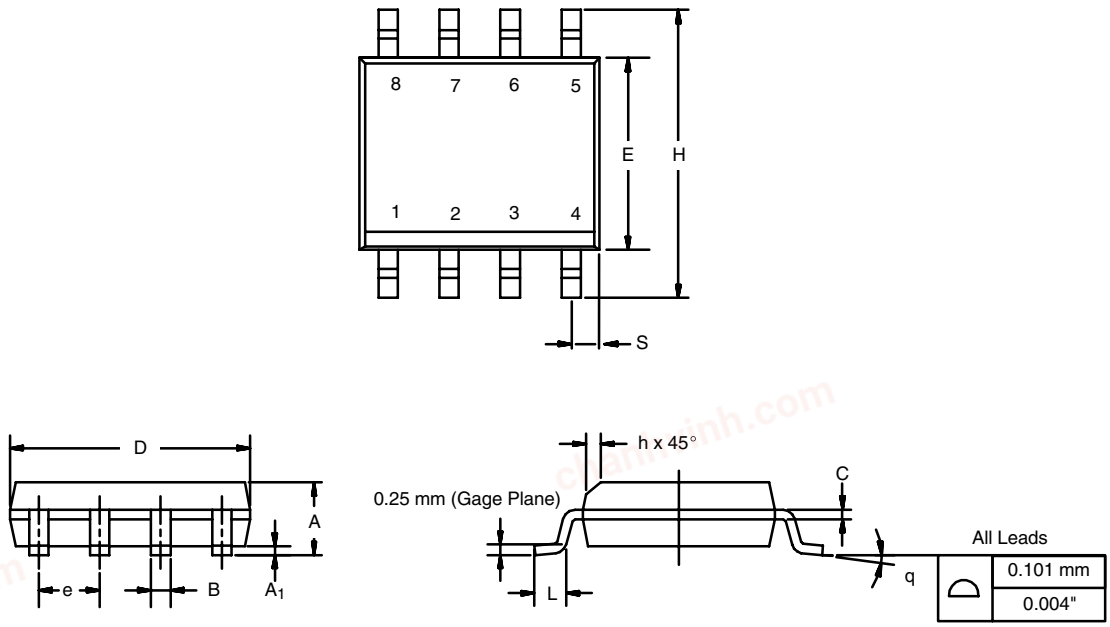
chanhvinh.com

Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD

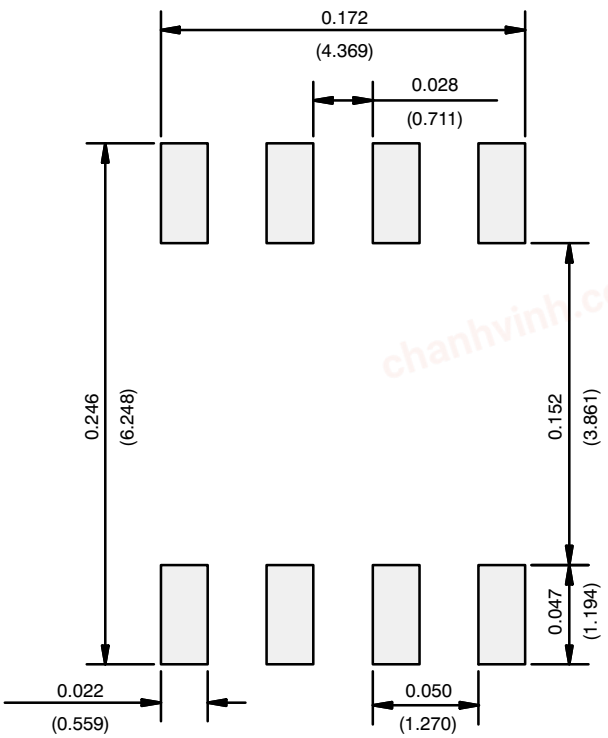
JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A_1	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498				

chanhvinh.com

RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads
Dimensions in Inches/(mm)

Return to Index

chanhvinh.com

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, “Vishay”), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2026 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000