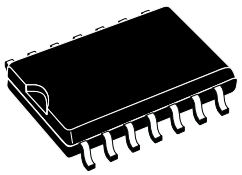
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LM2901

Low-power quad voltage comparator

Datasheet - production data



SO14



TSSOP14



QFN16 3x3
wettable flank

Features

- Wide single supply voltage range or dual supplies for all devices: +2 V to +36 V or ±1 V to ±18 V
- Very low supply current (1.1 mA) independent of supply voltage (1.4 mW/comparator at +5 V)
- Low input bias current: 25 nA typ.
- Low input offset current: ±5 nA typ.
- Input common-mode voltage range includes negative rail
- Low output saturation voltage: 250 mV typ. (I_O = 4 mA)
- Differential input voltage range equal to the supply voltage
- TTL, DTL, ECL, MOS, CMOS compatible outputs

Description

This device consists of four independent precision voltage comparators, which are designed specifically to operate from a single supply over a wide range of voltages. Operation from split power supplies is also possible.

These comparators also have a unique characteristic in that the input common-mode voltage range includes the negative rail even though operated from a single power supply voltage.

This is information on a product in full production. www.st.com

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Contents

1 Pin connection and schematic diagram 3

2 Absolute maximum ratings and operating conditions 4

3 Electrical characteristics 5

4 Typical application schematics 7

5 Package information 10

5.1 QFN16 3 x 3 package information 11

5.2 QFN16 3 x 3 wettable flank package information 14

5.3 SO-14 package information 16

5.4 TSSOP14 package information 17

6 Ordering information 18

7 Revision history 19

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1 Pin connection and schematic diagram

Figure 1. Pin connections (top view)

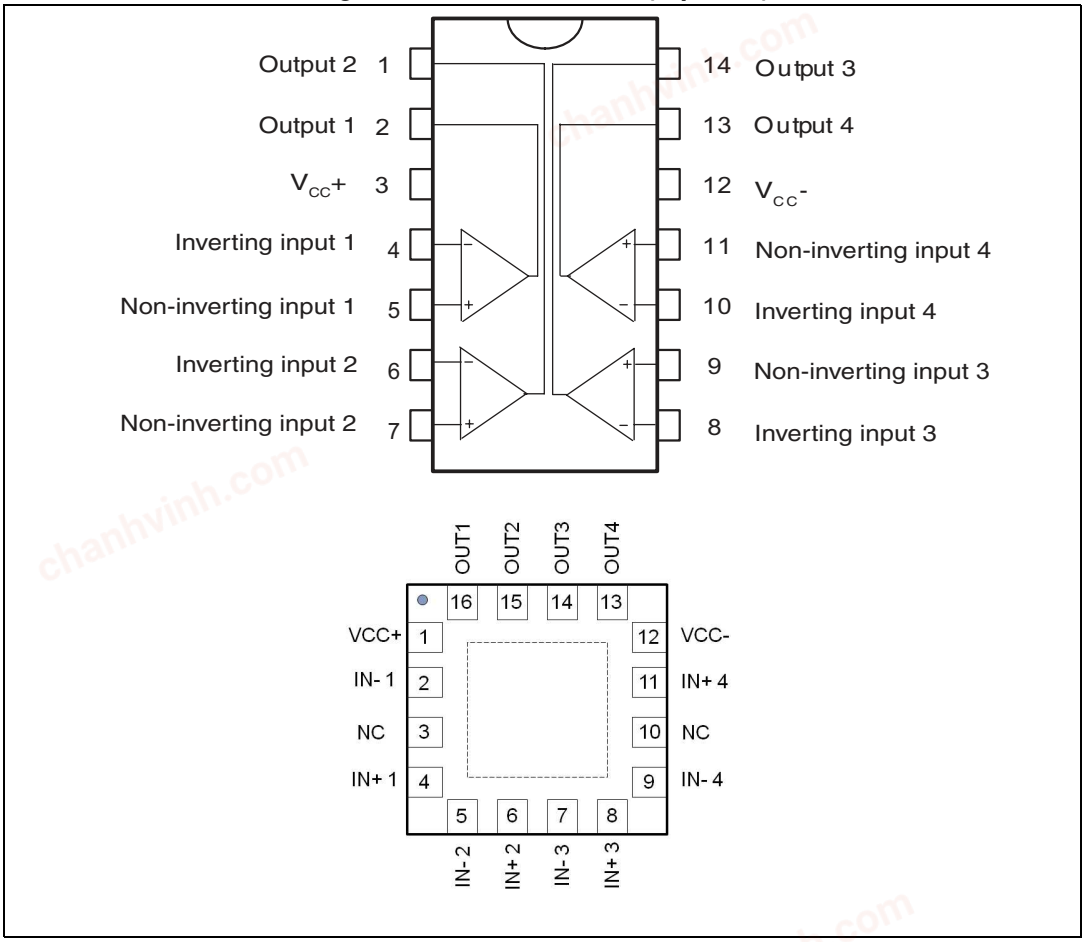
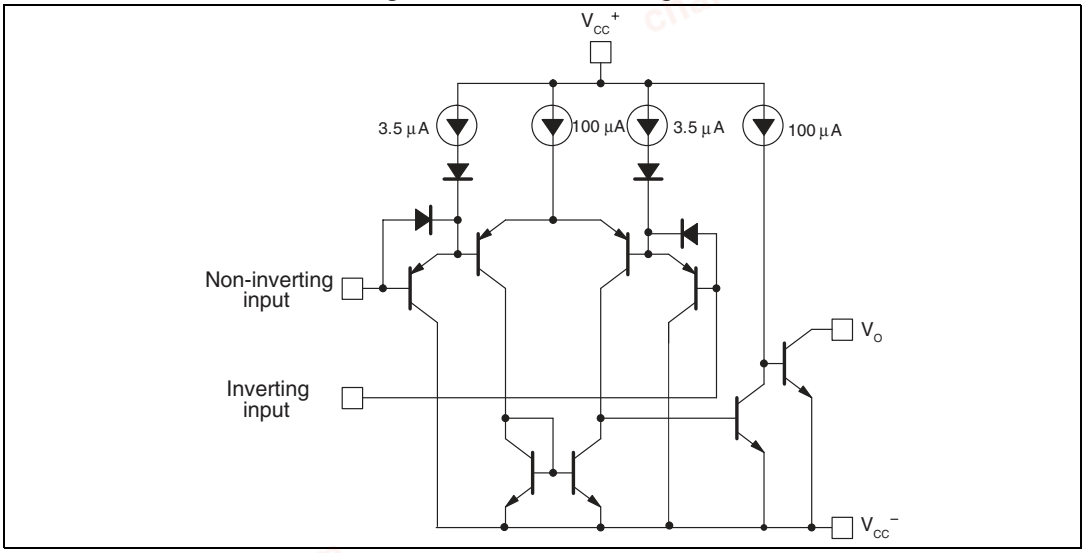


Figure 2. Schematic diagram



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2 Absolute maximum ratings and operating conditions

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{CC}	Supply voltage	±18 to 36	V
V_{id}	Differential input voltage	±36	V
V_{in}	Input voltage	-0.3 to +36	V
	Output short-circuit to ground (1)		
R_{thja}	Thermal resistance junction to ambient (2) DIP14 SO-14 TSSOP14 QFN16 3x3	 80 105 100 45	°C/W
R_{thjc}	Thermal resistance junction to case (2) DIP14 SO-14 TSSOP14 QFN16 3x3	 33 31 32 14	
T_j	Maximum junction temperature	+150	°C
T_{stg}	Storage temperature range	-65 to +150	°C
ESD	HBM: human body model (3)	500	V
	MM: machine model (4)	100	V
	CDM: charged device model (5)	1500	V

1. Short-circuits from the output to V_{CC}^+ can cause excessive heating and eventual destruction. The maximum output current is approximately 20 mA, independent of the magnitude of V_{CC}^+.
2. Short-circuits can cause excessive heating. Destructive dissipation can result from simultaneous short-circuits on all amplifiers. All values are typical.
3. Human body model: a 100 pF capacitor is charged to the specified voltage, then discharged through a 1.5 kΩ resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.
4. Machine model: a 200 pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5 Ω). This is done for all couples of connected pin combinations while the other pins are floating.
5. Charged device model: all pins and the package are charged together to the specified voltage and then discharged directly to the ground through only one pin. This is done for all pins.

Table 2. Operating conditions

Symbol	Parameter	Value	Unit
V_{CC}	Supply voltage	2 to 32 ±1 to ±16	V
V_{icm}	Common mode input voltage range $T_{min} \leq T_{amb} \leq T_{max}$	0 to (V_{CC}^+ -1.5) 0 to (V_{CC}^+ -2)	V
T_{oper}	Operating free-air temperature range	-40 to +125	°C

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3 Electrical characteristics

Table 3. Electrical characteristics at V_{CC}^+ = 5 V, V_{CC}^- = GND, T_{amb} = 25° C (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit
V_{io}	Input offset voltage (1) $T_{min} \le T_{amb} \le T_{max}$		1	7 15	mV
I_{io}	Input offset current $T_{min} \le T_{amb} \le T_{max}$		5	50 150	nA
I_{ib}	Input bias current (I_I^+ or I_I^-) (2) $T_{min} \le T_{amb} \le T_{max}$		25	250 400	nA
A_{vd}	Large signal voltage gain (V_{CC} = 15 V, R_L = 15 kΩ, V_o = 1 to 11 V)	25	200		V/mV
I_{CC}	Supply current (all comparators) V_{CC} = +5 V, no load V_{CC} = +30 V, no load		 1.1 1.3	 2 2.5	mA
V_{id}	Differential input voltage (3)			V_{CC}^+	V
V_{OL}	Low level output voltage V_{id} = -1 V, I_{sink} = 4 mA $T_{min} \le T_{amb} \le T_{max}$		250	400 700	mV
I_{OH}	High level output current (V_{CC} = V_o = 30 V, V_{id} = 1 V $T_{min} \le T_{amb} \le T_{max}$		0.1	 1	nA µA
I_{sink}	Output sink current (V_{id} = -1 V, V_o = 1.5 V)	6	16		mA
t_{res}	Small signal response time(4) (R_L = 5.1 kΩ connected to V_{CC}^+)		1.3		µs
t_{rel}	Large signal response time(5) TTL input (V_{ref} = +1.4 V, R_L = 5.1 kΩ to V_{CC}^+) Output signal at 50% of final value Output signal at 95% of final value			 500 1	 ns µs

1. At output switch point, $V_O \approx$ 1.4 V, R_S = 0 with V_{CC}^+ from 5 V to 30 V, and over the full input common-mode range (0 V to V_{CC}^+ –1.5 V).
2. The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output, so there is no loading charge on the reference of input lines.
3. The response time specified is for a 100 mV input step with 5 mV overdrive.
4. Positive excursions of input voltage may exceed the power supply level. As long as the other voltage remains within the common-mode range, the comparator will provide a proper output state. The low input voltage state must not be less than –0.3 V (or 0.3 V below the negative power supply, if used).
5. Maximum values are guaranteed by design.

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Figure 3. Supply current vs. supply voltage

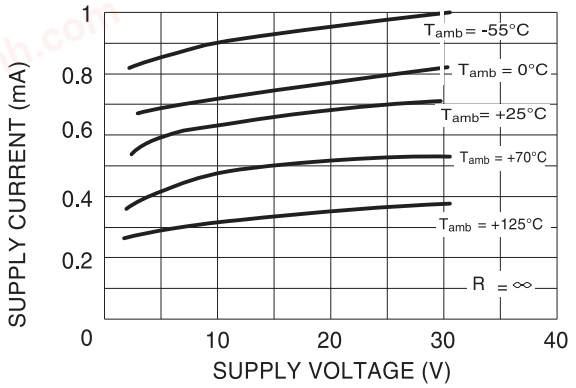


Figure 4. Input current vs. supply voltage

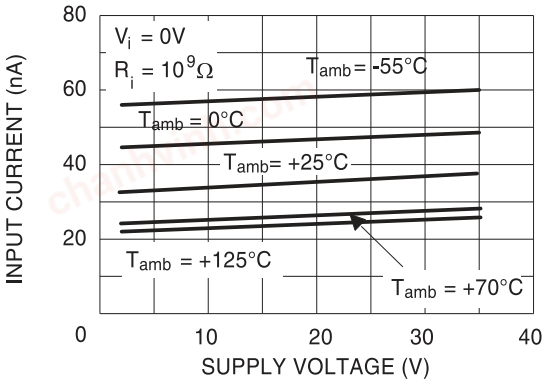


Figure 5. Output saturation voltage vs. output current

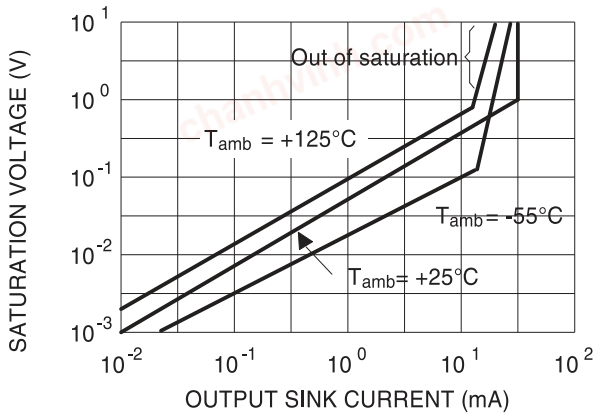


Figure 6. Response time for various input overdrives - negative transition

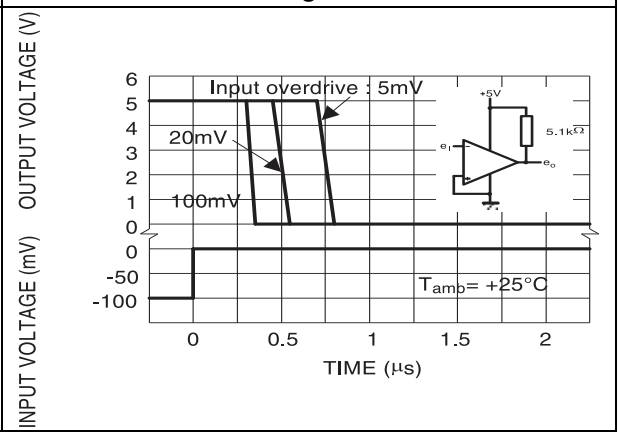
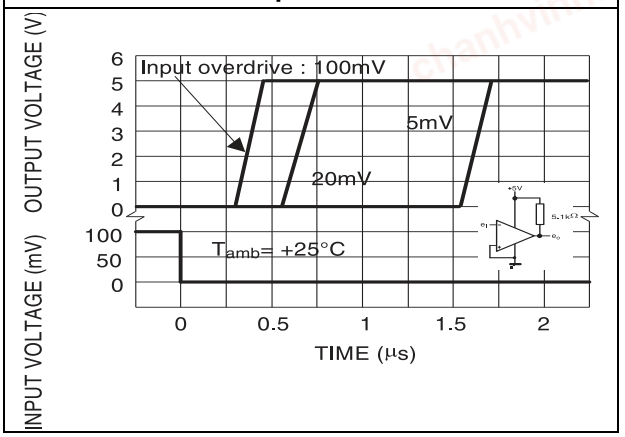


Figure 7. Response time for various input overdrives - positive transition



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4 Typical application schematics

Figure 8. Basic comparator

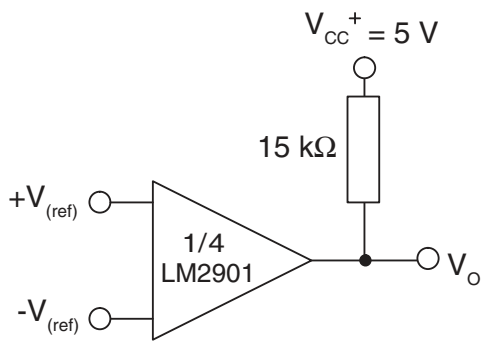


Figure 9. Driving CMOS

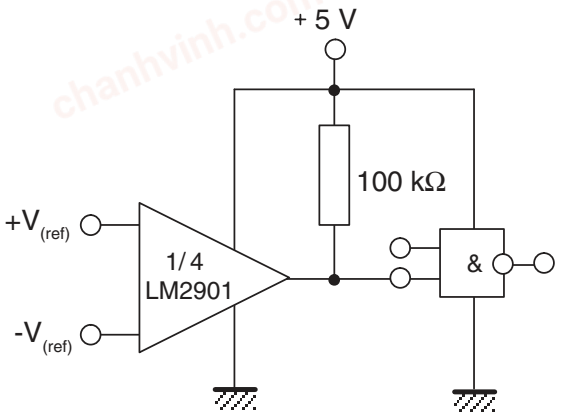


Figure 10. Driving TTL

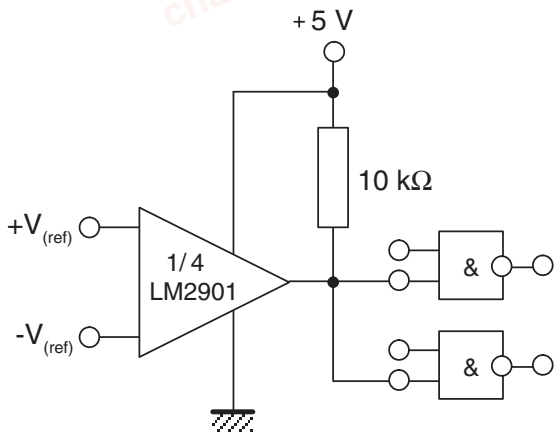


Figure 11. Low frequency op amp

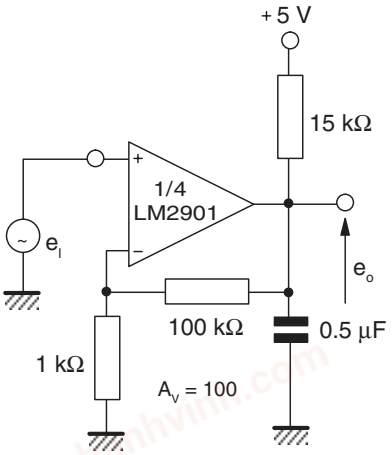


Figure 12. Low frequency op amp with boost

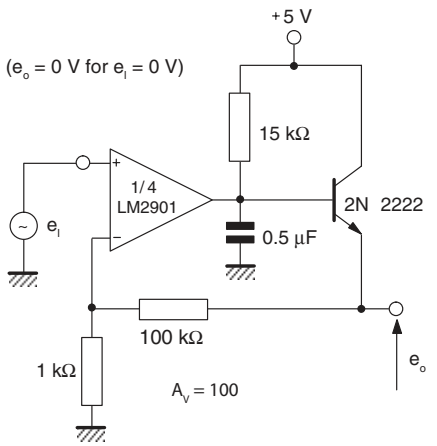
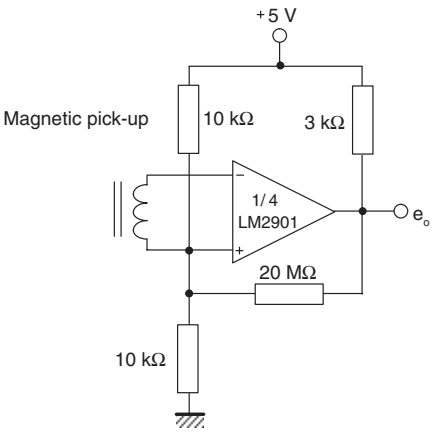


Figure 13. Transducer amplifier



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Figure 14. Low frequency op amp with offset adjust

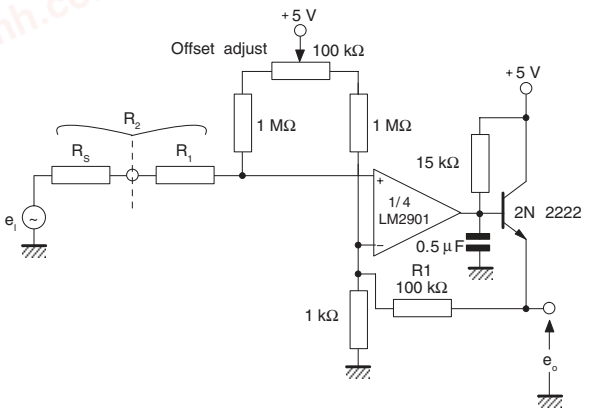


Figure 15. Zero crossing detector (single power supply)

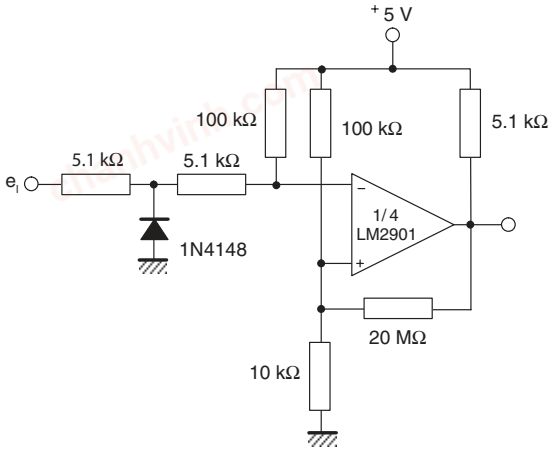


Figure 16. Limit comparator

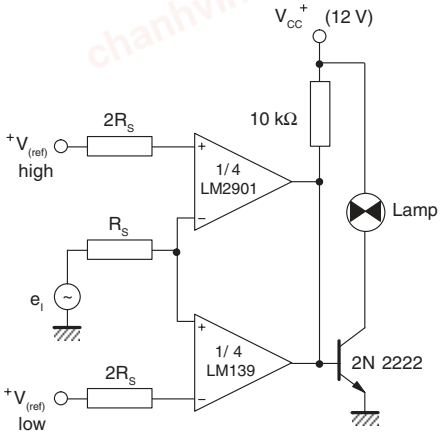


Figure 17. Split-supply applications - zero crossing detector

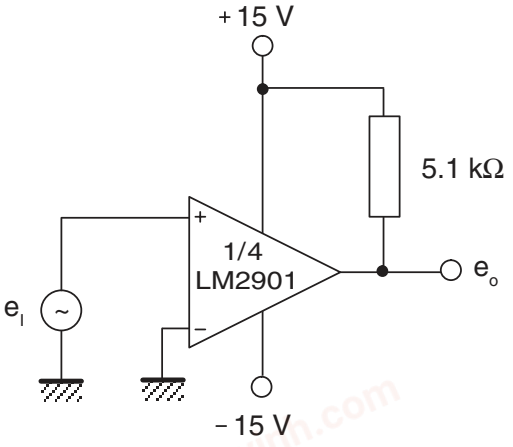


Figure 18. Crystal controlled oscillator

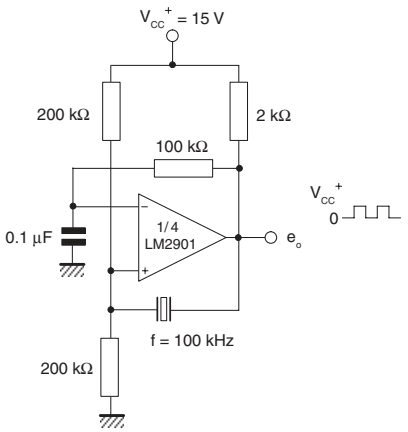
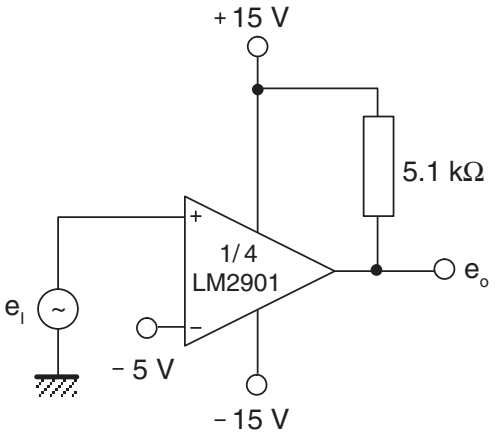


Figure 19. Comparator with a negative reference



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Figure 20. Time delay generator

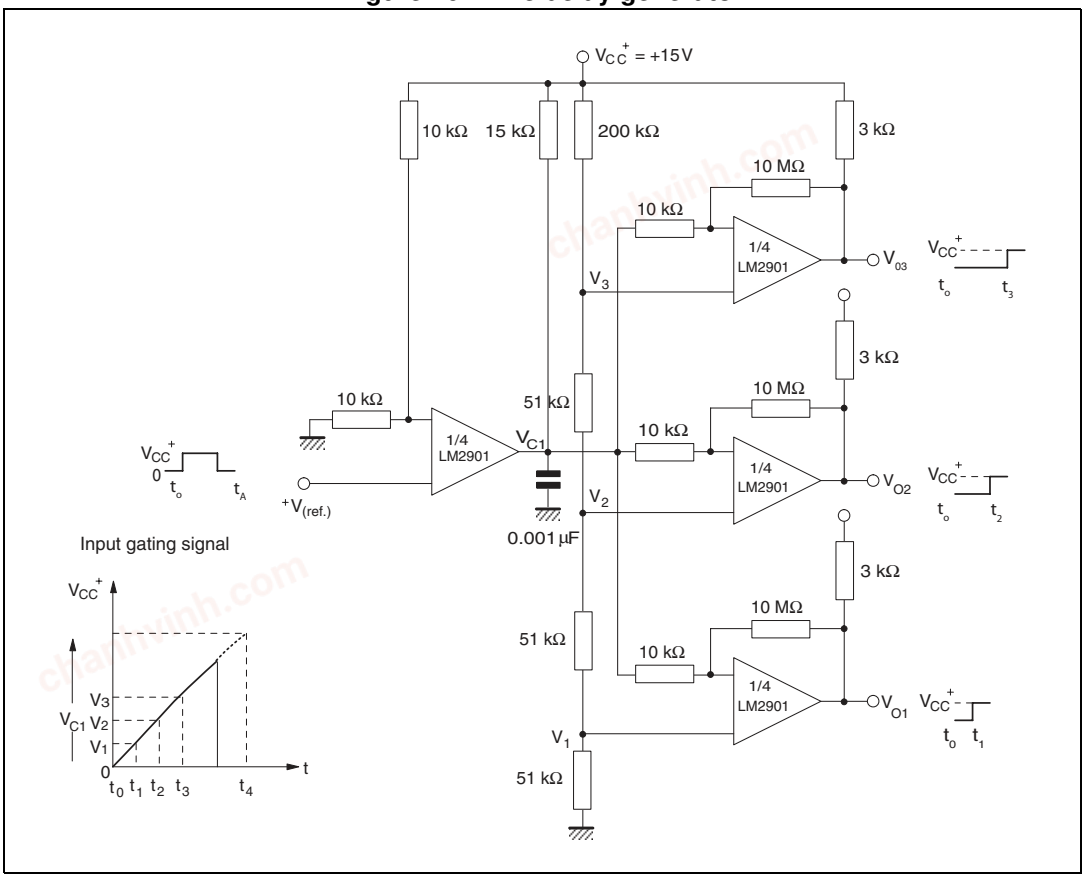
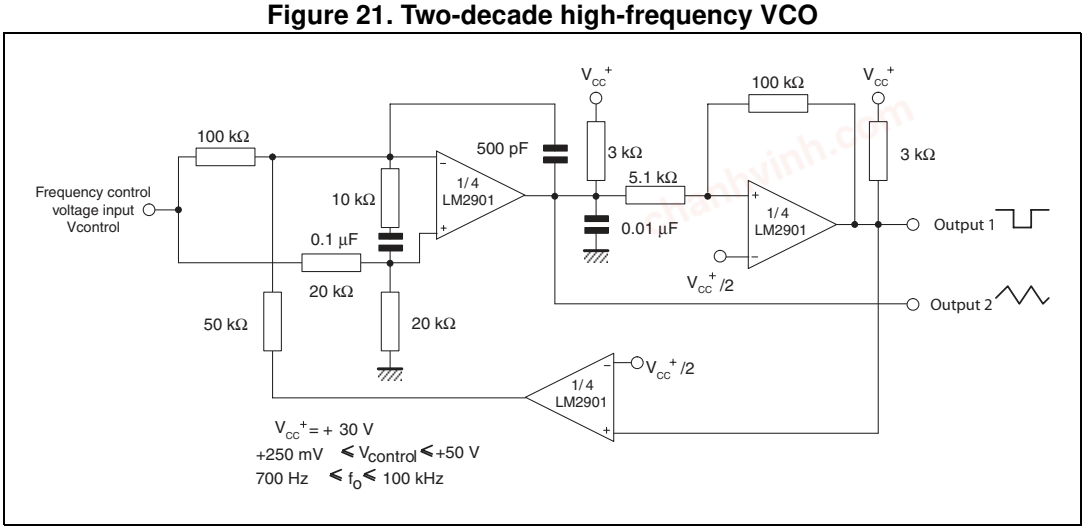


Figure 21. Two-decade high-frequency VCO



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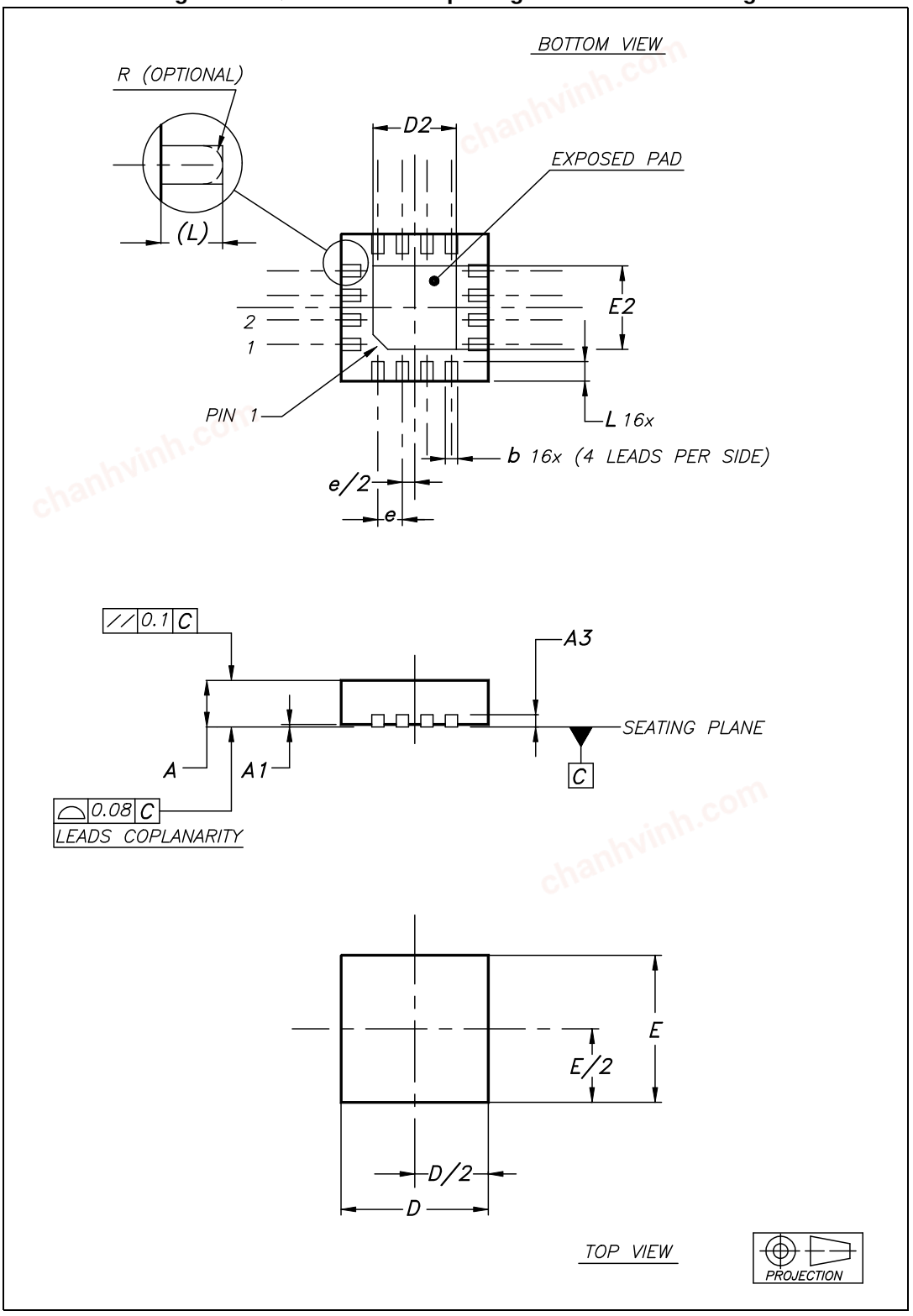
5 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK is an ST trademark.

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5.1 QFN16 3 x 3 package information

Figure 22. QFN16 3 x 3 mm package mechanical drawing



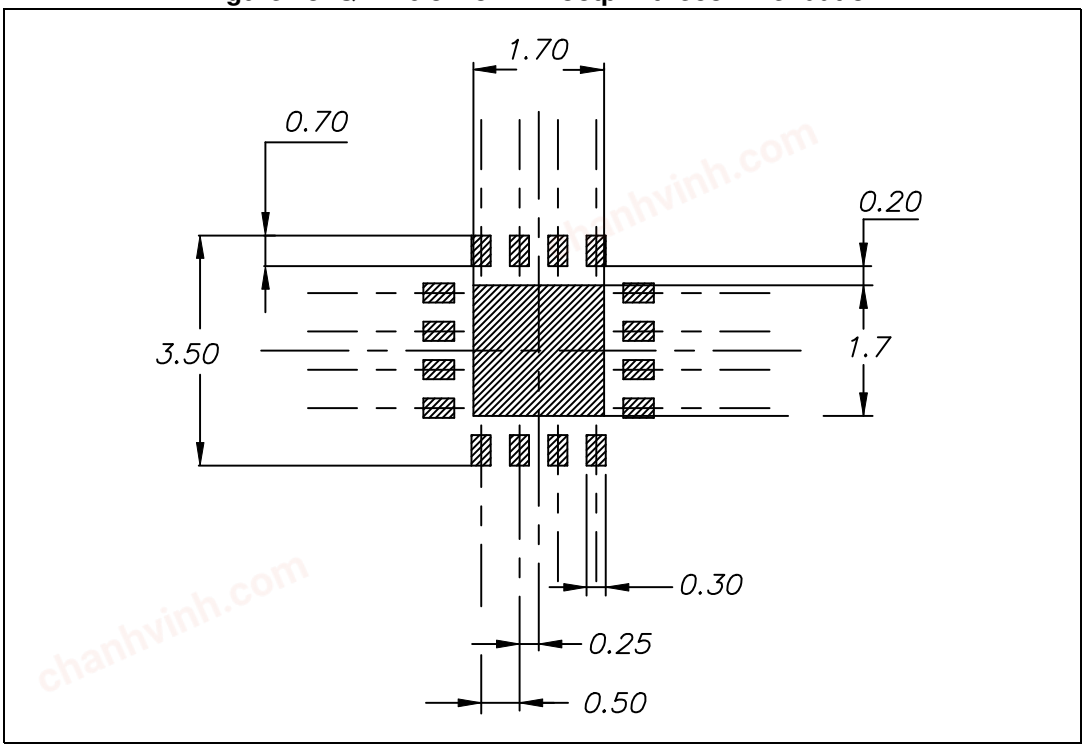
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Table 4. QFN16 3 x 3 mm package mechanical data (pitch 0.5 mm)

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.80	0.90	1.00	0.031	0.035	0.039
A1	0		0.05	0		0.002
A3		0.20			0.008	
b	0.18		0.30	0.007		0.012
D	2.90	3.00	3.10	0.114	0.118	0.122
D2	1.50		1.80	0.059		0.071
E	2.90	3.00	3.10	0.114	0.118	0.122
E2	1.50		1.80	0.059		0.071
e		0.50			0.020	
L	0.30		0.50	0.012		0.020

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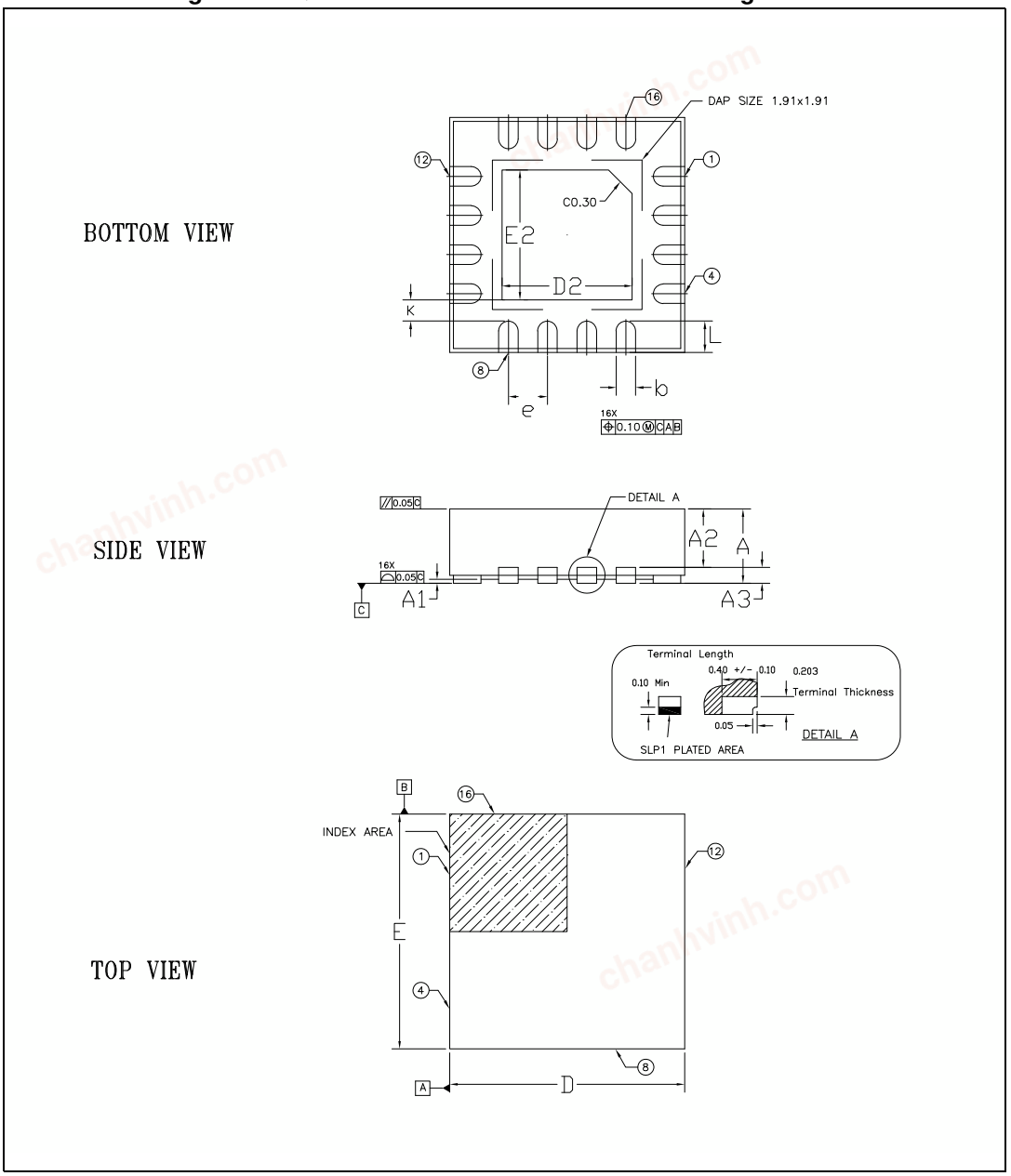
Figure 23. QFN16 3 x 3 mm footprint recommendation



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5.2 QFN16 3 x 3 wettable flank package information

Figure 24. QFN16 3 x 3 mm wettable flank drawing outline

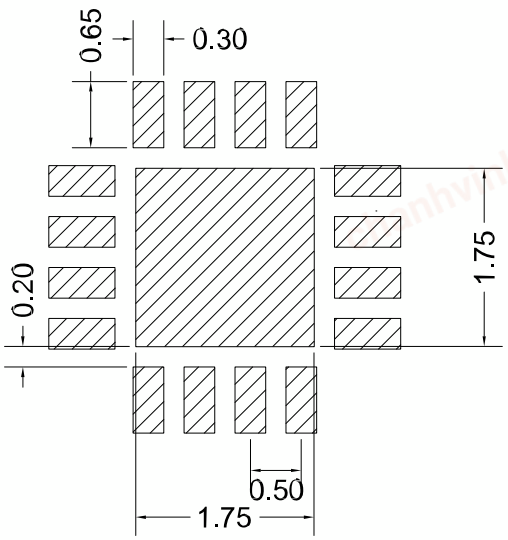


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Table 5. QFN16 3x3 wettable flank mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.90	0.95	1.00	0.035	0.037	0.039
A1	0		0.05	0		0.002
A2		0.75			0.030	
A3		0.20			0.008	
b	0.20	0.25	0.30	0.008	0.010	0.012
D		3.00			0.118	
E		3.00			0.118	
e		0.50			0.020	
D2	1.56	1.66	1.76	0.061	0.065	0.069
E2	1.56	1.66	1.76	0.061	0.065	0.069
K		0.27			0.011	
L	0.30	0.40	0.50	0.012	0.016	0.020

Figure 25. QFN16 3x3 wettable flank recommended footprint



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5.3 SO-14 package information

Figure 26. SO-14 package mechanical drawing

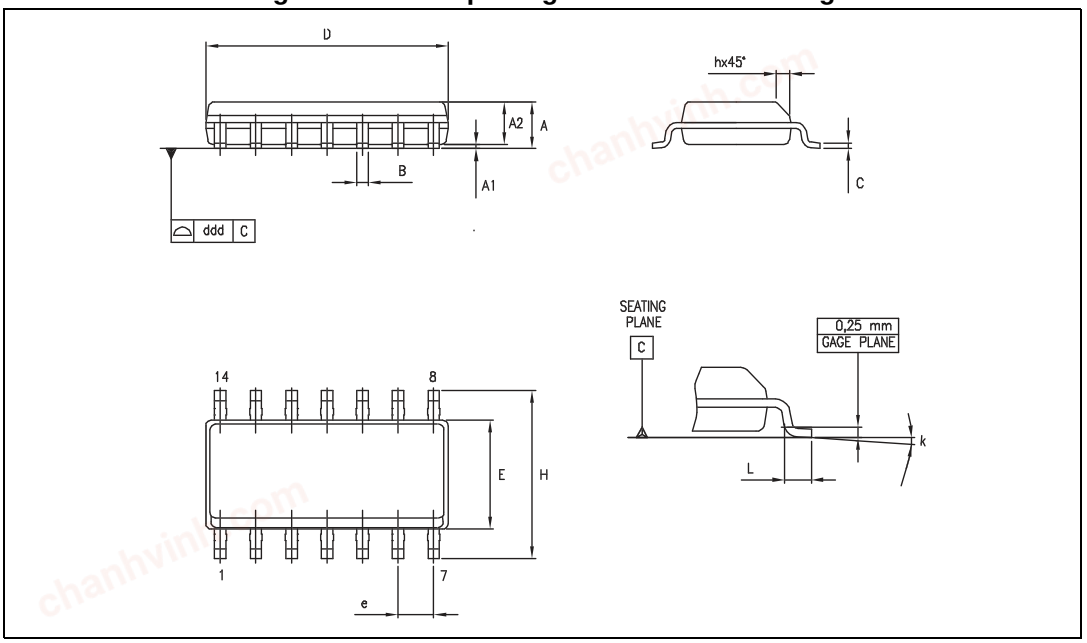


Table 6. SO-14 package mechanical data

Dimensions						
Ref.	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.35		1.75	0.05		0.068
A1	0.10		0.25	0.004		0.009
A2	1.10		1.65	0.04		0.06
B	0.33		0.51	0.01		0.02
C	0.19		0.25	0.007		0.009
D	8.55		8.75	0.33		0.34
E	3.80		4.0	0.15		0.15
e		1.27			0.05	
H	5.80		6.20	0.22		0.24
h	0.25		0.50	0.009		0.02
L	0.40		1.27	0.015		0.05
k	8° (max.)					
ddd			0.10			0.004

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5.4 TSSOP14 package information

Figure 27. TSSOP14 package mechanical drawing

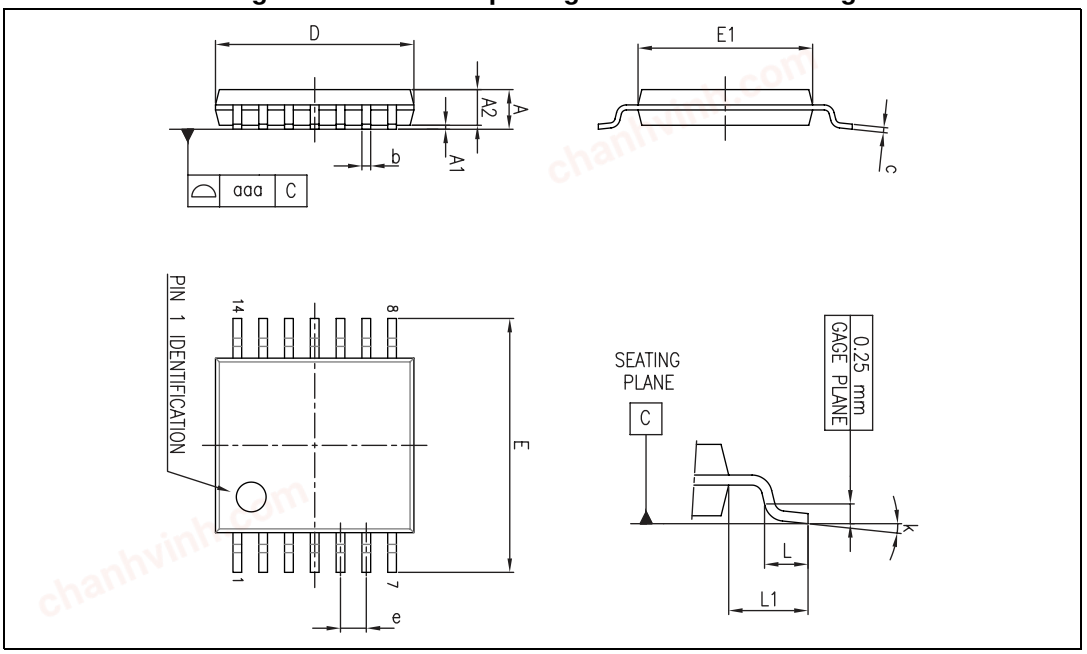


Table 7. TSSOP14 package mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.20			0.047
A1	0.05		0.15	0.002	0.004	0.006
A2	0.80	1.00	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
c	0.09		0.20	0.004		0.0089
D	4.90	5.00	5.10	0.193	0.197	0.201
E	6.20	6.40	6.60	0.244	0.252	0.260
E1	4.30	4.40	4.50	0.169	0.173	0.176
e		0.65			0.0256	
L	0.45	0.60	0.75	0.018	0.024	0.030
L1		1.00			0.039	
k	0°		8°	0°		8°
aaa			0.10			0.004

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6 Ordering information

Table 8. Order codes

Order code	Temperature range	Package	Packing	Marking
LM2901D LM2901DT	-40 °C to +125 °C	SO-14	Tube or tape & reel	2901
LM2901PT		TSSOP14	Tape & reel	
LM2901Q4T		QFN16 3 x 3		K5I
LM2901YQ5T (1)		QFN16 3 x 3 wettable flank (Automotive grade)		K552
LM2901YDT (1)		SO-14 (Automotive grade)		2901Y
LM2901YPT (1)		TSSOP14 (Automotive grade)		

1. Qualified and characterized according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 & Q 002 or equivalent.

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7 Revision history

Table 9. Document revision history

Date	Revision	Changes
01-Jan-2002	1	Initial release.
01-Jul-2005	2	1 - PPAP references inserted in the datasheet see Table: Order codes on page 1. 2 - ESD protection inserted in Table 1 on page 3.
01-Oct-2005	3	– The following changes were made in this revision: – – PPAP part number added in table Order codes on page 1. – – Formatting changes throughout.
18-Jul-2006	4	ESD HBM value corrected in Table 1 on page 3.
19-Dec-2007	5	Added R_{thja} and R_{thjc} parameters to Table 1: Absolute maximum ratings. Added footnotes for ESD parameters. Removed V_{icm} parameter from electrical characteristics in Table 3. Reformatted package information in Section 5. Added footnotes for automotive grade parts in Table 8: Order codes.
30-Apr-2009	6	Document reformatted. Updated package information in Chapter 5: Package information. Removed note 2 under Table 8: Order codes.
06-Feb-2012	7	Added QFN16 3 x 3 package in Chapter 5. Removed LM2901YD order code from Table 8.
12-Dec-2022	8	Updated figure on the cover page. Removed DIP14 package. Added new LM2901Q5T order code in Table 8 and new Section 5.2.
22-Jun-2023	9	Updated I_{sink} unit value in Table 3.
20-Nov-2023	10	Updated *Table 8: Order codes*.

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