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SK80MB120CR03TE1



SEMITOP®E1

Half-Bridge (Full SiC)

SK80MB120CR03TE1

Features*

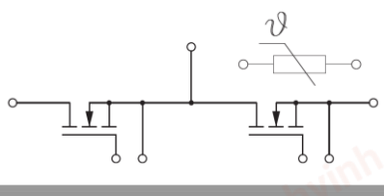
- Optimized design for superior thermal performance
- Extremely low inductance design
- Press-Fit contact technology
- 1200V Planar Gen3 SiC MOSFET
- Simple to drive with +15V gate voltage
- Optimized switching stability thanks to module integrated gate resistors
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

Typical Applications

- Switched Mode Power Supplies
- Energy Storage Systems
- Electric Vehicle charging
- UPS
- Solar
- Motor Driver

Remarks

- Recommended T_{jop} = -40°C...+150°C
- Recommended turn-off / turn-on gate voltage V_{GS} = -4...0/+15V



MB-T

Absolute Maximum Ratings

Symbol	Conditions		Values	Unit
MOSFET				
V_{DSS}	T_j = 25 °C		1200	V
I_D	HPTP / HP-PCM Tj = 175 °C	T_s = 25 °C	100	A
		T_s = 70 °C	83	A
I_{DM}	Pulse witdh t_p limited by T_{vjmax}		240	A
V_{GS}	Transient Gate - Source voltage (t<100ns)		-8 ... 19	V
T_j			-40 ... 175	°C
Integrated body diode				
I_{FM}	Pulse width t_p limited by T_{vjmax}		240	A
I_{FSM}	t_p = 10 ms, sin 180°, T_j = 150 °C		403	A

Absolute Maximum Ratings

Symbol	Conditions	Values	Unit
Module			
$I_{t(RMS)}$	$\Delta T_{terminal}$ at PCB joint = 30 K, per pin	30	A
T_{stg}	module without TIM	-40 ... 125	°C
V_{isol}	AC, sinusoidal, t = 1 min	2500	V

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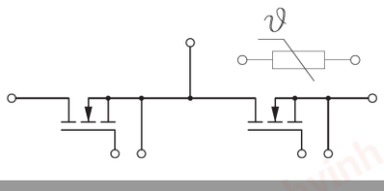

MB-T

Characteristics

Symbol	Conditions		min.	typ.	max.	Unit
MOSFET						
$V_{(BR)DSS}$	V_{GS} = 0 V, I_D = 0.2 mA, T_j = 25 °C		1200			V
$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 23 mA, T_j = 25 °C		1.8	2.5	3.6	V
I_{DSS}	V_{GS} = 0 V, V_{DS} = 1200 V, T_j = 25 °C				0.2	mA
I_{GSS}	V_{DS} = 0 V, V_{GS} = 15 V, T_j = 25 °C				400	nA
$R_{DS(on)}$	V_{GS} = 15 V, I_D = 83 A, chiplevel	T_j = 25 °C		16	22	mΩ
		T_j = 150 °C		25		mΩ
C_{iss}	V_{GS} = 0 V, V_{DS} = 1000 V, T_j = 25 °C	f = 0.1 MHz		6800		pF
C_{oss}		f = 0.1 MHz		260		pF
C_{rss}		f = 0.1 MHz		20		pF
Q_G	V_{GS} = -4…15V, V_{DD} = 800V, I_D = 83 A			236		nC
R_{Gint}	T_j = 25 °C			5.9		Ω
$t_{d(on)}$	V_{DD} = 600 V, I_D = 80 A, V_{GS} = -4/+15 V, $R_{G\ on/off}$ = 1 Ω, di/dt_{off} = 5.6 kA/µs, di/dt_{on} = 7.1 kA/µs, dv/dt = 28 kV/µs, L_s = 17 nH	T_j = 150 °C		36		ns
$t_{d(off)}$		T_j = 150 °C		90		ns
t_r		T_j = 150 °C		16		ns
t_f		T_j = 150 °C		23		ns
E_{on}		T_j = 150 °C		1.02		mJ
E_{off}		T_j = 150 °C		0.71		mJ
$R_{th(j-s)}$	per MOSFET, HPTP / HP-PCM			0.49		K/W
Integrated body diode						
$V_F = V_{SD}$	$-I_D$ = 80 A, V_{GS} = -4 V, chiplevel	T_j = 25 °C		4.6		V
		T_j = 150 °C		4.3		V
$V_{F0} = V_{SD0}$	chiplevel	T_j = 25 °C		3.8		V
		T_j = 150 °C		3.6		V
$r_F = r_{SD}$	chiplevel	T_j = 25 °C		19		mΩ
		T_j = 150 °C		17		mΩ
t_{rr}	V_{DD} = 600 V, $-I_D$ = 80 A, V_{GS} = -4 V, R_{Gon} = 1 Ω, di/dt_{off} = 7 kA/µs	T_j = 150 °C		33		µs
Q_{rr}		T_j = 150 °C		1.4		µC
I_{rr}		T_j = 150 °C		86		A
E_{rr}		T_j = 150 °C		0.74		mJ

Characteristics

Symbol	Conditions	min.	typ.	max.	Unit
Module					
L_{CE}			9		nH
M_s	to heatsink	1.6		2.3	Nm
w	weight		35		g

Characteristics

Symbol	Conditions	min.	typ.	max.	Unit
Temperature Sensor					
R_{100}	T_r = 100 °C		493 ± 5%		Ω
$B_{100/125}$	$R_{(T)}=R_{100}*\exp[B_{100/125}*(1/T-1/T_{100})]$, T[K];		3550 (± 2%)		K

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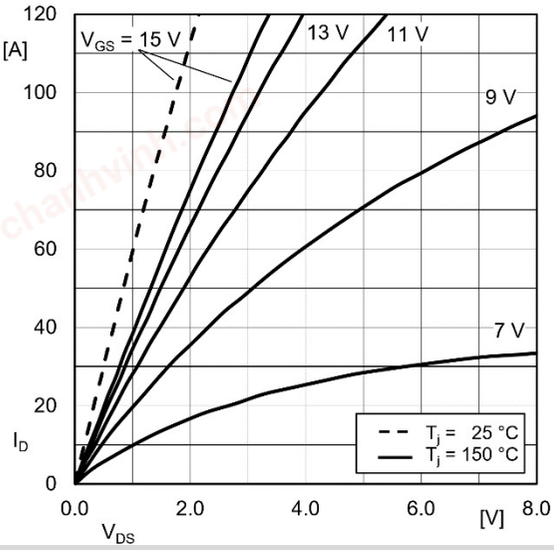

Fig.1: Typ. MOSFET forward output characteristic, incl. $R_{DD'+ SS'}$

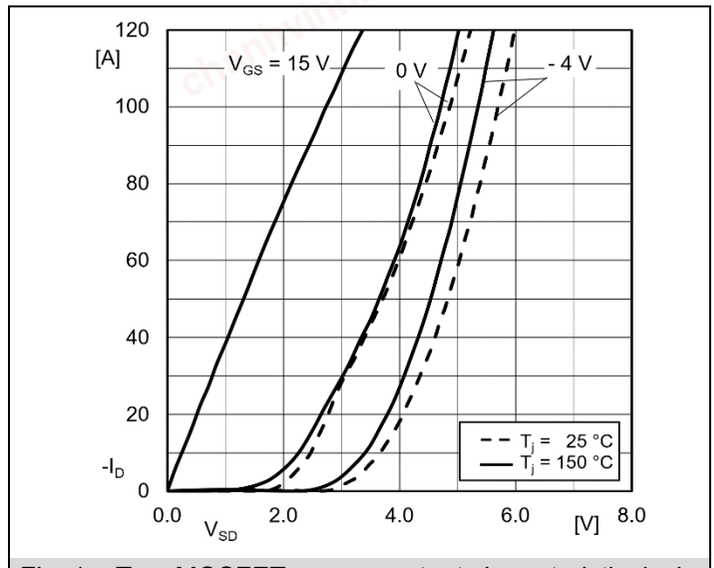

Fig. 1a: Typ. MOSFET reverse output characteristic, incl. $R_{DD'+ SS'}$

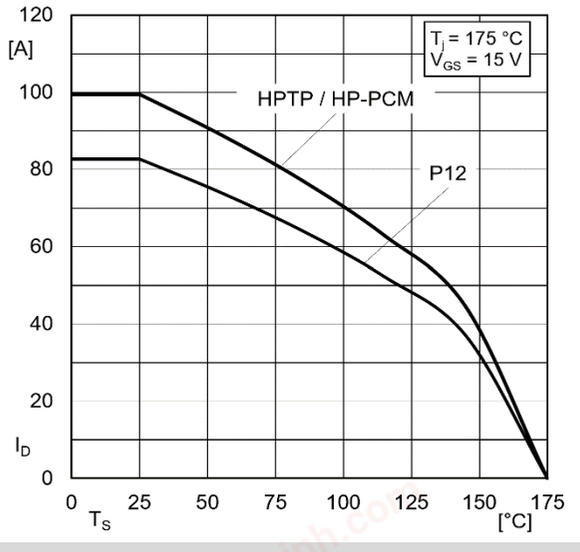

Fig. 2: MOSFET Rated current vs. temperature $I_D = f(T_S)$

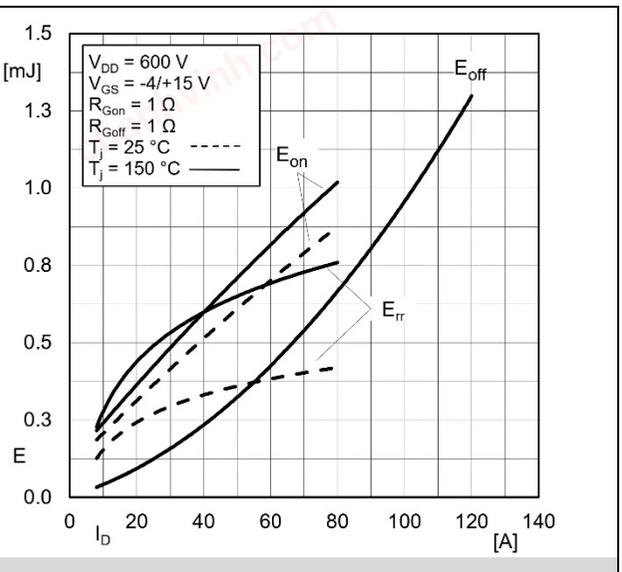

Fig. 3: Typ. MOSFET switching energy $E = f(I_D)$ at R_{G1}

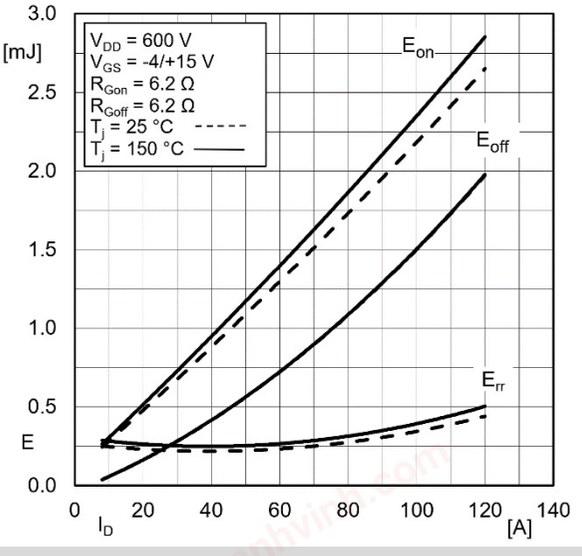

Fig. 3a: Typ. MOSFET switching energy $E = f(I_D)$ at R_{G2}

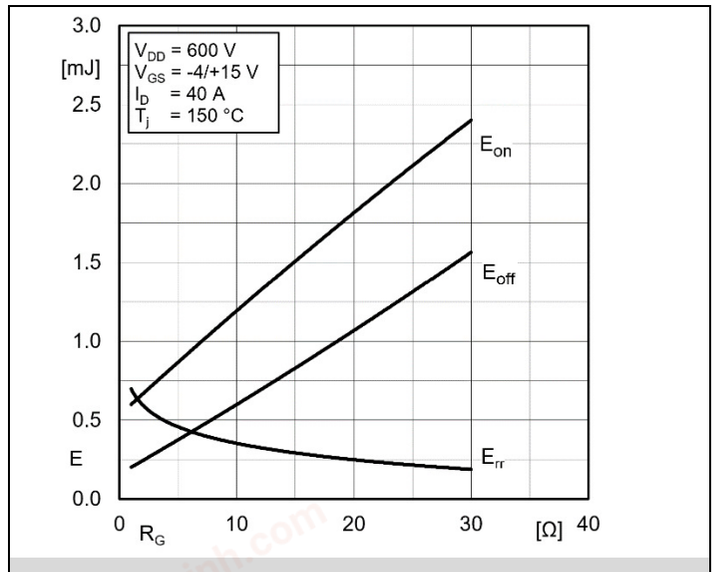

Fig. 4: Typ. MOSFET switching energy $E = f(R_G)$ at I_{D1}

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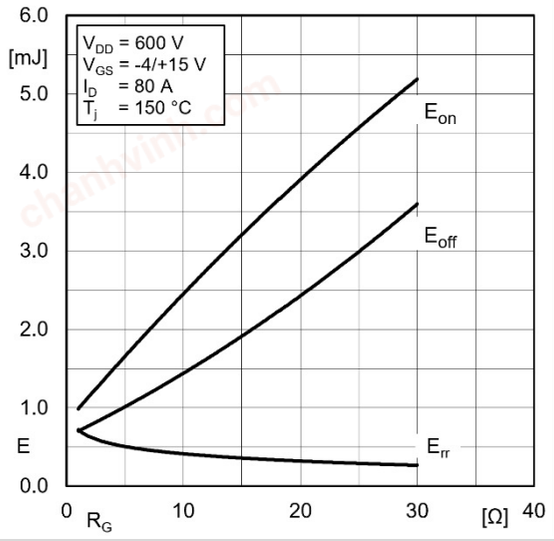

Fig. 4a: Typ. MOSFET switching energy E = f (R_G) at I_{D2}

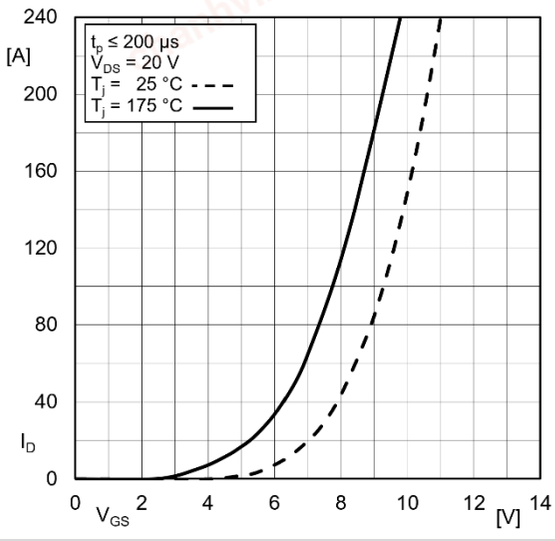

Fig. 5: Typ. MOSFET transfer characteristic

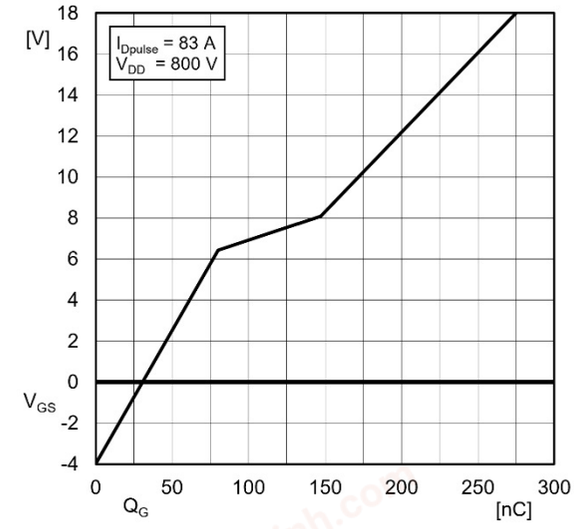

Fig. 6: Typ. MOSFET gate charge characteristic

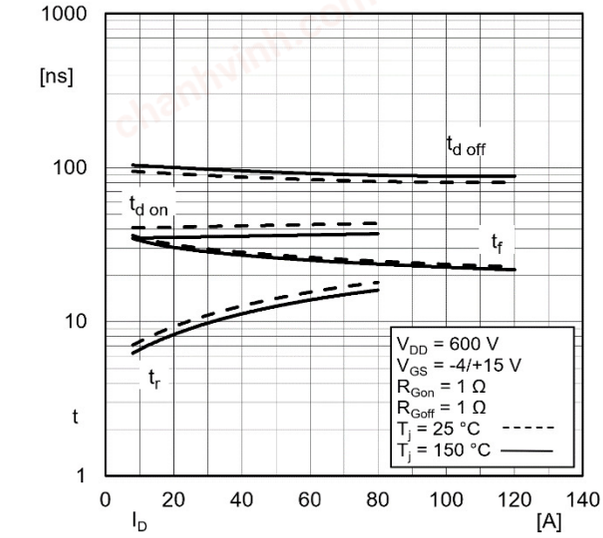

Fig. 7: Typ. MOSFET switching times t = f (I_D) at R_{G1}

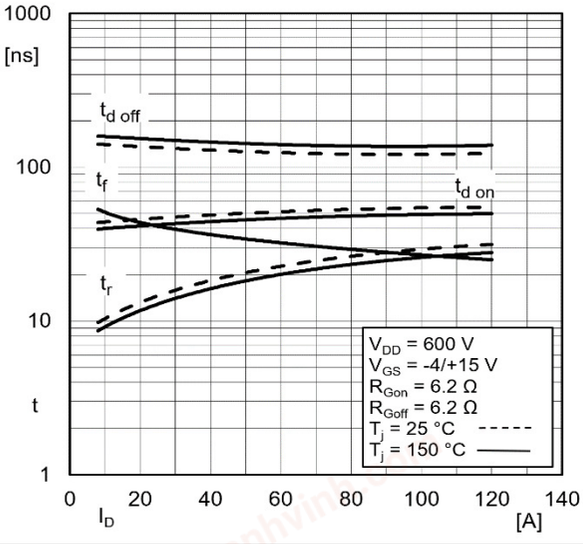

Fig. 7a: Typ. MOSFET switching times t = f (I_D) at R_{G2}

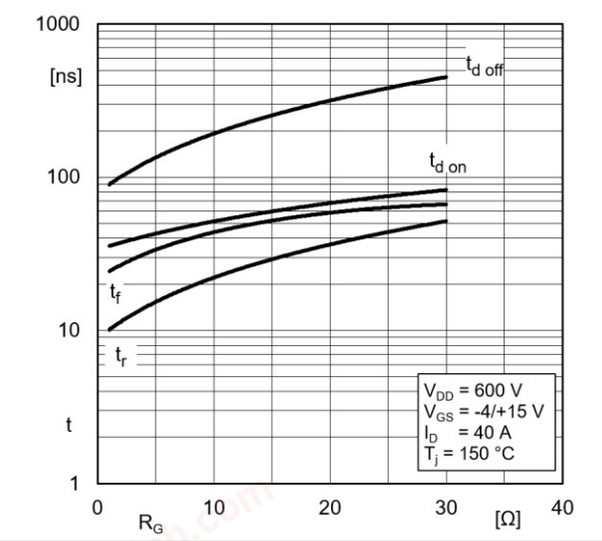

Fig. 8: Typ. MOSFET switching times t = f (R_G) at I_{D1}

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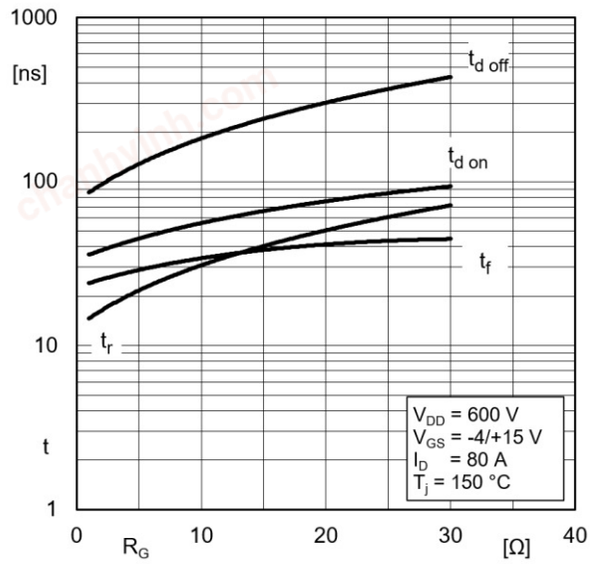

Fig. 8a: Typ. MOSFET switching times t = f (R_G) at I_{D2}

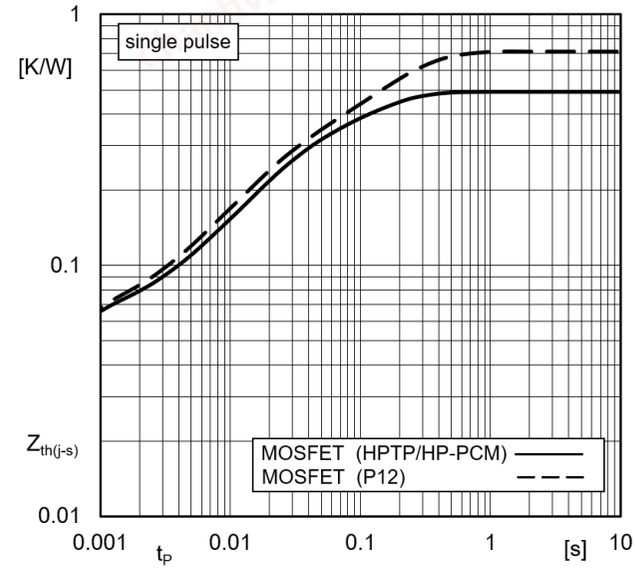

Fig. 9: Typ. transient thermal impedance

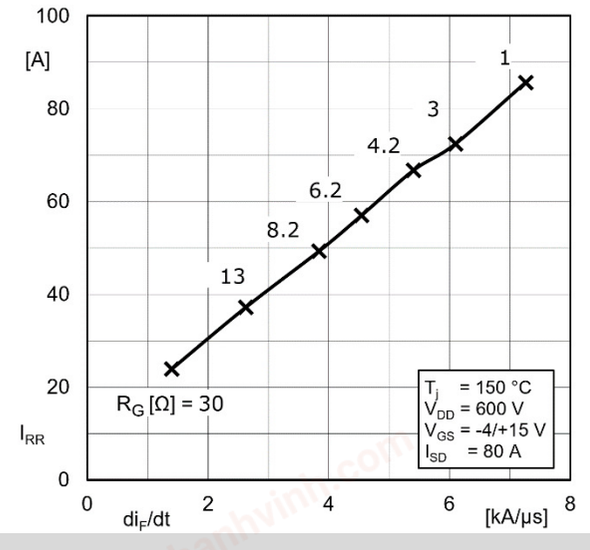

Fig. 10: Typ. body diode peak reverse recovery current I_{RR} = f (di_F/dt)

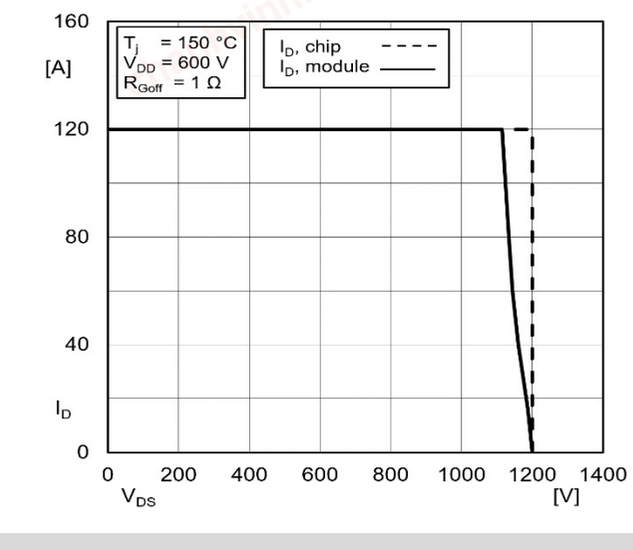

Fig. 11: MOSFET Reverse Bias Safe Operating Area (RBSOA)

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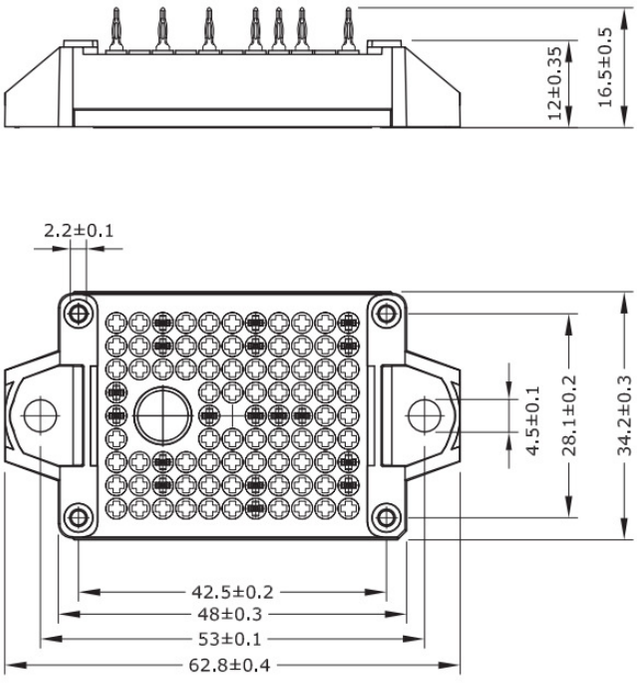
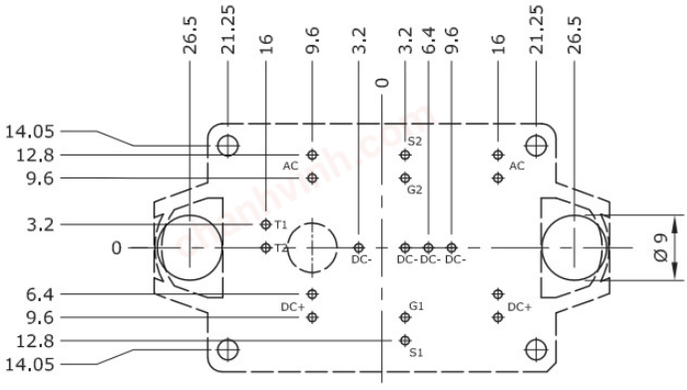
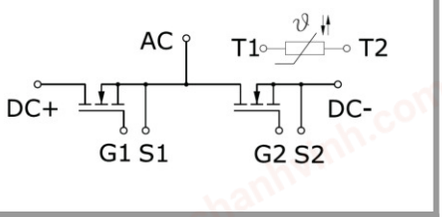




- Pin-Grid 3.2 mm
- Tolerance of PCB hole pattern ⌖ Ø0.1
- Diameters of drill ∅ 1.15mm
- Copper thickness in hole 25 - 50 µm
- Hole specification for contacts:
  refer to SEMITOP E1/E2 Mounting Instruction

Pinout and Dimensions



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This is an electrostatic discharge sensitive device (ESDS) according to international standard IEC 61340.

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