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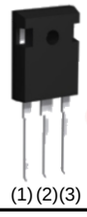
RGW50TS65DGC11

650V 25A Field Stop Trench IGBT Datasheet

V_{CES}	650V
$I_{C\ (100°C)}$	25A
$V_{CE(sat)\ (Typ.)}$	1.5V
P_D	156W

●Outline

TO-247N

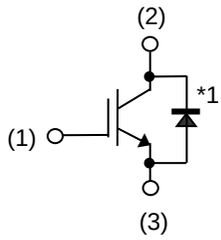


(1) (2) (3)

●Features

1) Low Collector - Emitter Saturation Voltage
2) High Speed Switching
3) Low Switching Loss & Soft Switching
4) Built in Very Fast & Soft Recovery FRD
5) Pb - free Lead Plating ; RoHS Compliant

●Inner Circuit



(1) Gate
(2) Collector
(3) Emitter

*1 Built in FRD

●Application

PFC
UPS
Welding
Solar Inverter
IH

●Packaging Specifications

Type	Packaging	Tube
	Reel Size (mm)	-
	Tape Width (mm)	-
	Basic Ordering Unit (pcs)	450
	Packing Code	C11
	Marking	RGW50TS65D

●Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V_{CES}	650	V
Gate - Emitter Voltage		V_{GES} *1	±30	V
Collector Current	T_C = 25°C	I_C	50	A
	T_C = 100°C	I_C	25	A
Pulsed Collector Current		I_{CP} *2	100	A
Diode Forward Current	T_C = 25°C	I_F	40	A
	T_C = 100°C	I_F	20	A
Diode Pulsed Forward Current		I_{FP} *2	100	A
Power Dissipation	T_C = 25°C	P_D	156	W
	T_C = 100°C	P_D	78	W
Operating Junction Temperature		T_j	-40 to +175	°C
Storage Temperature		T_{stg}	-55 to +175	°C

*1 Please be advised not to use IGBTs with V_{GE} below 10V as doing so may cause thermal runaway.

*2 Pulse width limited by T_{jmax}.

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●Thermal Resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.96	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	1.62	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Collector - Emitter Breakdown Voltage	BV_{CES}	I_C = 10μA, V_{GE} = 0V	650	-	-	V
Collector Cut - off Current	I_{CES}	V_{CE} = 650V, V_{GE} = 0V	-	-	10	μA
Gate - Emitter Leakage Current	I_{GES}	V_{GE} = ±30V, V_{CE} = 0V	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	V_{CE} = 5V, I_C = 16.4mA	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	$V_{CE(sat)}$	I_C = 25A, V_{GE} = 15V,				V
		T_j = 25°C	-	1.5	1.9	
		T_j = 175°C	-	1.85	-	

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●**IGBT Electrical Characteristics** (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	V_{CE} = 30V,	-	2080	-	pF
Output Capacitance	C_{oes}	V_{GE} = 0V,	-	56	-	
Reverse transfer Capacitance	C_{res}	f = 1MHz	-	38	-	
Total Gate Charge	Q_g	V_{CE} = 400V,	-	73	-	nC
Gate - Emitter Charge	Q_{ge}	I_C = 25A,	-	15	-	
Gate - Collector Charge	Q_{gc}	V_{GE} = 15V	-	28	-	
Turn - on Delay Time	$t_{d(on)}$	I_C = 25A, V_{CC} = 400V, V_{GE} = 15V, R_G = 10Ω, T_j = 25°C Inductive Load *E_{on} include diode reverse recovery	-	35	-	ns
Rise Time	t_r		-	11	-	
Turn - off Delay Time	$t_{d(off)}$		-	102	-	
Fall Time	t_f		-	53	-	
Turn - on Switching Loss	E_{on}		-	0.39	-	mJ
Turn - off Switching Loss	E_{off}		-	0.43	-	
Turn - on Delay Time	$t_{d(on)}$	I_C = 25A, V_{CC} = 400V, V_{GE} = 15V, R_G = 10Ω, T_j = 175°C Inductive Load *E_{on} include diode reverse recovery	-	34	-	ns
Rise Time	t_r		-	12	-	
Turn - off Delay Time	$t_{d(off)}$		-	118	-	
Fall Time	t_f		-	78	-	
Turn - on Switching Loss	E_{on}		-	0.41	-	mJ
Turn - off Switching Loss	E_{off}		-	0.60	-	
Reverse Bias Safe Operating Area	RBSOA	I_C = 100A, V_{CC} = 520V, V_P = 650V, V_{GE} = 15V, R_G = 100Ω, T_j = 175℃	FULL SQUARE			-

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●**FRD Electrical Characteristics** (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Diode Forward Voltage	V_F	I_F = 20A, T_j = 25°C T_j = 175°C	- -	1.45 1.55	1.9 -	V
Diode Reverse Recovery Time	t_{rr}	I_F = 20A, V_{CC} = 400V, di_F/dt = 200A/μs, T_j = 25°C	-	92	-	ns
Diode Peak Reverse Recovery Current	I_{rr}		-	6.7	-	A
Diode Reverse Recovery Charge	Q_{rr}		-	0.34	-	μC
Diode Reverse Recovery Energy	E_{rr}		-	14.1	-	μJ
Diode Reverse Recovery Time	t_{rr}	I_F = 20A, V_{CC} = 400V, di_F/dt = 200A/μs, T_j = 175°C	-	123	-	ns
Diode Peak Reverse Recovery Current	I_{rr}		-	7.8	-	A
Diode Reverse Recovery Charge	Q_{rr}		-	0.59	-	μC
Diode Reverse Recovery Energy	E_{rr}		-	30.7	-	μJ

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●Electrical Characteristic Curves

Fig.1 Power Dissipation vs. Case Temperature

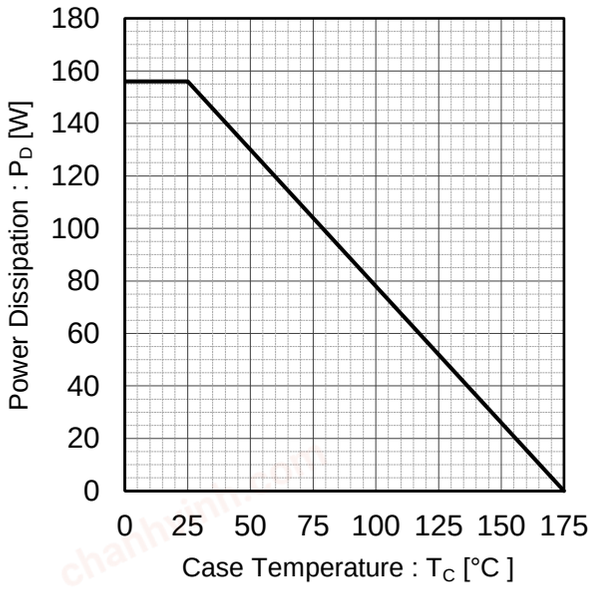


Fig.2 Collector Current vs. Case Temperature

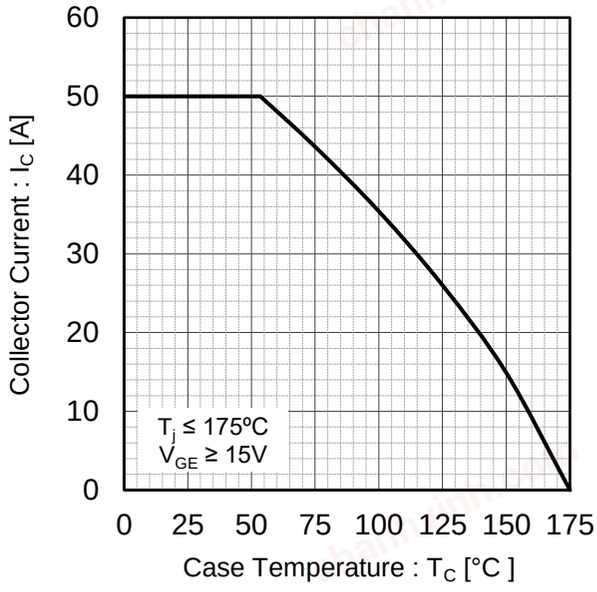


Fig.3 Forward Bias Safe Operating Area

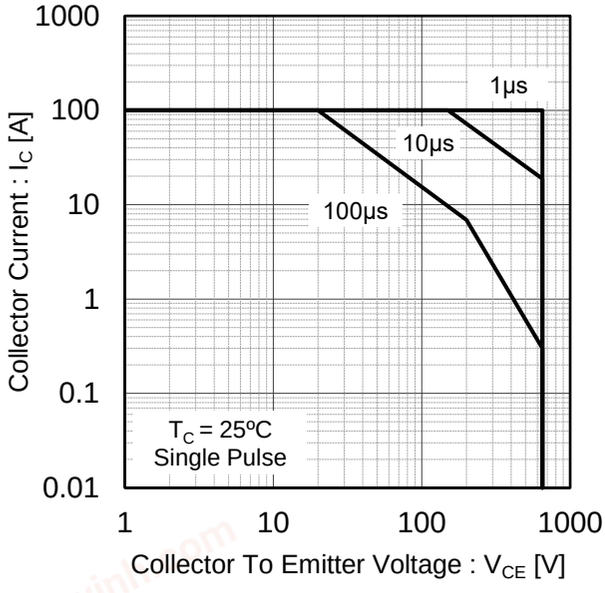
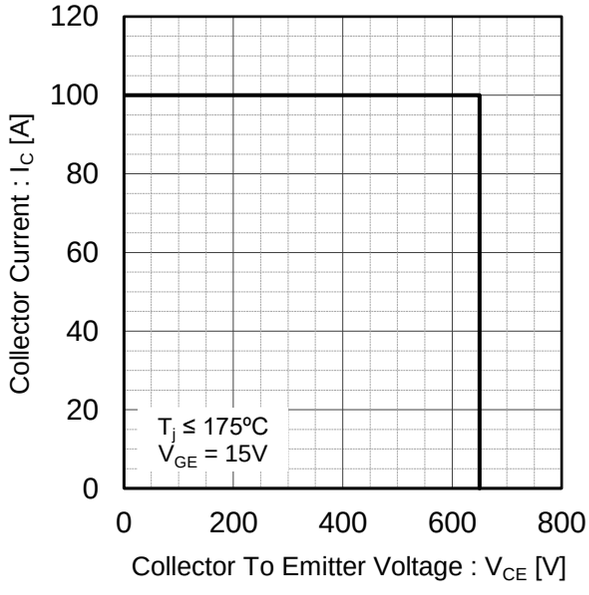


Fig.4 Reverse Bias Safe Operating Area



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●Electrical Characteristic Curves

Fig.5 Typical Output Characteristics

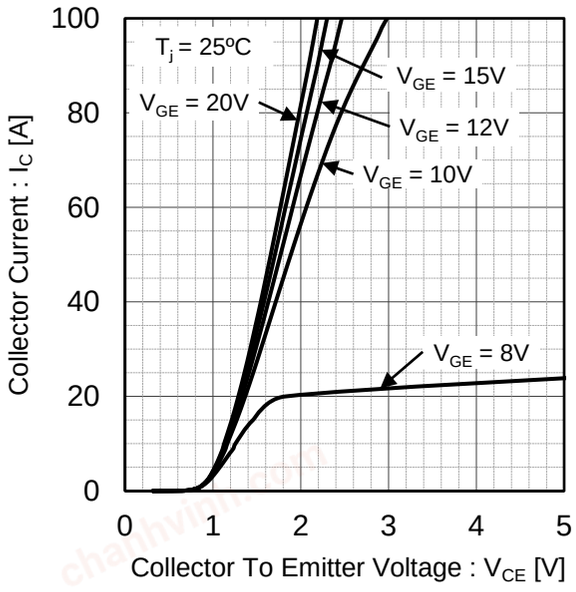


Fig.6 Typical Output Characteristics

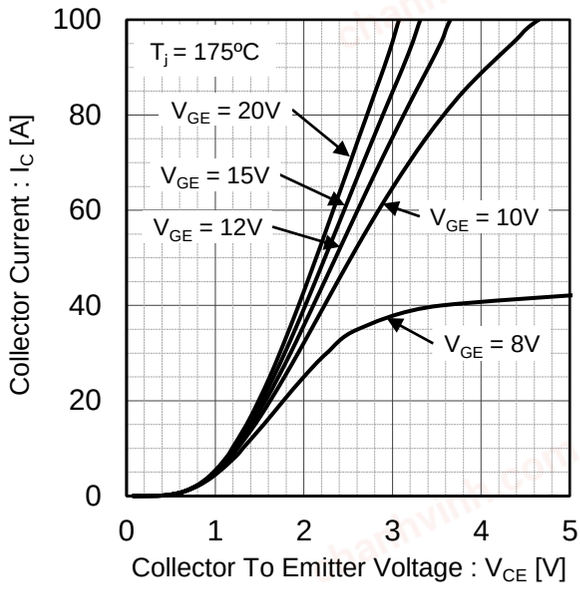


Fig.7 Typical Transfer Characteristics

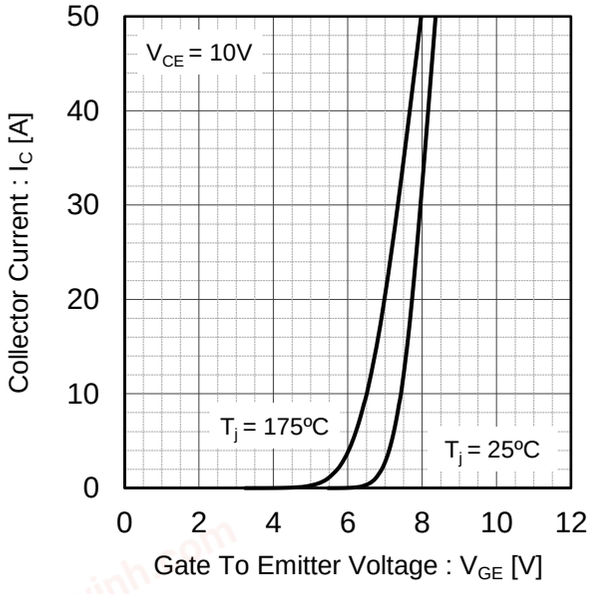
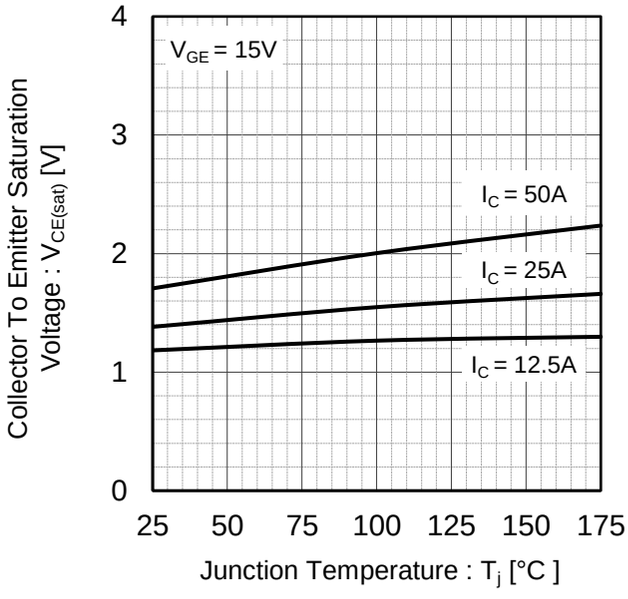


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



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●Electrical Characteristic Curves

Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

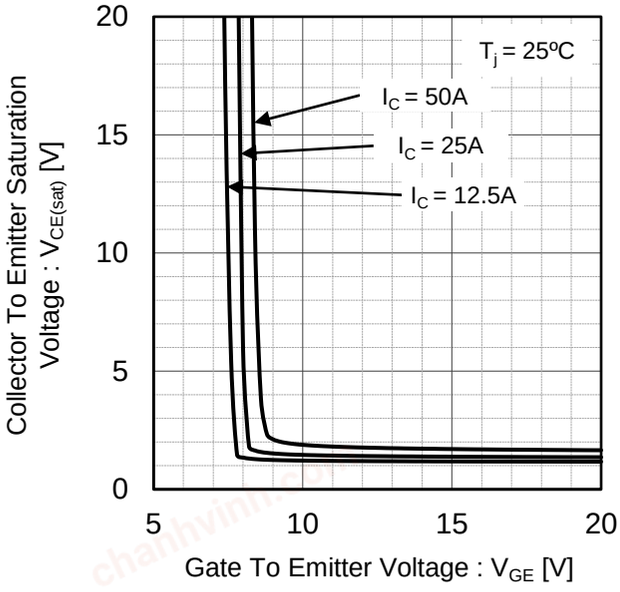


Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

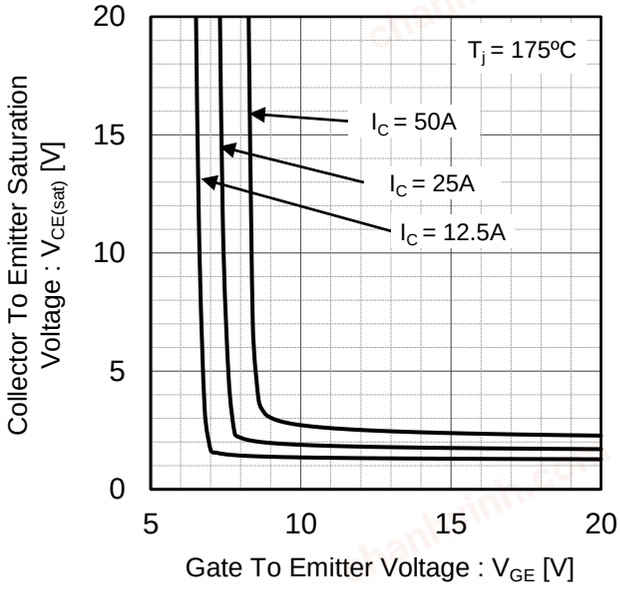


Fig.11 Typical Switching Time vs. Collector Current

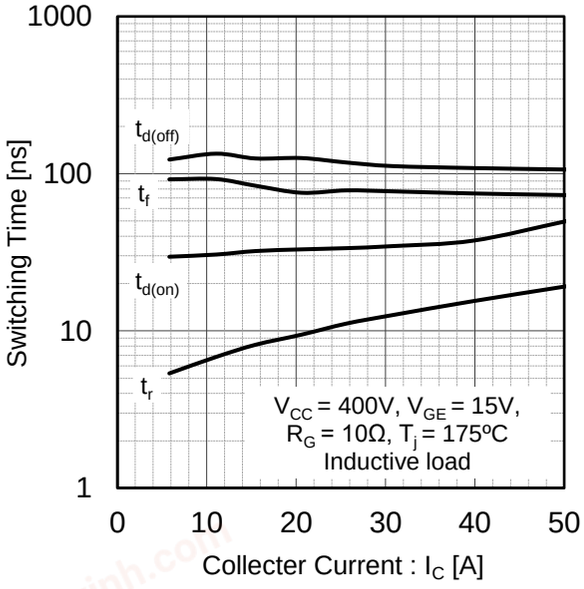
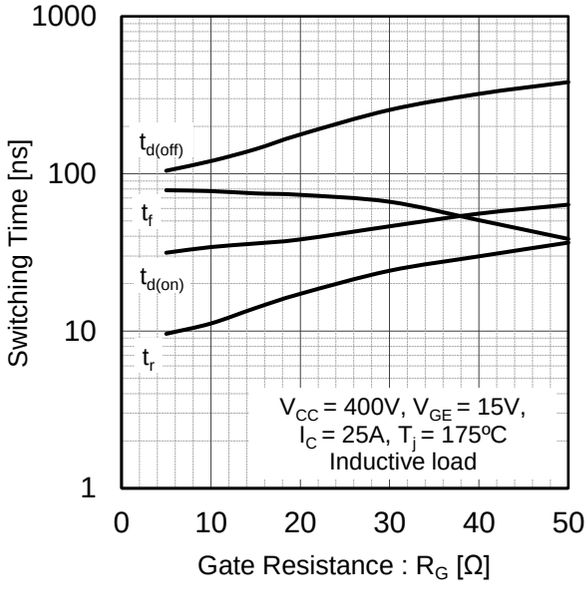


Fig.12 Typical Switching Time vs. Gate Resistance



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●Electrical Characteristic Curves

Fig.13 Typical Switching Energy Losses vs. Collector Current

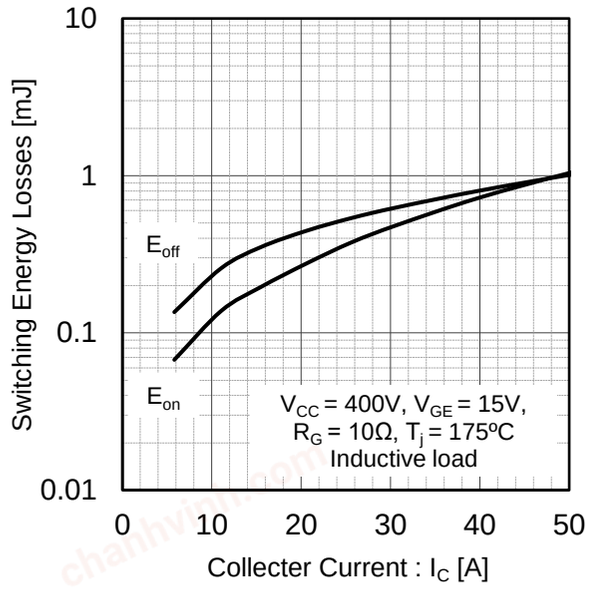


Fig.14 Typocal Switching Energy Losses vs. Gate Resistance

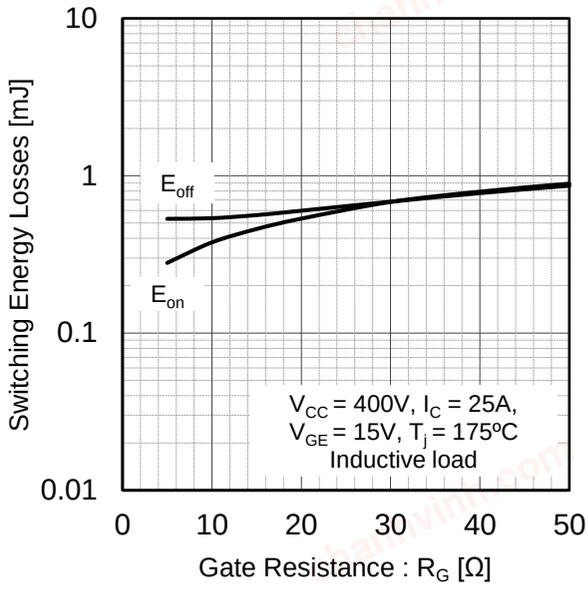


Fig.15 Typical Capacitance vs. Collector to Emitter Voltage

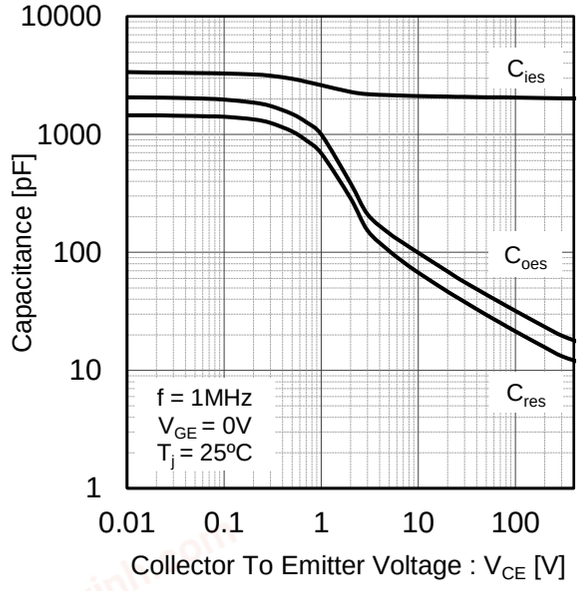
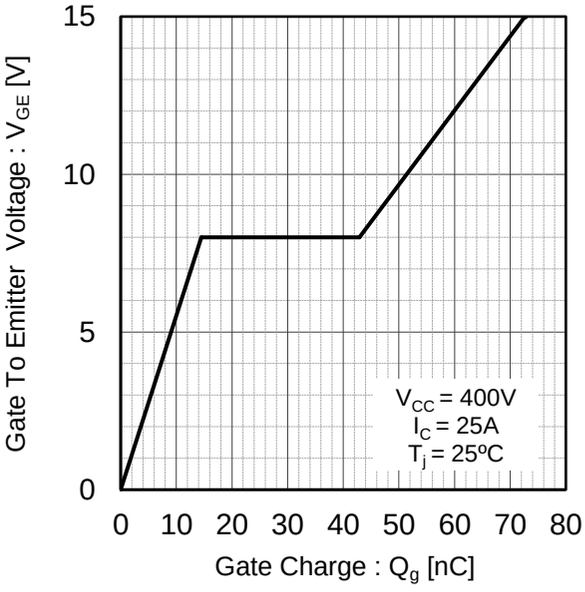


Fig.16 Typical Gate Charge



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●Electrical Characteristic Curves

Fig.17 Typical Diode Forward Current vs. Forward Voltage

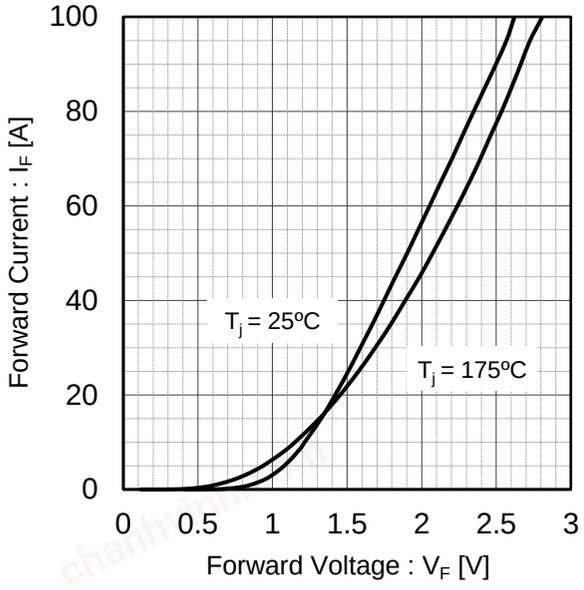


Fig.18 Typical Diode Revese Recovery Time vs. Forward Current

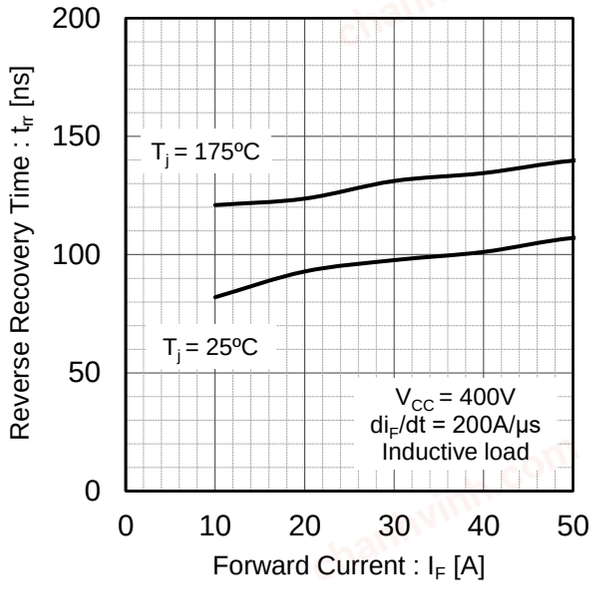


Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

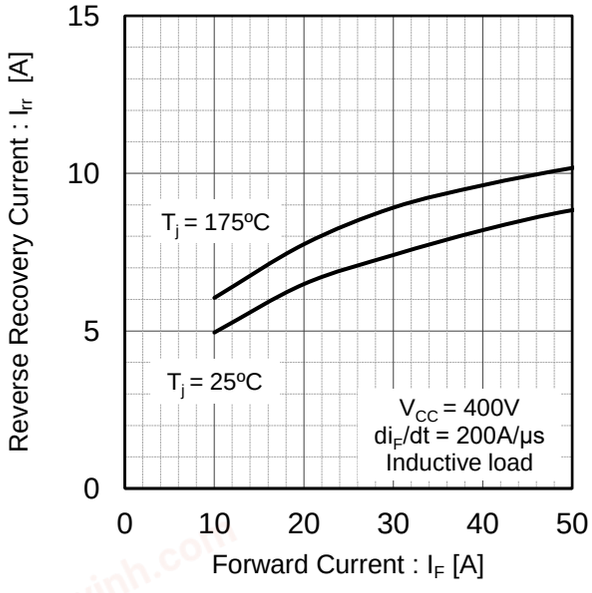
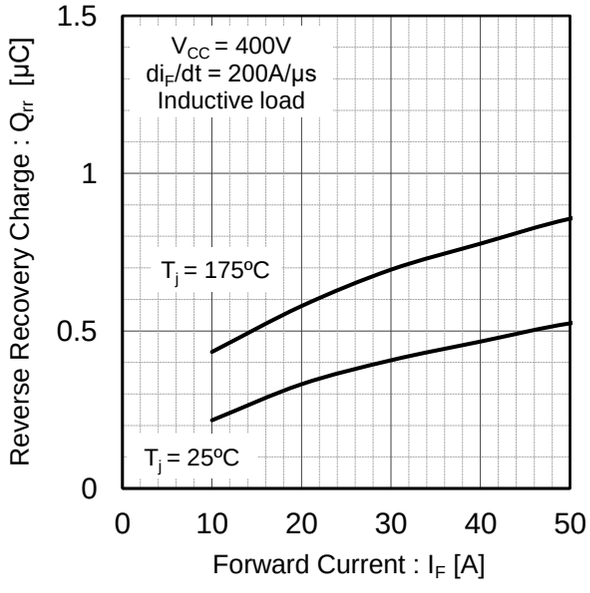


Fig.20 Typical Diode Rrverse Recovery Charge vs. Forward Current

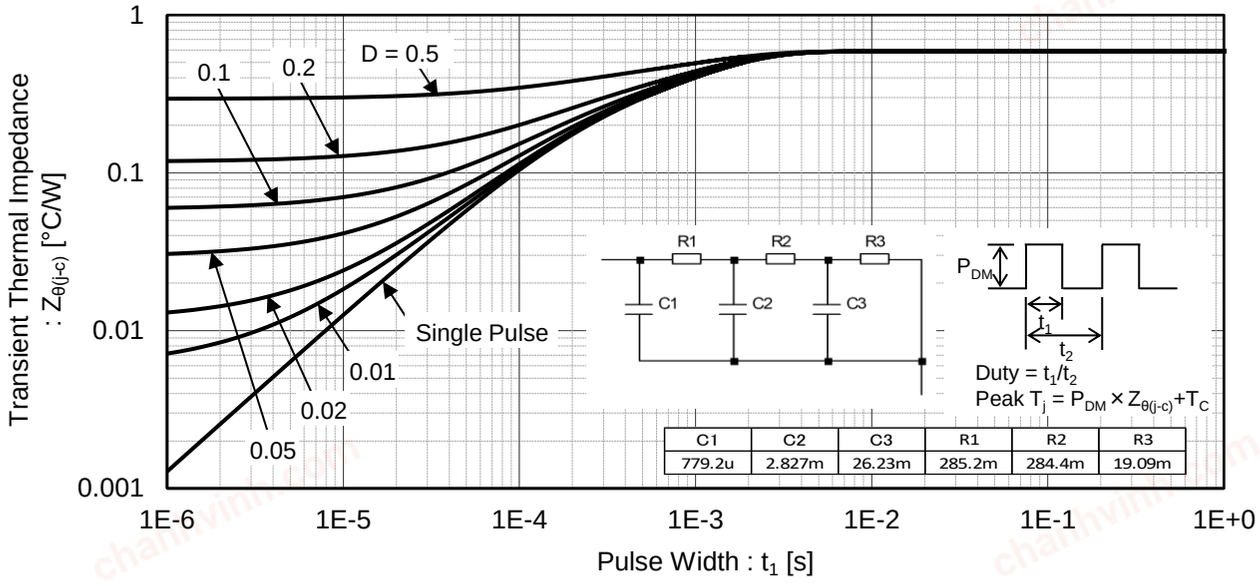


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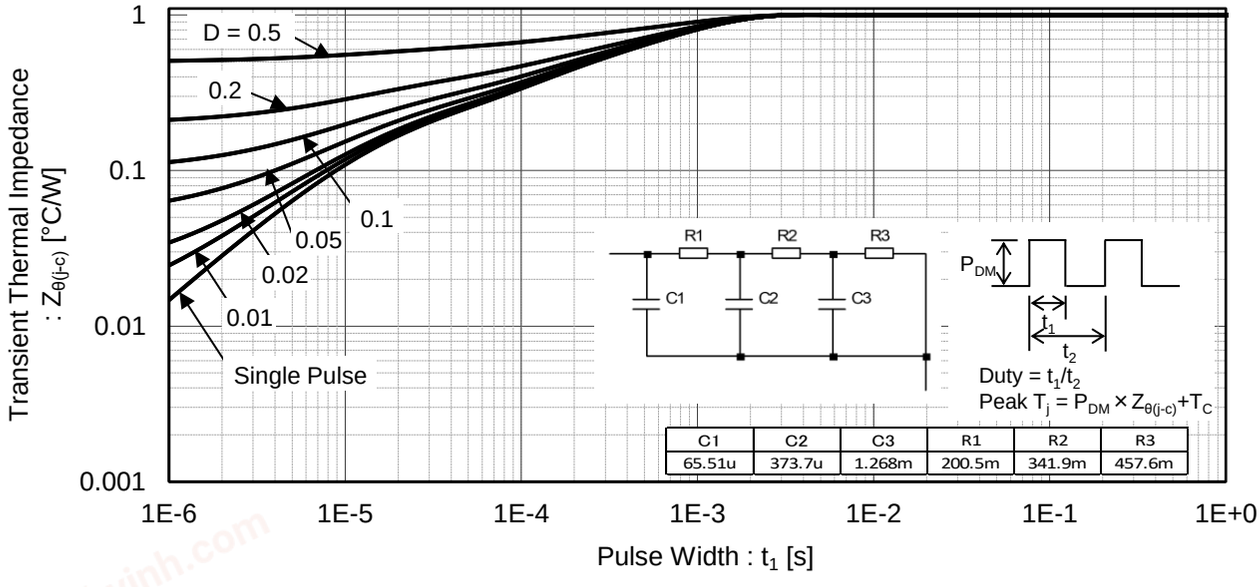
●Electrical Characteristic Curves

Fig.21 Typical IGBT Transient Thermal Impedance



C1	C2	C3	R1	R2	R3
779.2u	2.827m	26.23m	285.2m	284.4m	19.09m

Fig.22 Typical Diode Transient Thermal Impedance



C1	C2	C3	R1	R2	R3
65.51u	373.7u	1.268m	200.5m	341.9m	457.6m

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●Inductive Load Switching Circuit and Waveform

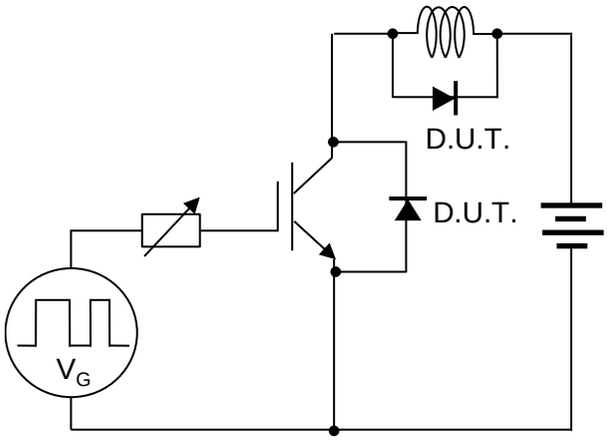


Fig.23 Inductive Load Circuit

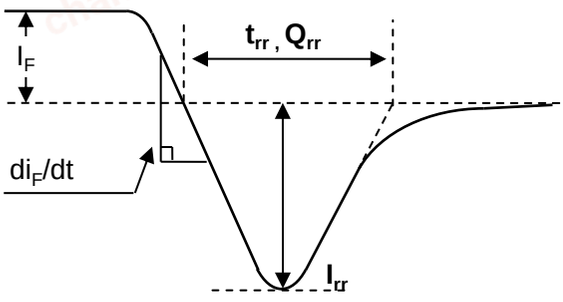


Fig.25 Diode Reverse Recovery Waveform

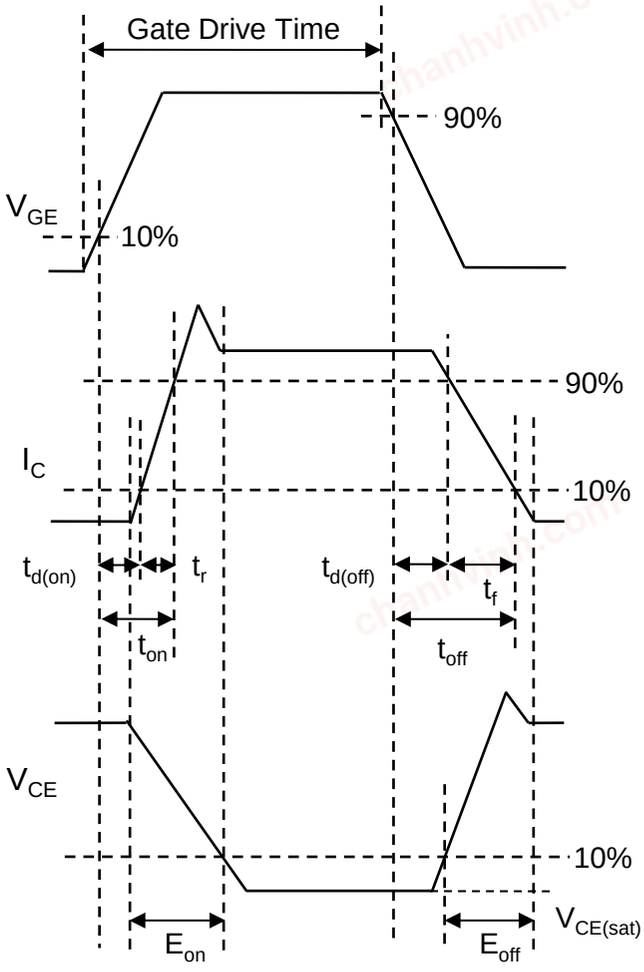


Fig.24 Inductive Load Waveform

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