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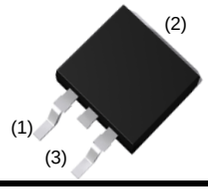
RGW40NL65HRBTL

650V 20A Field Stop Trench IGBT Datasheet

V_{CES}	650V
I_C	20A
$V_{CE(sat)}$ (Typ.)	1.5V
P_D	144W

●Outline

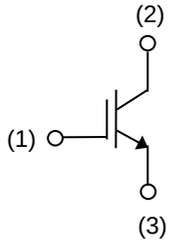
LPDL (TO-263L)



●Features

1) AEC-Q101 Qualified
2) Low Collector - Emitter Saturation Voltage
3) Low Switching Loss & Soft Switching
4) Pb - free Lead Plating ; RoHS Compliant

●Inner Circuit



(1) Gate
(2) Collector
(3) Emitter

●Application

Automotive
On & Off Board Chargers
DC-DC Converters
PFC
Industrial Inverter

●Packaging Specifications

Type	Packaging	Taping
	Reel Size (mm)	330
	Tape Width (mm)	24
	Basic Ordering Unit (pcs)	1,000
	Packing Code	TL
	Marking	RGW40NL65

●Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V_{CES}	650	V
Gate - Emitter Voltage		V_{GES}	±30	V
Collector Current	T_C = 25°C	I_C	48	A
	T_C = 100°C	I_C	30	A
Pulsed Collector Current		I_{CP} *1	80	A
Power Dissipation	T_C = 25°C	P_D	144	W
	T_C = 100°C	P_D	72	W
Operating Junction Temperature		T_j	-40 to +175	°C
Storage Temperature		T_{stg}	-55 to +175	°C

*1 Pulse width limited by $T_{jmax.}$

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●Thermal Resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	1.04	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Collector - Emitter Breakdown Voltage	BV_{CES}	I_C = 10μA, V_{GE} = 0V	650	-	-	V
Collector Cut - off Current	I_{CES}	V_{CE} = 650V, V_{GE} = 0V	-	-	10	μA
Gate - Emitter Leakage Current	I_{GES}	V_{GE} = ±30V, V_{CE} = 0V	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	V_{CE} = 5V, I_C = 13.3mA	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	$V_{CE(sat)}$	I_C = 20A, V_{GE} = 15V,				V
		T_j = 25°C	-	1.5	1.9	
		T_j = 175°C	-	1.85	-	

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●**IGBT Electrical Characteristics** (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	V_{CE} = 30V,	-	1680	-	pF
Output Capacitance	C_{oes}	V_{GE} = 0V,	-	47	-	
Reverse transfer Capacitance	C_{res}	f = 1MHz	-	31	-	
Total Gate Charge	Q_g	V_{CE} = 400V,	-	59	-	nC
Gate - Emitter Charge	Q_{ge}	I_C = 20A,	-	13	-	
Gate - Collector Charge	Q_{gc}	V_{GE} = 15V	-	23	-	
Turn - on Delay Time	$t_{d(on)}$	I_C = 10A, V_{CC} = 400V, V_{GE} = 15V, R_G = 10Ω, T_j = 25°C Inductive Load *E_{on} include diode reverse recovery	-	33	-	ns
Rise Time	t_r		-	7	-	
Turn - off Delay Time	$t_{d(off)}$		-	129	-	
Fall Time	t_f		-	32	-	
Turn - on Switching Loss	E_{on}		-	0.11	-	mJ
Turn - off Switching Loss	E_{off}		-	0.16	-	
Turn - on Delay Time	$t_{d(on)}$	I_C = 10A, V_{CC} = 400V, V_{GE} = 15V, R_G = 10Ω, T_j = 175°C Inductive Load *E_{on} include diode reverse recovery	-	32	-	ns
Rise Time	t_r		-	7	-	
Turn - off Delay Time	$t_{d(off)}$		-	143	-	
Fall Time	t_f		-	48	-	
Turn - on Switching Loss	E_{on}		-	0.12	-	mJ
Turn - off Switching Loss	E_{off}		-	0.21	-	
Reverse Bias Safe Operating Area	RBSOA	I_C = 80A, V_{CC} = 520V, V_P = 650V, V_{GE} = 15V, R_G = 100Ω, T_j = 175℃	FULL SQUARE			-

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●Electrical Characteristic Curves

Fig.1 Power Dissipation vs. Case Temperature

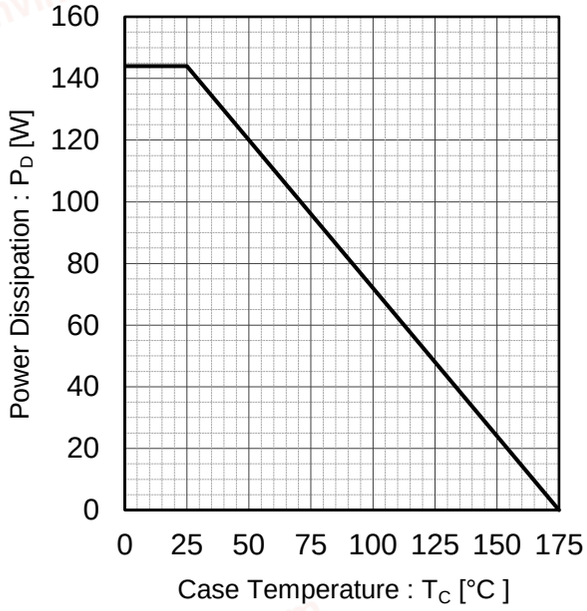


Fig.2 Collector Current vs. Case Temperature

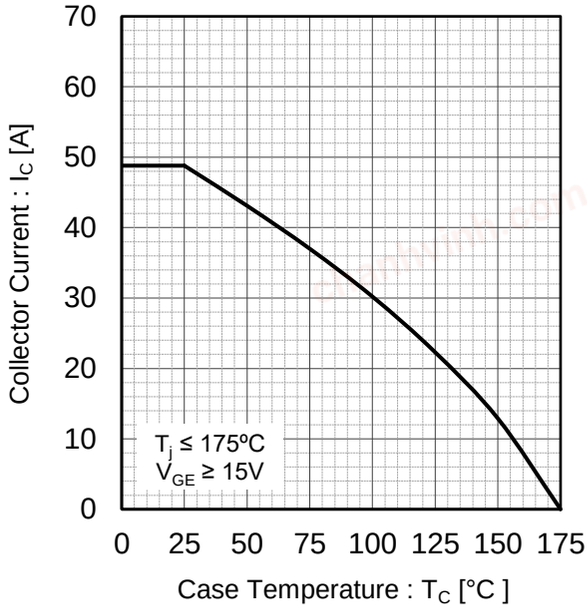


Fig.3 Forward Bias Safe Operating Area

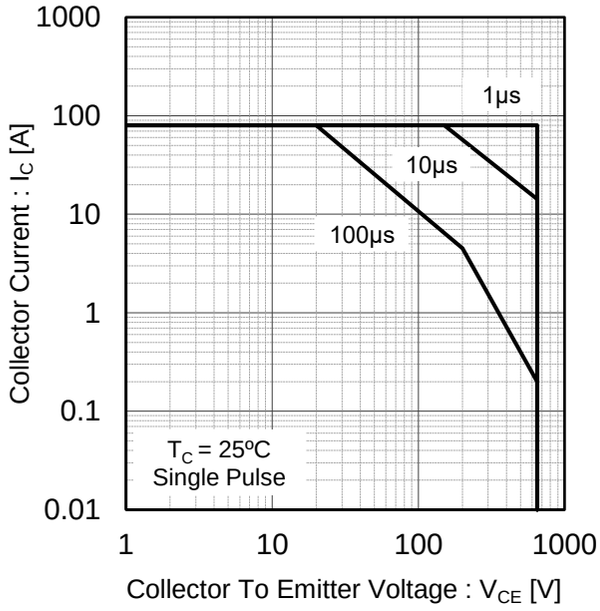
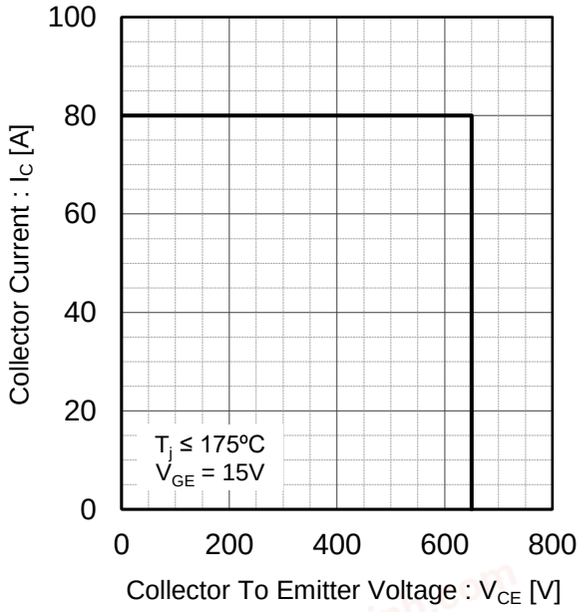


Fig.4 Reverse Bias Safe Operating Area



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●Electrical Characteristic Curves

Fig.5 Typical Output Characteristics

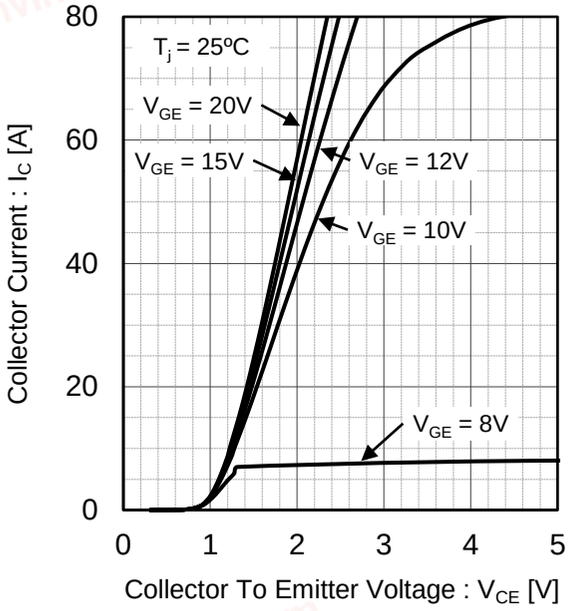


Fig.6 Typical Output Characteristics

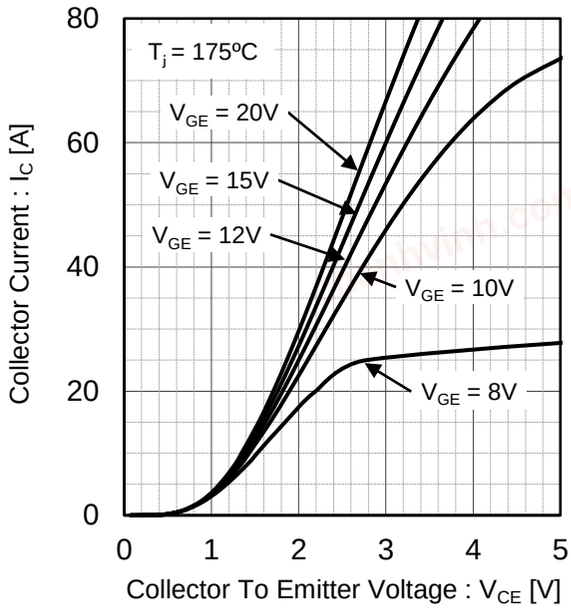


Fig.7 Typical Transfer Characteristics

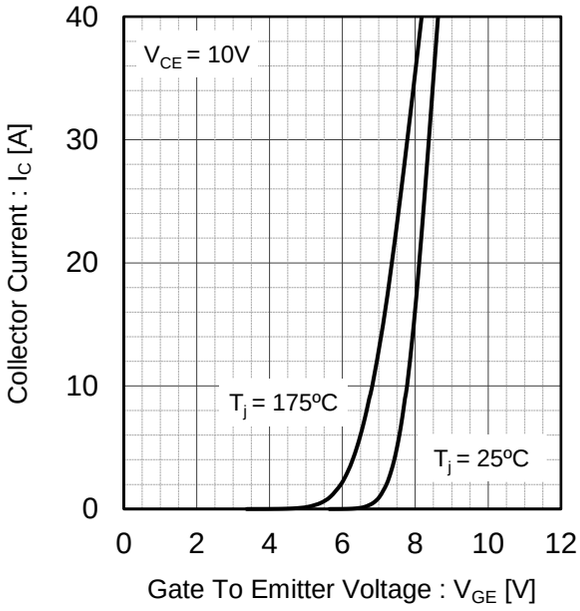
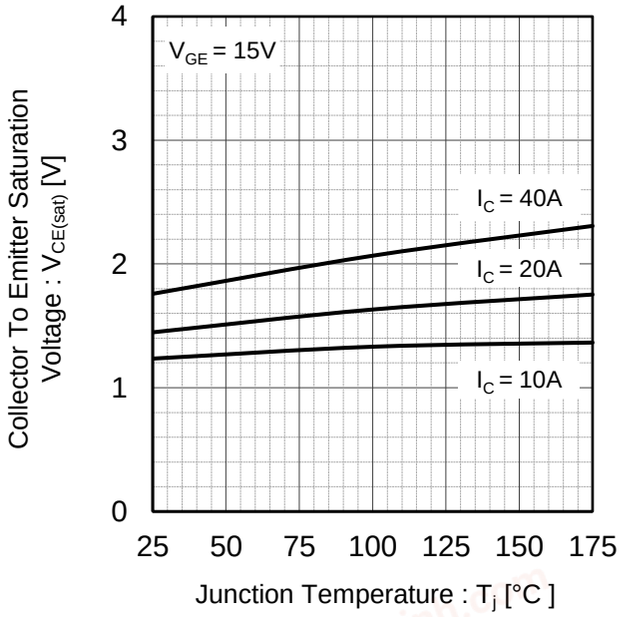


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



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●Electrical Characteristic Curves

Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

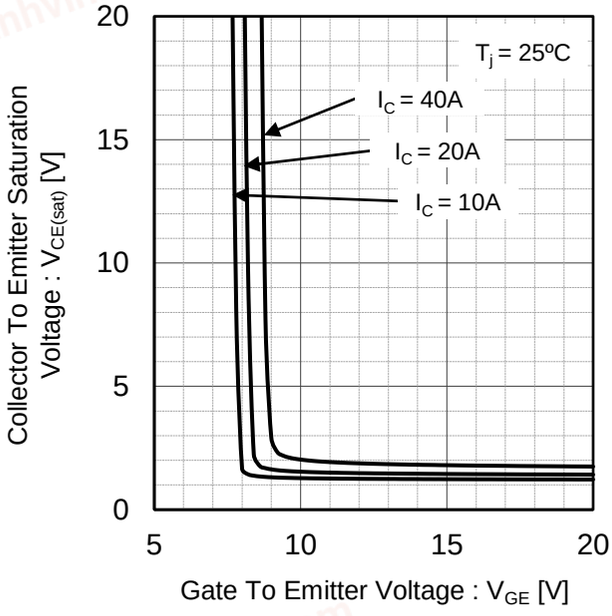


Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

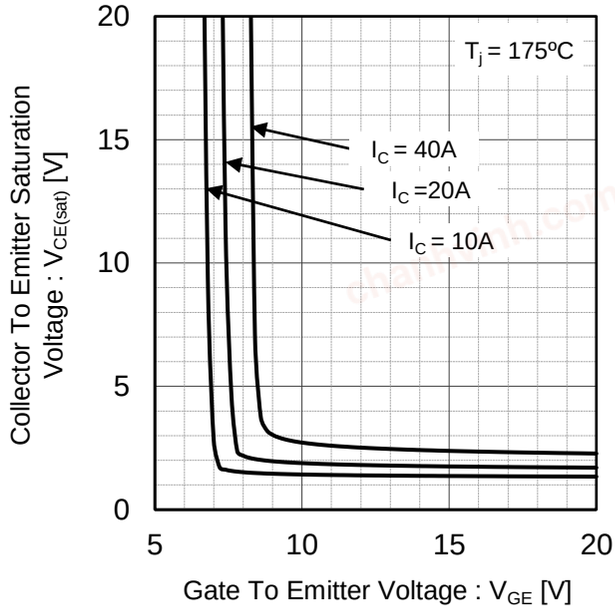


Fig.11 Typical Capacitance vs. Collector to Emitter Voltage

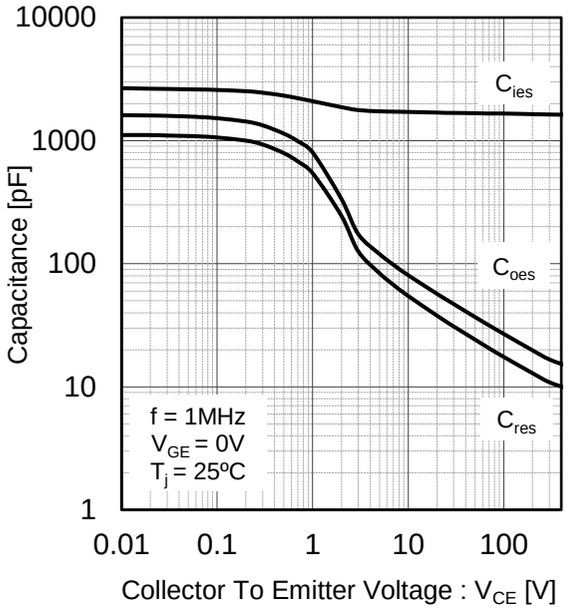
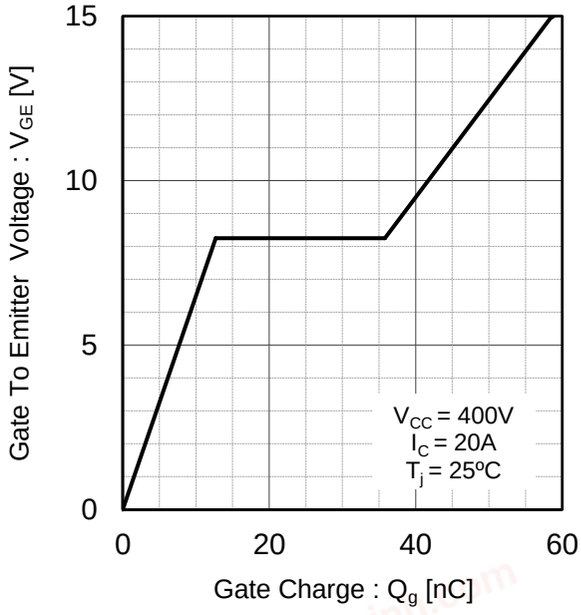


Fig.12 Typical Gate Charge



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●Electrical Characteristic Curves

Fig.13 Typical Switching Time vs. Collector Current

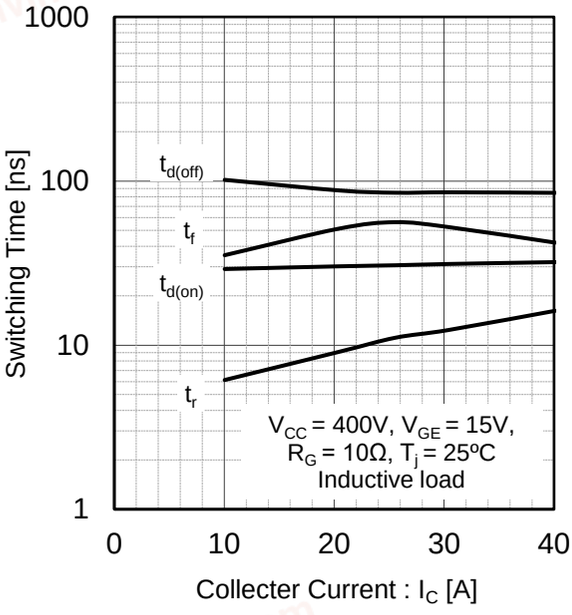


Fig.14 Typical Switching Time vs. Gate Resistance

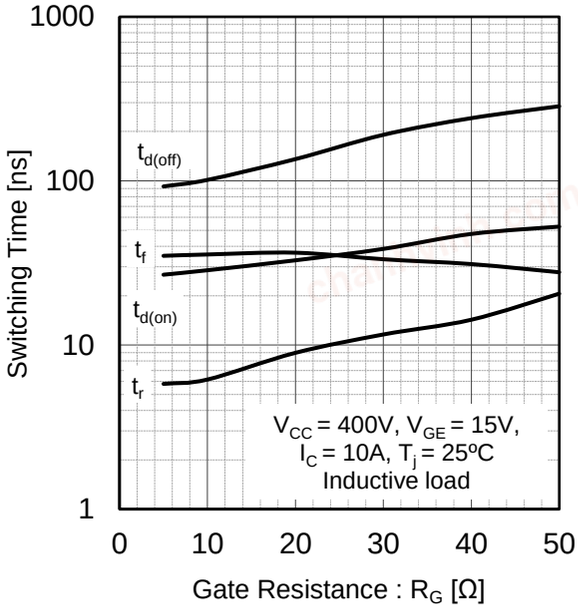


Fig.15 Typical Switching Energy Losses vs. Collector Current

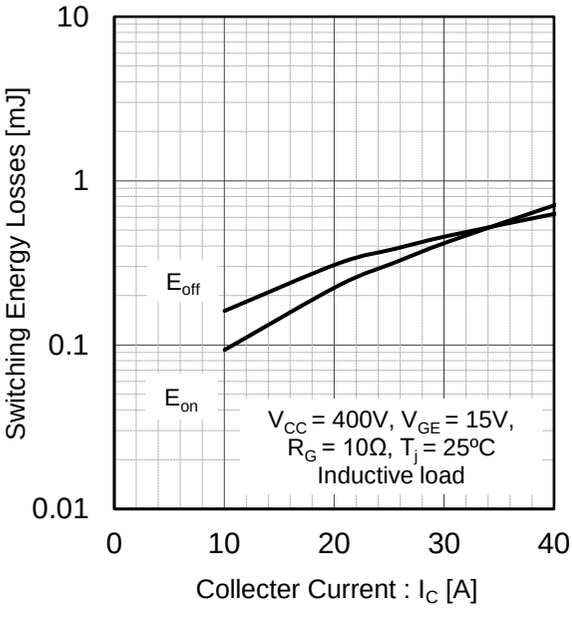
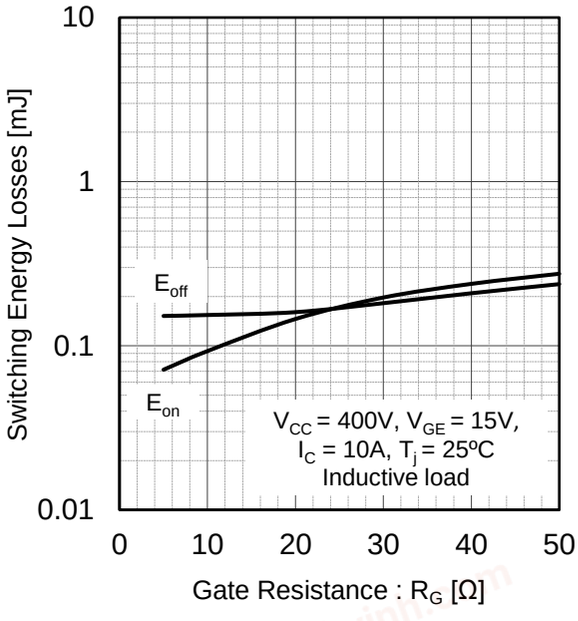


Fig.16 Typical Switching Energy Losses vs. Gate Resistance



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●Electrical Characteristic Curves

Fig.17 Typical Switching Time vs. Collector Current

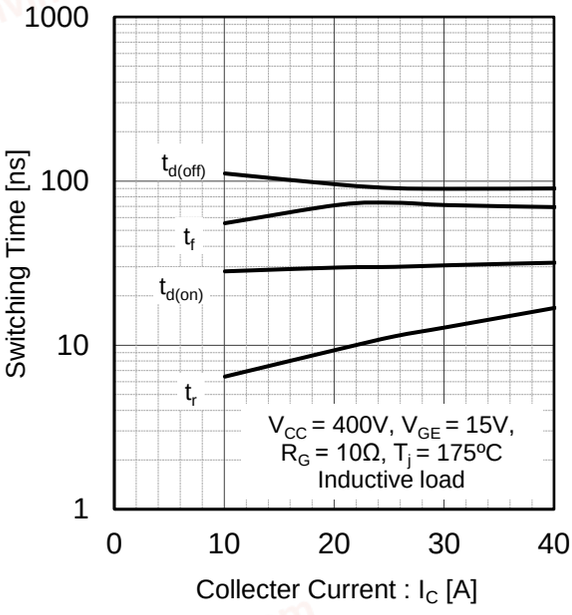


Fig.18 Typical Switching Time vs. Gate Resistance

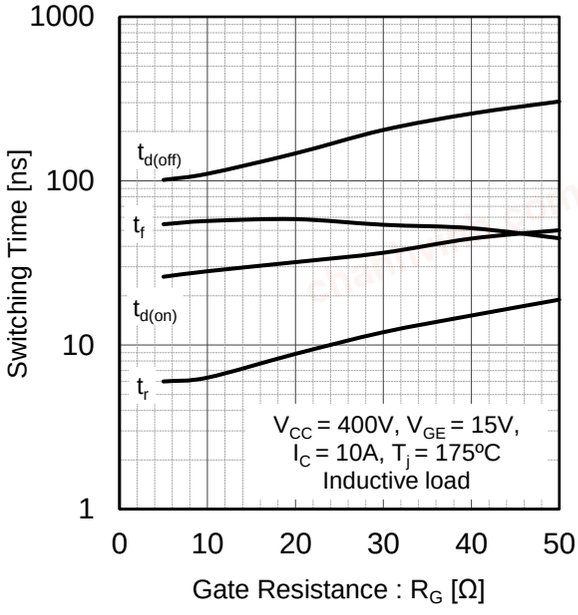


Fig.19 Typical Switching Energy Losses vs. Collector Current

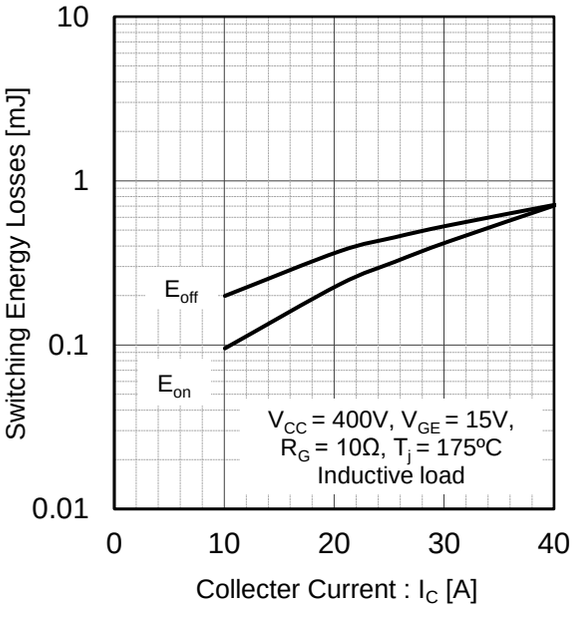
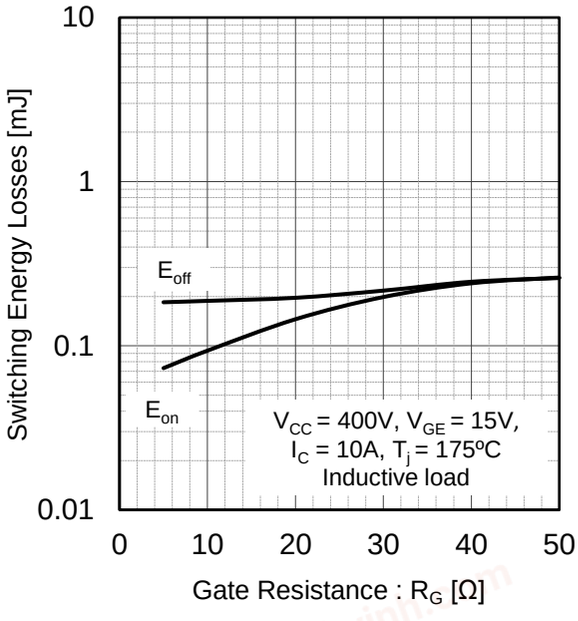


Fig.20 Typical Switching Energy Losses vs. Gate Resistance

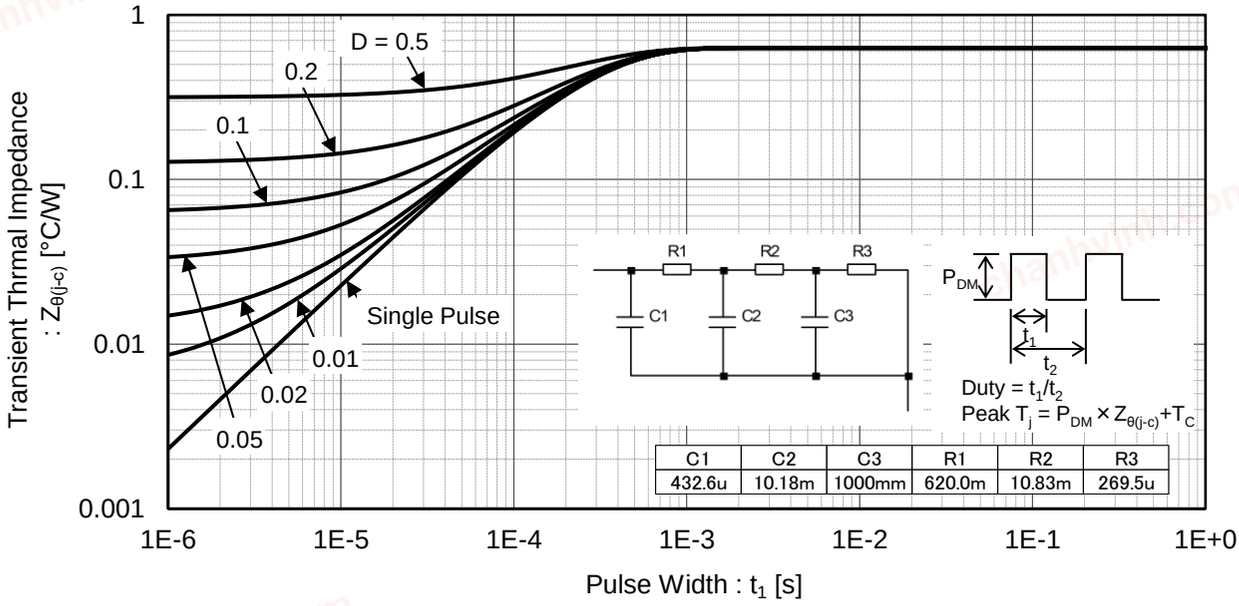


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●Electrical Characteristic Curves

Fig.21 Typical IGBT Transient Thermal Impedance



C1	C2	C3	R1	R2	R3
432.6u	10.18m	1000mm	620.0m	10.83m	269.5u

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●Inductive Load Switching Circuit and Waveform

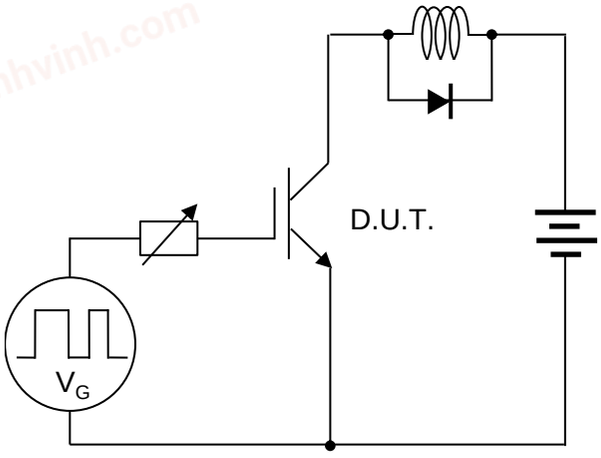


Fig.22 Inductive Load Circuit

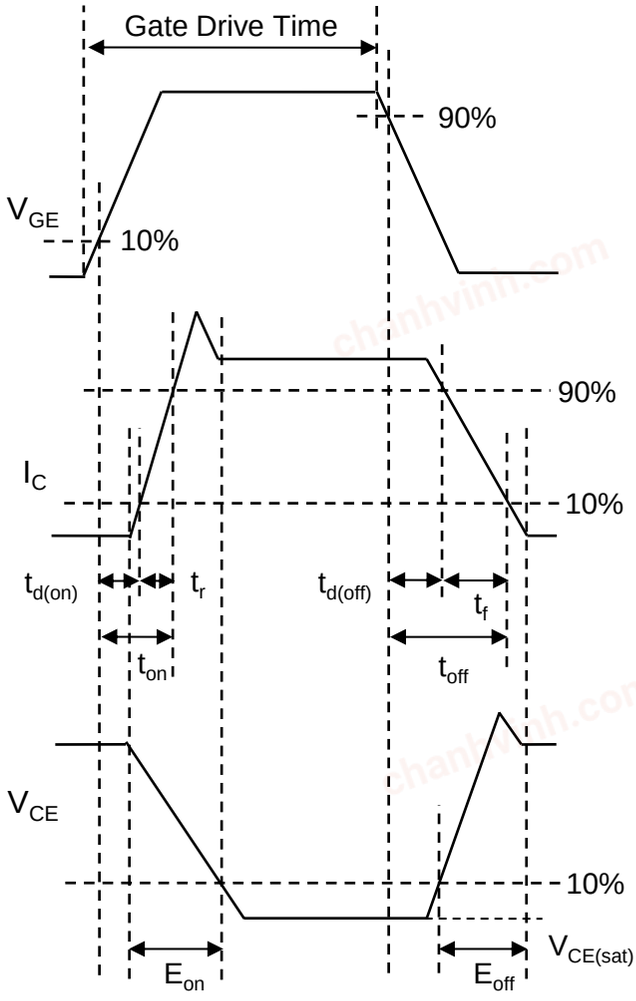


Fig.23 Inductive Load Waveform

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