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Datasheet

Omnipolar Detection Hall IC

(Dual Outputs for both S and N Pole Polarity Detection)

BU52737GWZ

General Description

The omnipolar detection Hall IC incorporating a polarity determination circuit enables separate operation (output) of both the South and North poles. Using a magnet and the Hall IC, detection of open and close of the cover are possible in smart phones and tablets, and detection of putting wireless earphones in a case, and detection of front/back side or rotational direction are possible in digital cameras and other applications involving display panels.

Features

- Omnipolar Detection
  (OUT1 = S-pole Detection; OUT2 = N-pole Detection)
- Micro Power Operation (Small Current Using Intermittent Operation Method)
- Ultra-compact Package

Applications

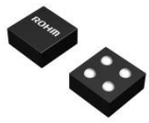
- Smart Phones, Tablets, Wireless Earphones, Notebook Computers, Digital Cameras, etc.

Key Specifications

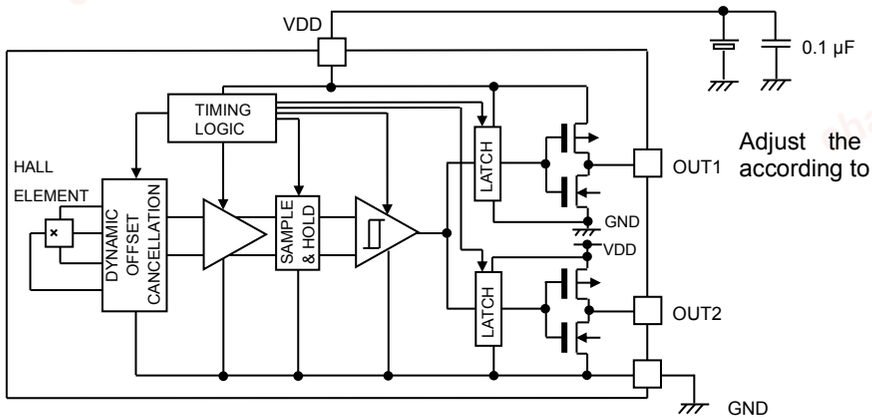
- VDD Voltage Range: 2.5 V to 4.5 V
- Operate Point: ±15 mT (Typ)
- Hysteresis: 2 mT (Typ)
- Period: 50 ms (Typ)
- Supply Current (AVG): 0.8 µA (Typ)
- Output Type: CMOS
- Operating Temperature Range: -40 °C to +85 °C

Package

Package	W (Typ) x D (Typ) x H (Max)
UCSP35L1	0.80 mm x 0.80 mm x 0.40 mm



Typical Application Circuit and Block Diagram



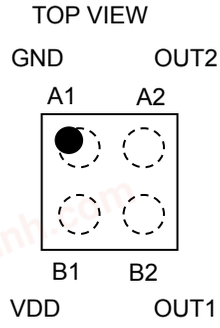
Adjust the bypass capacitor value as necessary, according to power supply noise conditions, etc.

Pin Descriptions

Pin No.	Pin Name	Function
A1	GND	Ground
A2	OUT2	Output (Detect to the north pole)
B1	VDD	Power supply*(Note 1)*
B2	OUT1	Output (Detect to the south pole)

(Note 1) Dispose a bypass capacitor between VDD and GND.

Pin Configuration



○Product structure: Silicon integrated circuit ○This product has no designed protection against radioactive rays

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Absolute Maximum Ratings (Ta = 25 °C)

Parameter	Symbol	Rating	Unit
Power Supply Voltage	V_{DD}	7.0	V
Output Current	I_{OUT}	±0.5	mA
Power Dissipation	Pd	0.10	W
Storage Temperature Range	Tstg	-40 to +125	°C
Maximum Junction Temperature	Tjmax	125	°C

Caution 1: *Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.*

Caution 2: *Should by any chance the maximum junction temperature rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, design a PCB with power dissipation taken into consideration by increasing board size and copper area so as not to exceed the maximum junction temperature rating.*

Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Unit
Power Supply Voltage	V_{DD}	2.5	3.0	4.5	V
Operating Temperature	Topr	-40	+25	+85	°C

Magnetic and Electrical Characteristics (Unless otherwise specified V_{DD} = 3.0 V Ta = 25 °C)

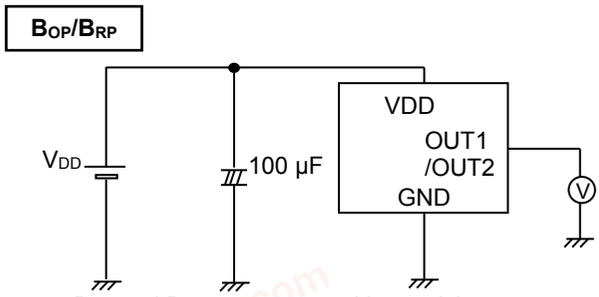
Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Operate Point	B_{OPS}	-	15	17	mT	Output: OUT1 (Detect to the south pole)
	B_{OPN}	-17	-15	-		Output: OUT2 (Detect to the north pole)
Release Point	B_{RPS}	11	13	-	mT	Output: OUT1 (Detect to the south pole)
	B_{RPN}	-	-13	-11		Output: OUT2 (Detect to the north pole)
Hysteresis	B_{HYSS}	-	2	-	mT	-
	B_{HYSN}	-	2	-		-
Period	t_P	-	50	100	ms	-
Output High Voltage	V_{OH}	V_{DD} -0.2	-	-	V	I_{OUT} = -0.5 mA
Output Low Voltage	V_{OL}	-	-	0.2	V	I_{OUT} = +0.5 mA
Supply Current	I_{DD}	-	0.8	1.8	µA	Average

(Note) Polarity of Magnetic flux density is defined as positive when south pole side of magnet approaches top surface of the device.

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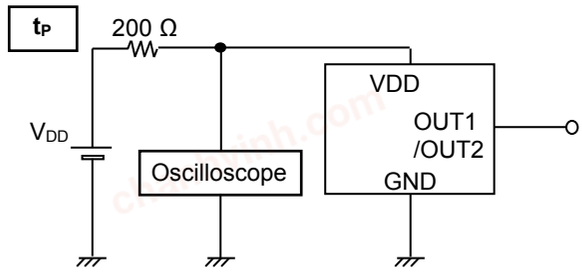
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Measurement Circuit



BOP and BRP are measured by applying an external magnetic field

Figure 1. BOP, BRP Measurement Circuit



The period is monitored by an oscilloscope

Figure 2. tP Measurement Circuit

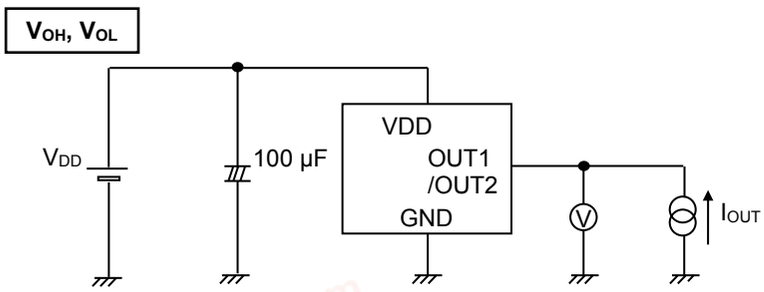


Figure 3. VOH, VOL Measurement Circuit

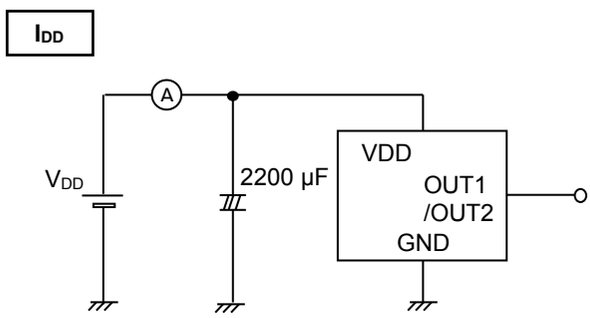


Figure 4. IDD Measurement Circuit

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Typical Performance Curves

(Reference data)

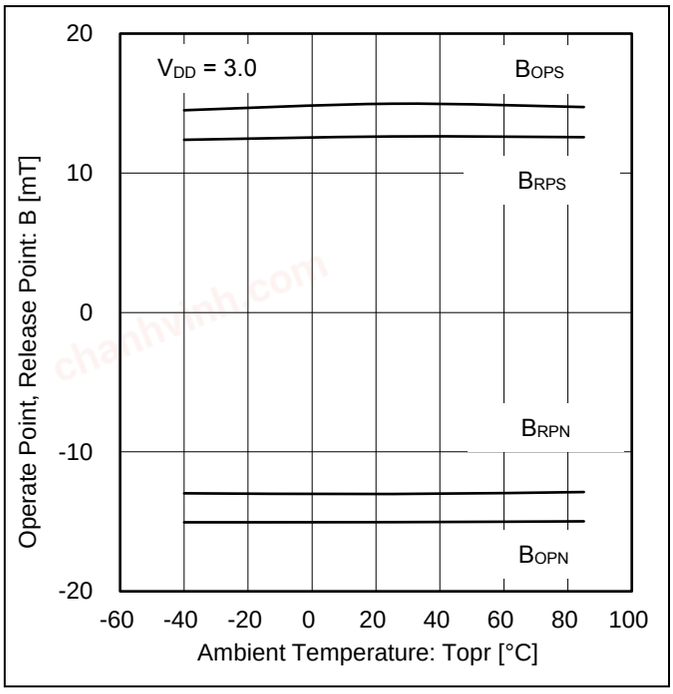


Figure 5. Operate Point, Release Point vs Ambient Temperature

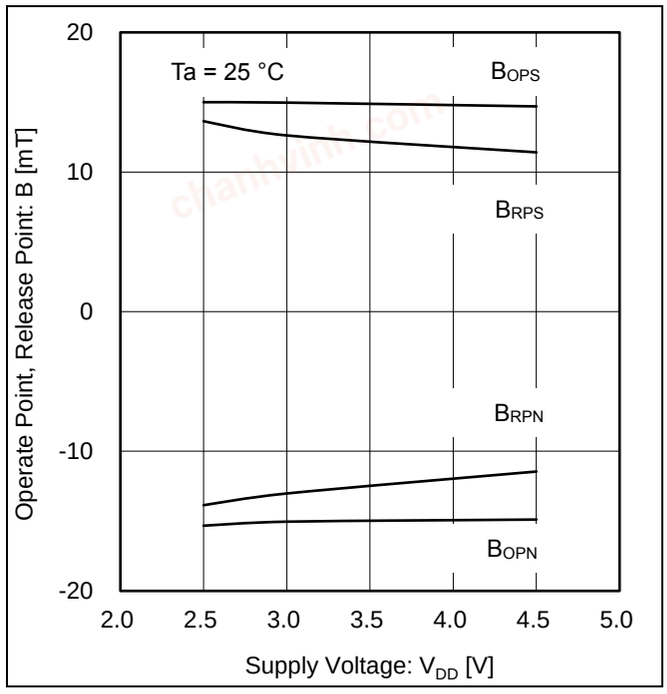


Figure 6. Operate Point, Release Point vs Supply Voltage

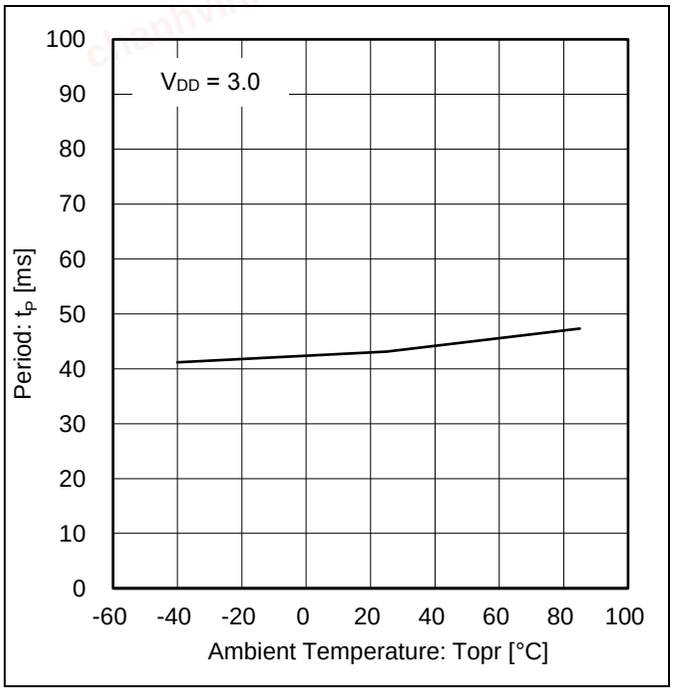


Figure 7. Period vs Ambient Temperature

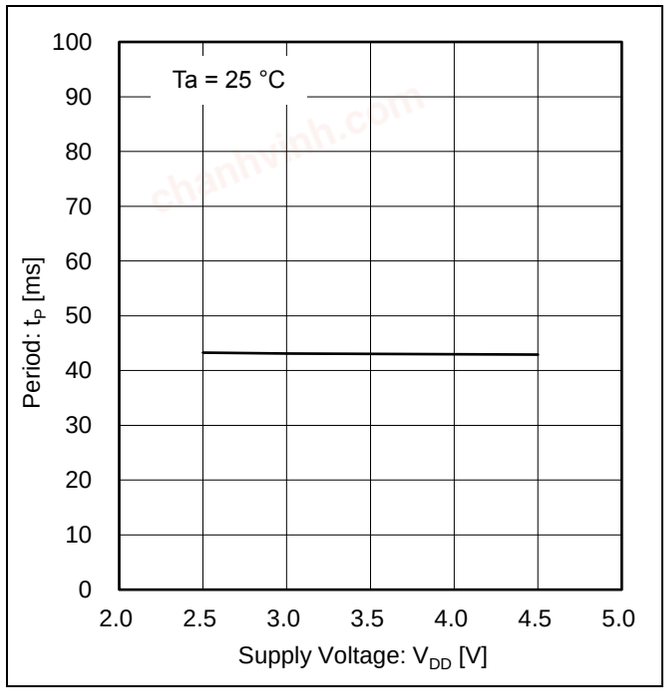


Figure 8. Period vs Supply Voltage

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Typical Performance Curves - continued
(Reference data)

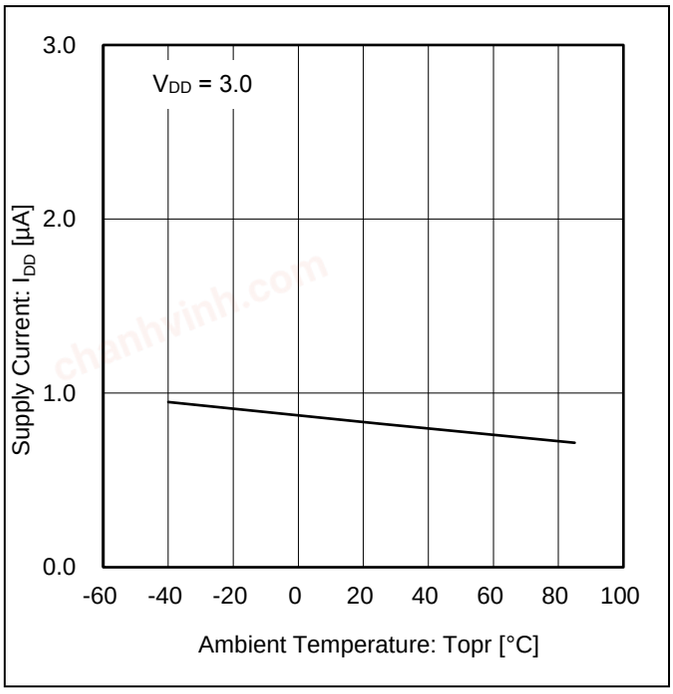


Figure 9. Supply Current vs Ambient Temperature

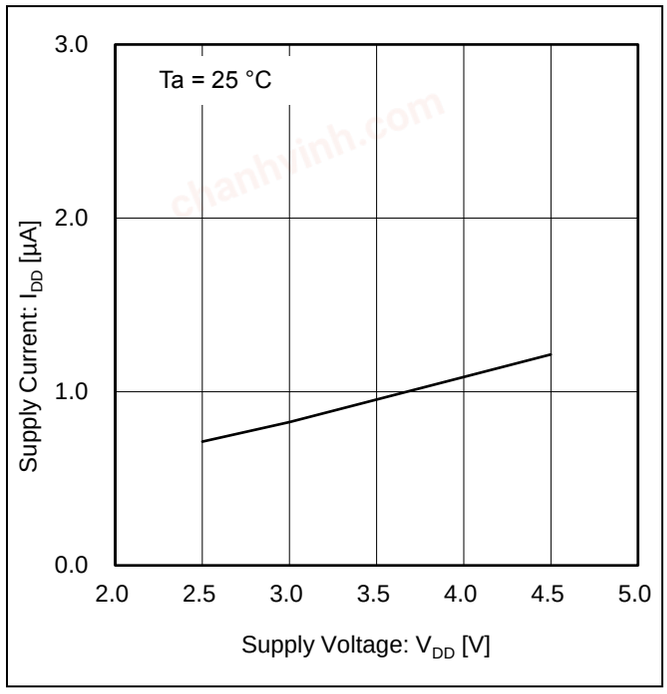


Figure 10. Supply Current vs Supply Voltage

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Description of Operations

Micropower Operation (Small Current Consumption Using Intermittent Sensing)

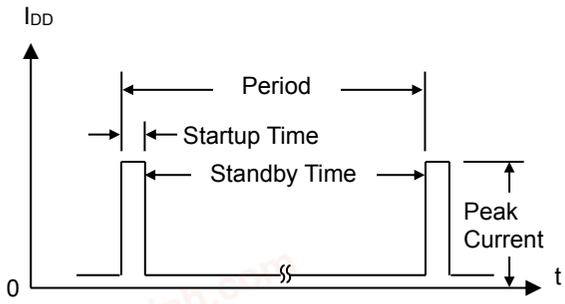


Figure 11. Timing Chart of Micropower Operation

The omnipolar detection Hall IC uses intermittent sensing save energy. At startup, the Hall elements, amplifier, comparator, and other detection circuits powered on and magnetic detection begins. During standby, the detection circuits powered off, thereby reducing current consumption. The detection results are held and output during standby time.

$$Period: t_P \text{ [ms]}$$

$$Startup\ Time: \frac{t_P}{50000} \text{x4 [us]}$$

$$Peak\ Current: 10 \text{ [mA]}$$

(Peak Current is reference data. This is not 100 % tested.)

(Offset Cancellation)

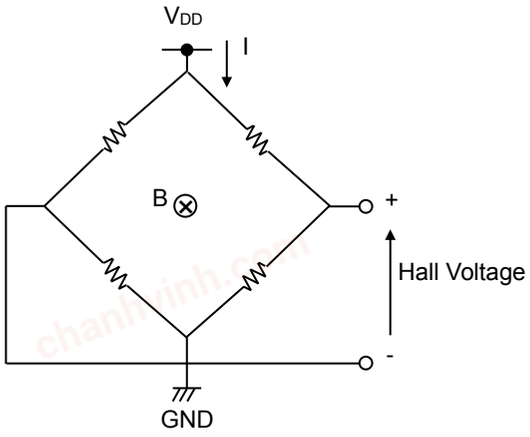


Figure 12. Equivalent Circuit of Hall Elements

The Hall elements are shown with an equivalent Wheatstone (resistor) bridge circuit. Offset voltage may be generated by a differential in this bridge resistance, or can arise from changes of resistance due to package or bonding stress. A dynamic offset cancellation circuit is employed to cancel this offset voltage.
When the Hall elements are connected as shown in Figure 12 and a magnetic field is applied perpendicular to the Hall elements, a voltage is generated at the mid-points of the bridge. This is known as Hall voltage.
Dynamic offset cancellation switches the wiring (shown in the figure 12) to redirect the current flow to a 90 ° angle from its original path, and thereby cancels the offset voltage of Hall elements.
Only the magnetic signal is maintained in the sample/hold circuit process and then released.

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Description of Operations - continued
(Magnetic Field Detection Mechanism)

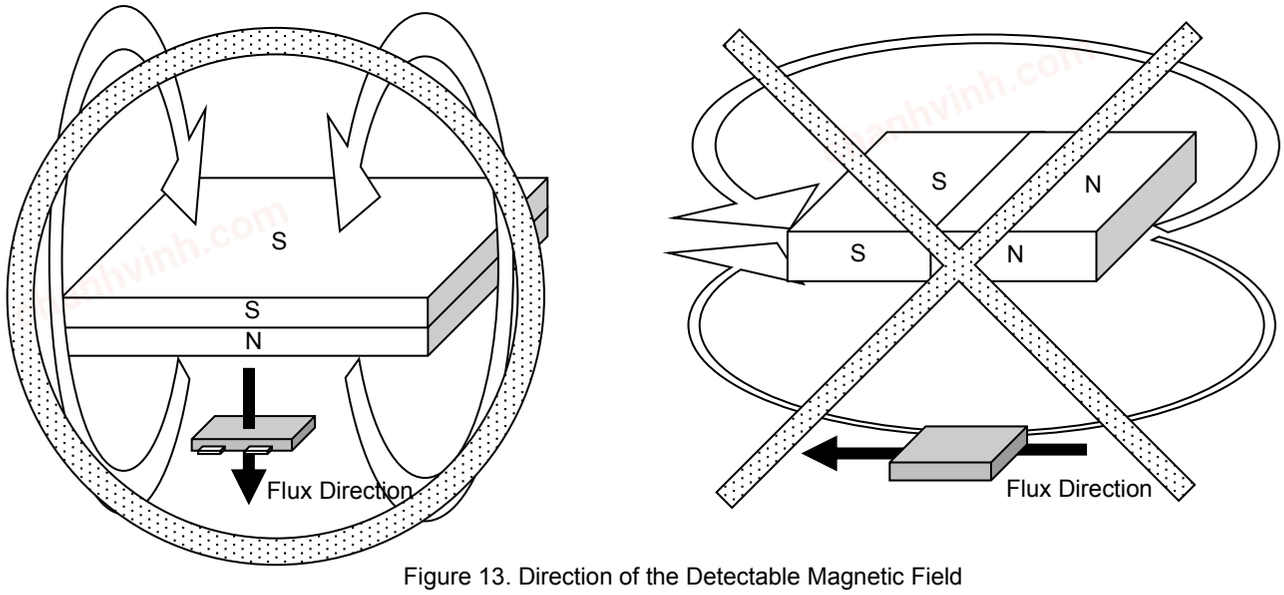


Figure 13. Direction of the Detectable Magnetic Field

The Hall IC cannot detect magnetic fields that run horizontal to the package top layer.
Be certain to configure the Hall IC so that the magnetic field is perpendicular to the top layer.

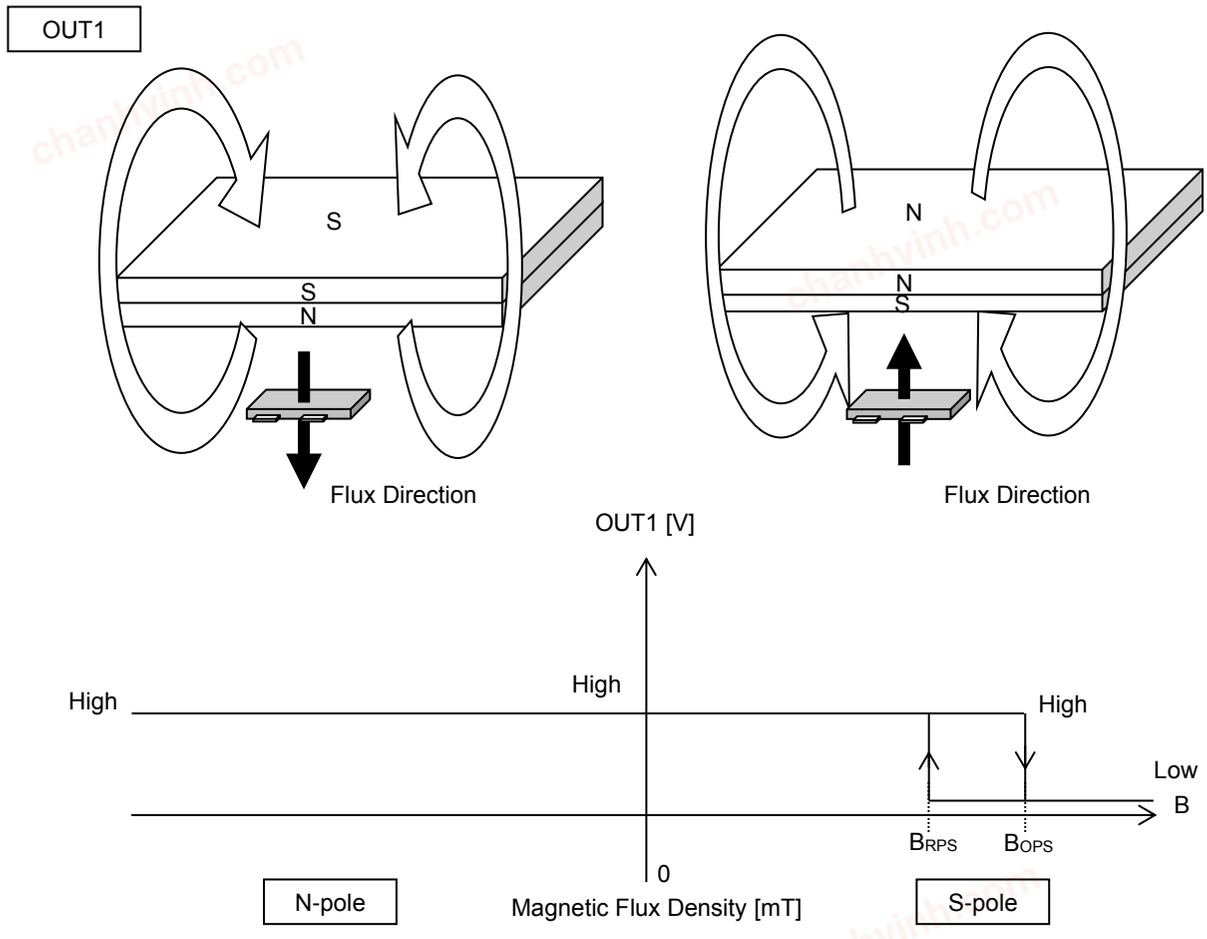


Figure 14. S-pole Detection

OUT1 detects only S pole magnetic field. (OUT1 doesn't detect N pole.)

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Description of Operations - continued

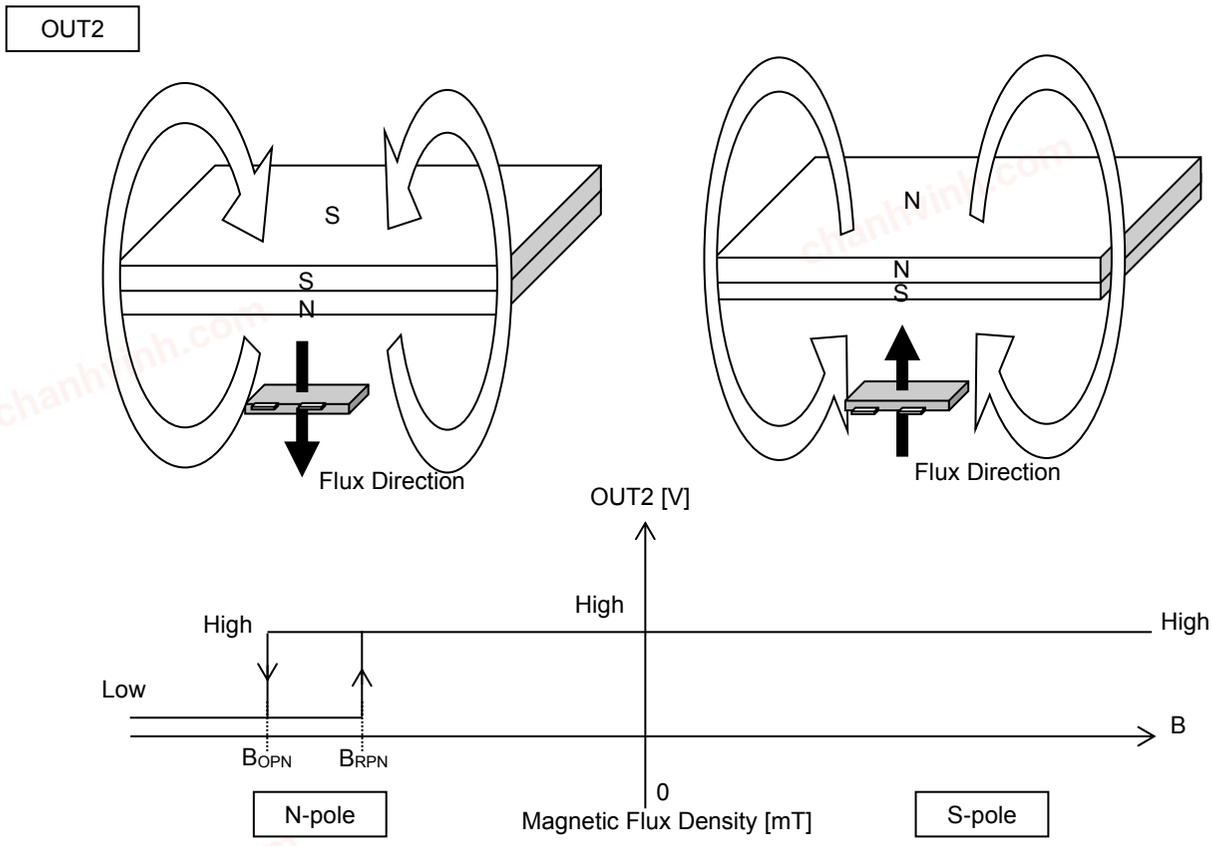


Figure 15. N-pole Detection

OUT2 detects only N pole magnetic field. (OUT2 doesn't detect S pole.)

The dual output omnipolar detection Hall IC detects magnetic fields running perpendicular to the top surface of the package. When the distance between magnet and Hall IC is far and magnetic flux density is smaller than the operate point (B_{OP}), output goes HIGH. When the magnet gets closer to the IC and magnetic density rises to the operate point, the output switches LOW. In LOW output mode, the distance from the magnet to the IC increases again until the magnetic density falls to a point just below B_{OP}, and output returns HIGH. The point where magnetic flux density restores a HIGH output is known as the release point, B_{RP}. This detection and adjustment mechanism is designed to prevent noise and other erratic system operation.

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Intermittent Operation at Power ON

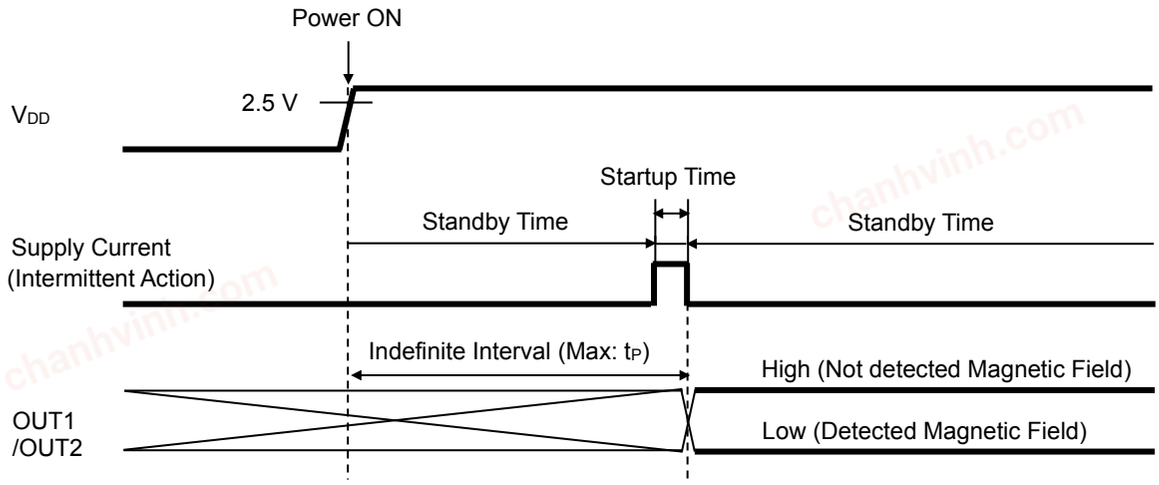


Figure 16. Timing Chart of Intermittent Operation at Power ON

The omnipolar detection Hall IC adopts an intermittent operation method in detecting the magnetic field during startup, as shown in Figure 16. The IC outputs the detection result and maintains the output condition during the standby period. The output is an indefinite interval from power ON to the first end of startup (Max: t_P).

Magnet Selection

Neodymium and ferrite are major permanent magnets. Neodymium generally offers greater magnetic power per volume than ferrite, thereby enabling miniaturization of magnet. The larger neodymium magnet is, the stronger magnetic flux density is. And the farther detection distance is, the weaker it is. Therefore, the proper size and detection distance of the magnet should be determined according to the operate point of Hall IC. To increase the magnet's detection distance, the magnet which is thicker or larger sectional area is used.

Position of the Hall Element

(Reference)

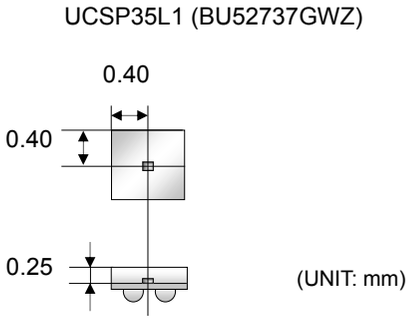


Figure 17. Position of the Hall Element

Output Equivalence Circuit

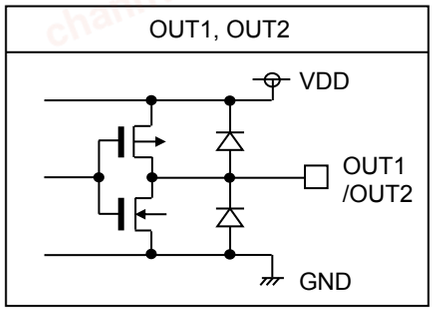


Figure 18. Output Equivalence Circuit

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Operational Notes

1. **Reverse Connection of Power Supply**
   Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

2. **Power Supply Lines**
   Design the PCB layout pattern to provide low impedance supply lines. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. **Ground Voltage**
   Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. **Ground Wiring Pattern**
   When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. **Recommended Operating Conditions**
   The function and operation of the IC are guaranteed within the range specified by the recommended operating conditions. The characteristic values are guaranteed only under the conditions of each item specified by the electrical characteristics.

6. **Inrush Current**
   When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

7. **Testing on Application Boards**
   When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

8. **Inter-pin Short and Mounting Errors**
   Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

9. **Unused Input Pins**
   Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

10. **Regarding the Input Pin of the IC**
    In the construction of this IC, P-N junctions are inevitably formed creating parasitic diodes or transistors. The operation of these parasitic elements can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions which cause these parasitic elements to operate, such as applying a voltage to an input pin lower than the ground voltage should be avoided. Furthermore, do not apply a voltage to the input pins when no power supply voltage is applied to the IC. Even if the power supply voltage is applied, make sure that the input pins have voltages within the values specified in the electrical characteristics of this IC.

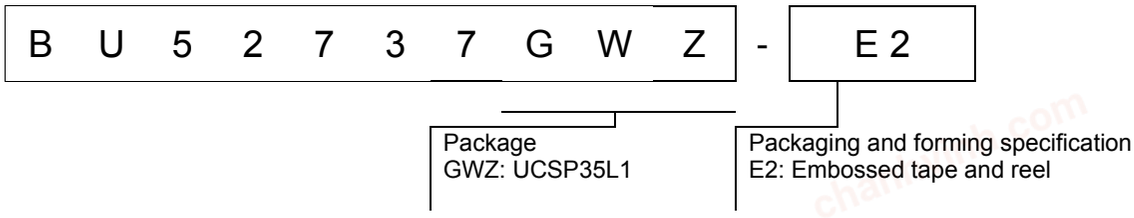
11. **Ceramic Capacitor**
    When using a ceramic capacitor, determine a capacitance value considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

12. **Disturbance light**
    In a device where a portion of silicon is exposed to light such as in a WL-CSP and chip products, IC characteristics may be affected due to photoelectric effect. For this reason, it is recommended to come up with countermeasures that will prevent the chip from being exposed to light.

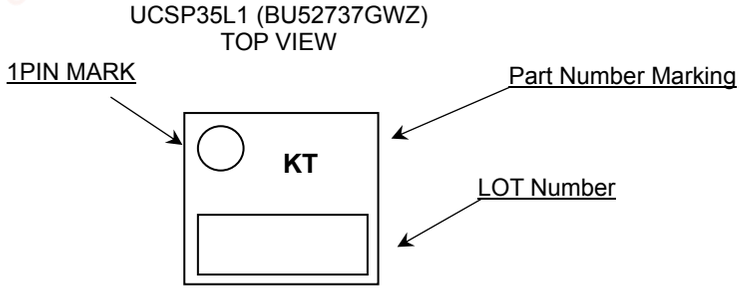
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Ordering Information



Marking Diagram

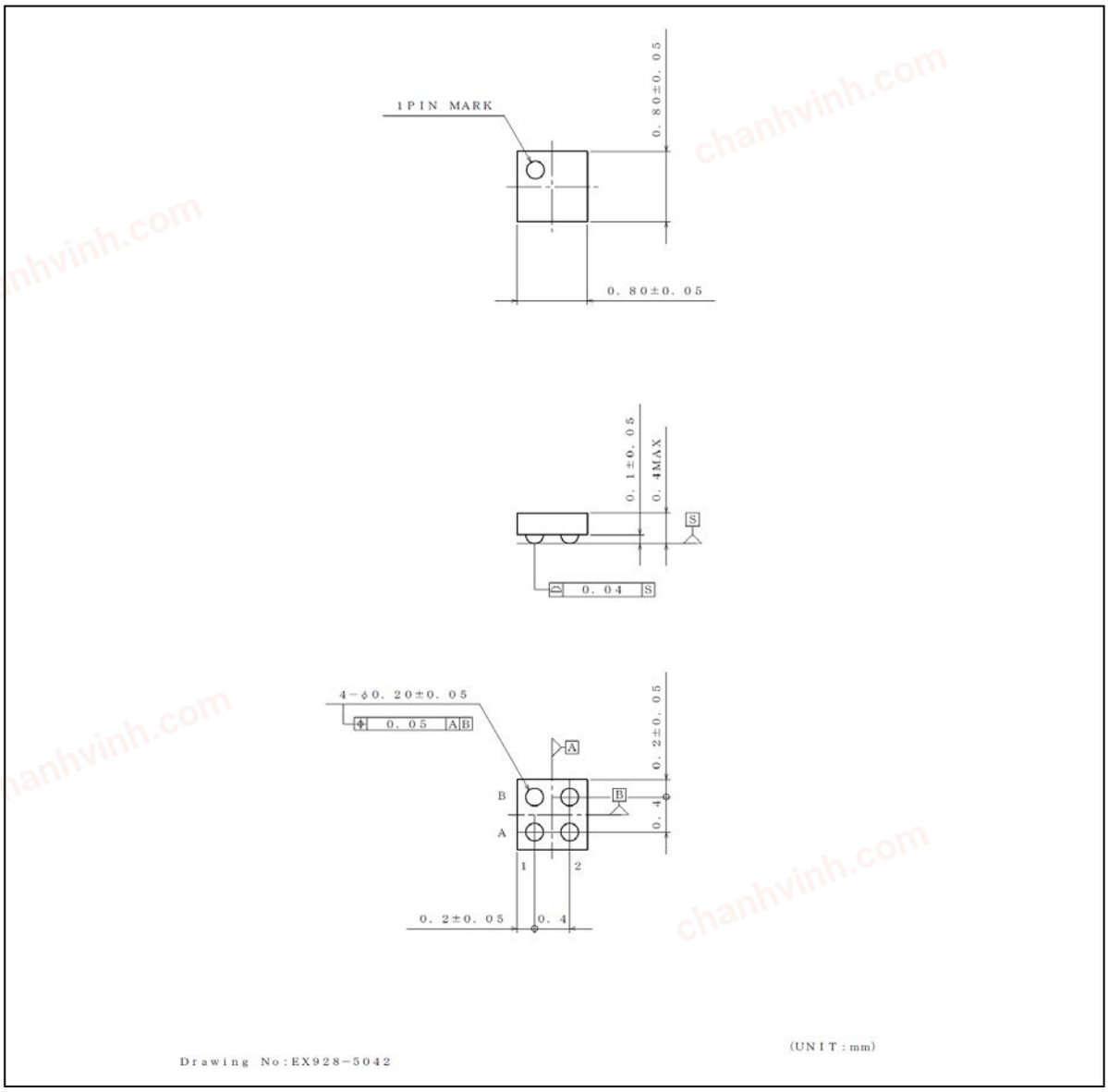


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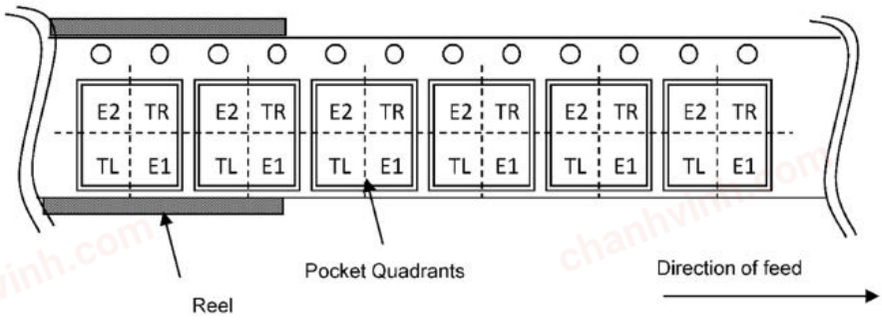
Physical Dimension and Packing Information

Package Name	UCSP35L1 (BU52737GWZ)



< Tape and Reel Information >

Tape	Embossed carrier tape
Quantity	6000 pcs
Direction of feed	E2 The direction is the pin 1 of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand



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Revision History

Date	Revision	Changes
05.Aug.2021	001	New Release

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Notice

Precaution on using ROHM Products

1. Our Products are designed and manufactured for application in ordinary electronic equipment (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSⅡb	CLASSⅢ
CLASSⅣ		CLASSⅢ	

2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
   [a] Installation of protection circuits or other protective devices to improve system safety
   [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure

3. Our Products are designed and manufactured for use under standard conditions and not under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
   [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
   [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
   [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   [f] Sealing or coating our Products with resin or other coating materials
   [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.) ; or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   [h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.

2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   - [a] the Products are exposed to sea winds or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   - [b] the temperature or humidity exceeds those recommended by ROHM
   - [c] the Products are exposed to direct sunshine or condensation
   - [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act

Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

Precaution Regarding Intellectual Property Rights

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General Precaution

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