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Datasheet

FS3000

Air Velocity Sensor Module

The FS3000 is a surface-mount type air velocity module that uses a MEMS thermopile-based sensor.

The FS3000 features a digital output with 12-bit resolution. The sensor comprises a “solid” thermal isolation technology and silicon-carbide coating to protect it from abrasive wear and water condensation.

The module features a compact design fit for low profile enclosures.

Features

- Robust “solid” isolation technology
- Resistant to surface contamination
- Resistant to vibration and pressure shock
- Low power application
- Digital output: I2C
- Supply voltage: 3.3V
- Module operating temperature range: -20°C to +85°C
- Module size: 8 × 9 × 3.7 mm

Applications

- Data center and servers
- HVAC and air control systems
- Laminar flow control systems
- Air filtration and collection systems

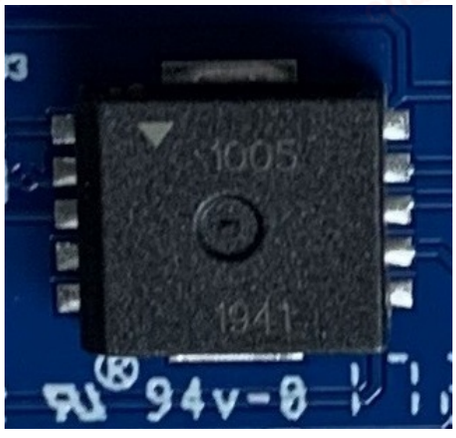


Figure 1. FS3000 Air Velocity Module

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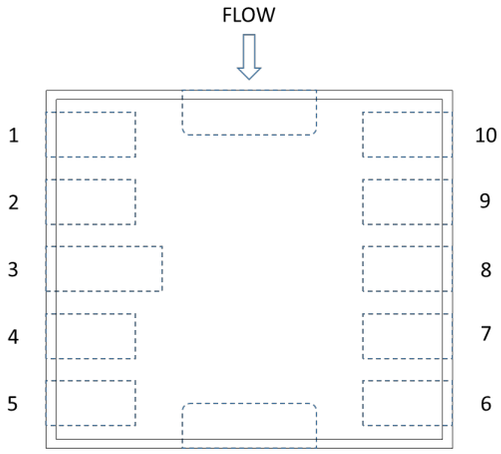
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1. Pin Information

1.1 Pin Assignments



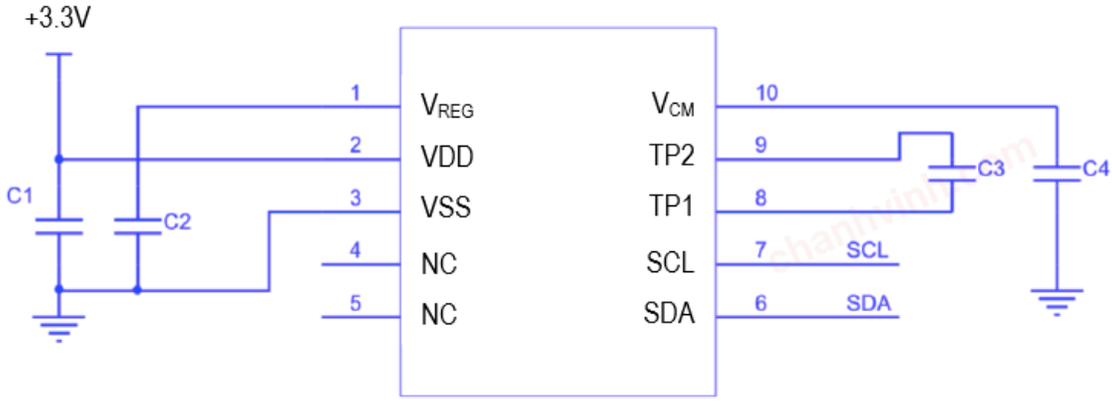
1.2 Pin Descriptions

Table 1. Pin Descriptions

Pin Number	Pin Name	Type	Description
1	V_{REG}	Input	ADC reference input.
2	V_{IN}	Input	Supply voltage.
3	GND	Ground	Ground.
4	-	-	Do not connect.
5	-	-	Do not connect.
6	SDA	Input/Output	Serial data.
7	SCL	Input	Serial clock.
8	TP1	Input/Output	Connect with capacitor.
9	TP2	Input/Output	Connect with capacitor.
10	V_{CM}	Output	Common bias.

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2. Application Circuit



Note:

- C1, C2, C3 are 0.1µF capacitors
- C4 is a 1µF capacitor

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3. Specifications

3.1 Absolute Maximum Ratings

The absolute maximum ratings are stress ratings only. Stresses greater than those listed below can cause permanent damage to the device. Functional operation of the FS3000 at absolute maximum ratings is not implied. Exposure to absolute maximum rating conditions may affect device reliability.

Symbol	Parameter	Conditions	Minimum	Maximum	Unit
V_{IN}	Supply Voltage			5.5	V
T_{STOR}	Storage Temperature		-40	105	°C

3.2 Recommended Operating Conditions

Symbol	Parameter	Minimum	Typical	Maximum	Unit
V_{IN}	Supply Voltage [1]		3.3		V
T_{AMB}	Ambient Operating Temperature	-20	–	85	°C

1. Must be regulated; changes in supply voltage will affect accuracy.

3.3 Electrical Characteristics

Symbol	Parameter	Conditions	Minimum	Typical	Maximum	Unit
I_{VIN}	Current Consumption			10		mA
F_{AIR}	Air Flow Range	FS3000-1005	0		7.23	m/sec
		FS3000-1015	0		15	
V_{OUT}	Digital Output	Min to Max of Flow Range	409	–	3686	Count
RES	Resolution				12	bit
E_{AIR}	Flow Accuracy	At 25°C		5		% of F.S.
t_{RESP}	Response Time			125		ms
f_{SCL}	SCL Clock Frequency				400	kHz
V_{IL}	I2C Input Logic Low Threshold				$0.3V_{DD}$ or 1.5	V
V_{IH}	I2C Input Logic High Threshold		$0.7V_{DD}$ or 3			V
V_{OL}	I2C Output Logic Low Threshold				0.4	V

1. m/sec: meter per second.
2. F.S: Full scale flow range.

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4. Typical Flow Graphs

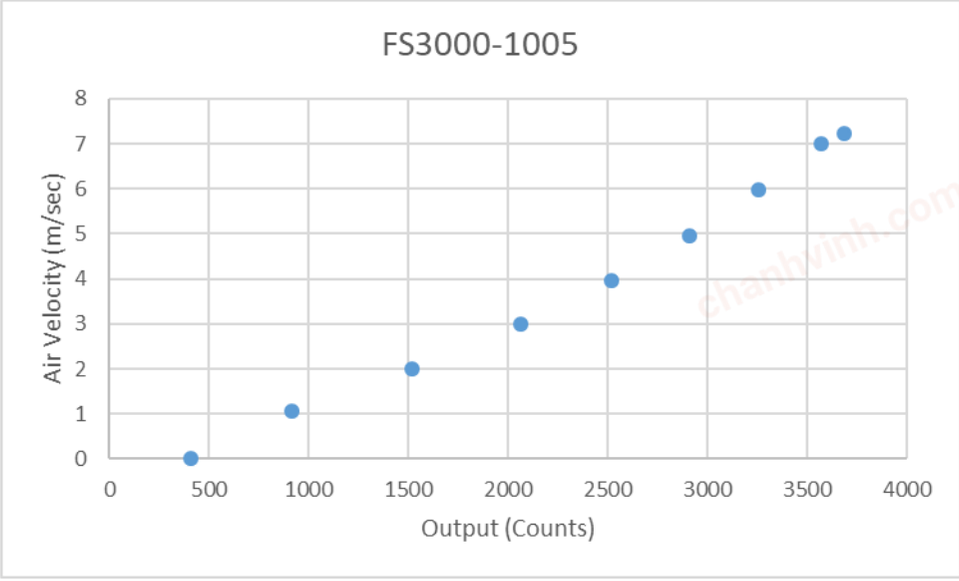


Figure 2. FS3000-1005 Output

Air Velocity (m/sec)	Output (Count)
0	409
1.07	915
2.01	1522
3.00	2066
3.97	2523
4.96	2908
5.98	3256
6.99	3572
7.23	3686

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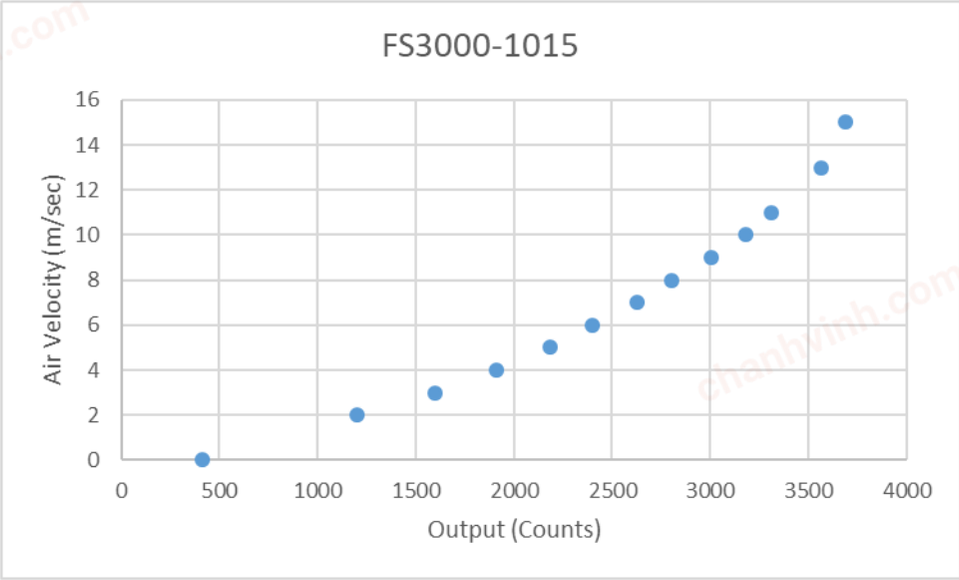


Figure 3. FS3000-1015 Output

Air Velocity (m/sec)	Output (Count)
0	409
2.00	1203
3.00	1597
4.00	1908
5.00	2187
6.00	2400
7.00	2629
8.00	2801
9.00	3006
10.00	3178
11.00	3309
13.00	3563
15.00	3686

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5. Functional Description

5.1 I2C Sensor Interface

The FS3000 includes an I2C digital, two-wire interface with a bidirectional data line (SDA) and a clock line (SCL). The two lines are open drain and connected to the supply voltage via two pull-up resistors (Rp). The FS3000 operates as a slave device on the I2C bus with support of 100kHz and 400kHz bit rates.

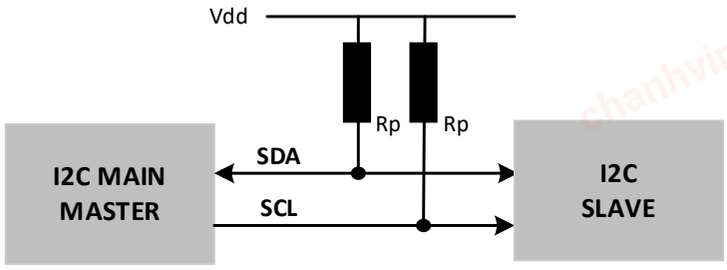


Figure 4. I2C Master-Slave Configuration

The recommended pull-up resistor (Rp) values depend on the system implementation, but a value between 2.2kΩ and 10kΩ can be used.

The capacitive load on both SDA and SCL should be the same; therefore, the signal lengths should be similar to avoid asymmetry.

5.1.1. Sensor Slave Address

The FS3000 default I2C address is 28_{HEX}. The device will respond only to this 7-bit address.

5.1.2. I2C Communication

The START condition is used to initiate I2C communication by the master. The sensor transmission is initiated when the master sends a 0 START bit (S). A HIGH-to-LOW transition on the SDA line while the SCL is HIGH indicates the beginning of a transmission.

The STOP condition is used to stop I2C communication by the master. The transmission is terminated when the master sends a 1 STOP bit (P). A LOW-to-HIGH transition on the SDA line while the SCL is HIGH indicates the end of a transmission.

All transfers consist of 8 bits and a response bit: 0 for Acknowledge (ACK) or 1 for Not Acknowledge (NACK). After the ACK is received, another data byte can be transferred or the communication can be stopped with a STOP bit.

The master expects an ACK back from the slave after each byte is transmitted. The slave pulls the SDA low to indicate that it has received a byte and then it frees the I2C bus again. If the slave does not initiate an ACK then it will consider it a NACK.

Data on the SDA line is always sampled on the rising edge of the SCL line and must remain stable while SCL is HIGH to prevent false START or STOP conditions.



Figure 5. START and STOP Condition Waveform

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5.2 Digital Output Measurements

The FS3000 continuously measures during operation. The data is sent in byte packages. Each byte is followed by an ACK from the slave. The most significant bit (MSB) is transmitted first.

To read the data, the following command is sent to the FS3000.

After the START bit, the master device sends the 7-bit slave address followed by an eighth bit = 1 (READ). The READ bit indicates a transmission from the FS3000 (slave) to master (see Figure 6).

The checksum used for data integrity is returned from the FS3000 followed by the two bytes of flow data.

The flow data is a 12-bit integer. Only the least significant four bits in the high byte are valid (see Figure 7).

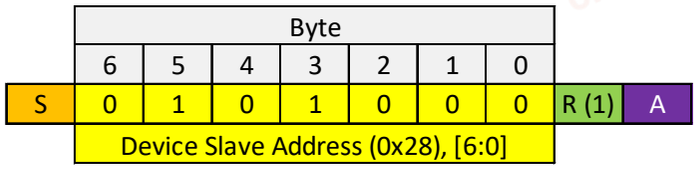


Figure 6. Flow Data Read Command

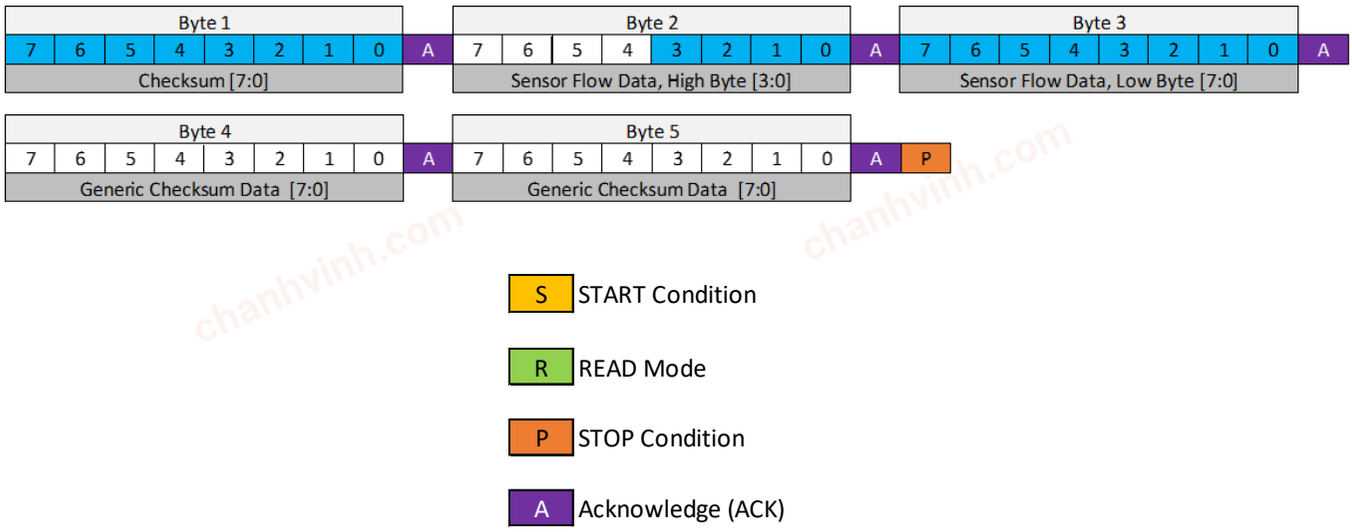


Figure 7. Flow Data from FS3000

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5.3 Calculating Checksum

The checksum used for data integrity is the 2’s complement (negative) of the 256-modulo (8-bit) sum of the data bytes (does not include I2C address).

Figure 7 shows the 5 bytes read:

Example:

Byte 1, 0xCC (Checksum)

Byte 2, 0x01

Byte 3, 0x99

Byte 4, 0x01 or 0x00

Byte 5, 0x99 or 0x00

The 256-modulo (8-bit) sum is calculated as:

$$sum = 0x01 + 0x99 + 0x01 + 0x99 = 0x134$$

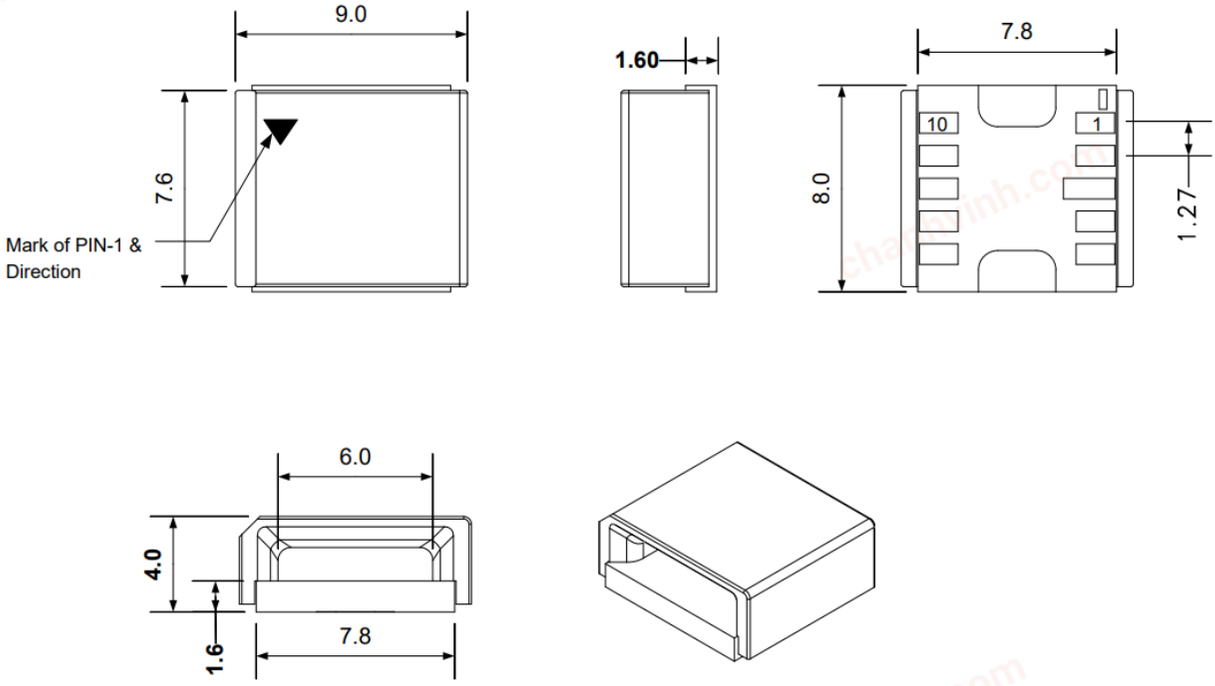
Validating the data payload is done by calculating the sum and adding it to the checksum. If the result is 0x00, then the data is valid.

$$checksum + sum = 0xCC + 0x134 = 0x00$$

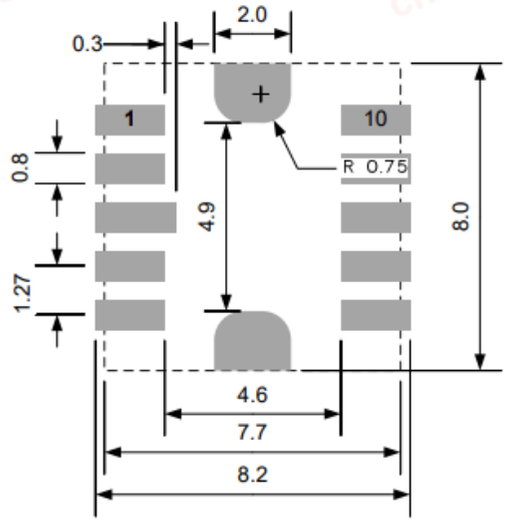
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6. Module Drawings

All dimensions are in mm.



PCB Footprint



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7. Ordering Information

Part Number	Description	MSL Rating	Carrier Type	Temp. Range
FS3000-1005	0 to 7 m/sec air velocity module	3	Reel	-20°C to +85°C
FS3000-1015	0 to 15 m/sec air velocity module	3		

8. Revision History

Revision	Date	Description
1.00	May 31, 2022	Initial release.

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