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MIC2860-2P

High Efficiency 2-Channel WLED Driver with PWM Control

Features

- High Efficiency (No Switching Losses)
- PWM Frequency as Low as 250 Hz
- Input Voltage Range: 3.0V to 5.5V
- Linear Driver Dropout of 52 mV at 30.2 mA
- Matching Better than ±0.5% (Typical)
- Current Accuracy Better than 1.0% (Typical)
- Available in Thin SOT-23 and SC-70 Packages

Applications

- Mobile Handsets
- Digital Cameras
- Portable Media/MP3 Players
- Portable Navigation Devices (GPS)
- Portable Applications

General Description

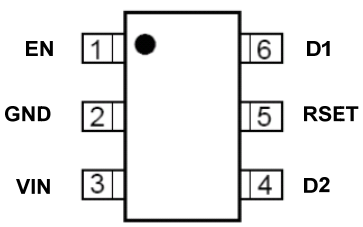
The MIC2860-2P is a high efficiency white LED (WLED) driver designed to drive two WLEDs and greatly extend battery life for portable display backlighting and keypad backlighting in low-cost mobile devices. The MIC2860-2P architecture provides the highest possible efficiency by eliminating switching losses present in traditional charge pumps or inductive boost circuits. It features a typical dropout of 52 mV at 30.2 mA per channel. This allows the WLEDs to be driven directly from the battery, eliminating switching noise and losses present with the use of boost circuitry.

The two channels have typical matching of ≤ ±0.5%, which ensures uniform display illumination under all conditions. The WLED's brightness is externally preset by a resistor and dimmed using PWM interface operating down to less than 1% duty cycle.

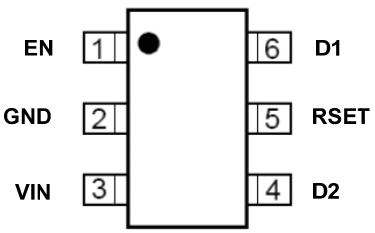
The MIC2860-2P is available in Thin SOT-23 and SC-70 6-lead packages with a junction temperature range of –40°C to +125°C.

Package Types

MIC2860-2P
6-Lead SC-70 (C6)
(Top View)



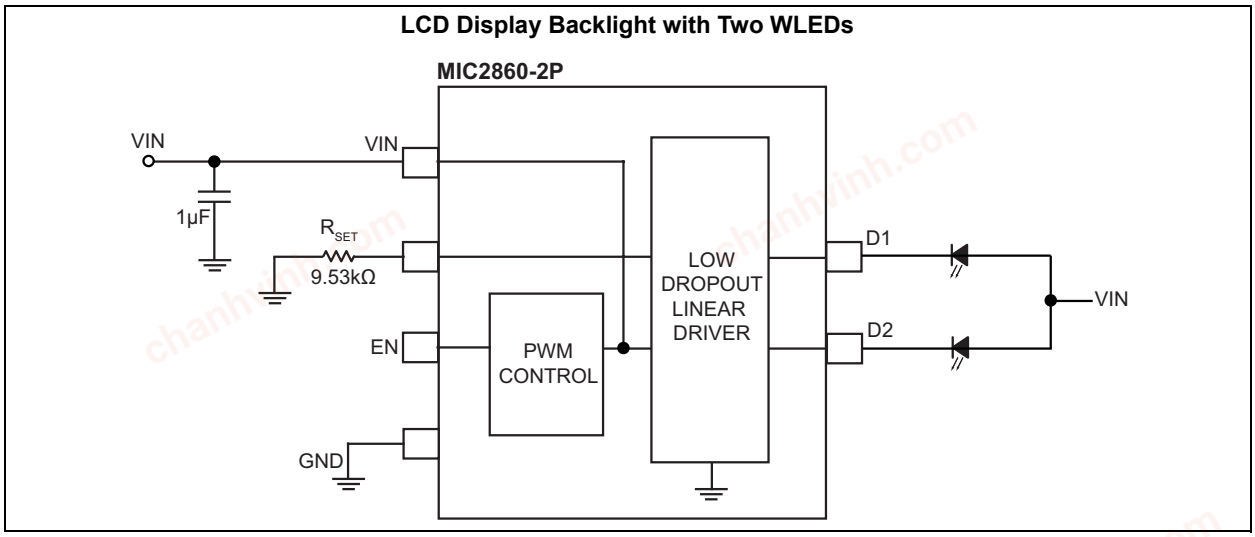
MIC2860-2P
6-Lead Thin SOT-23 (D6)
(Top View)



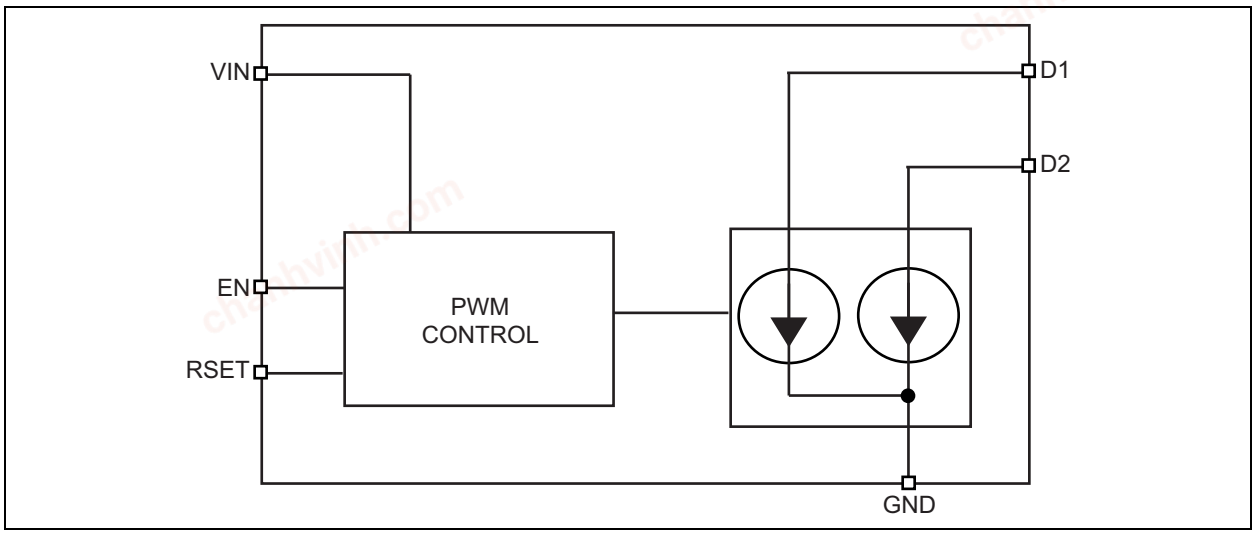
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Typical Application Circuit



Functional Block Diagram



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1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

Main Input Voltage (V_{IN}) ..... –0.3V to +6V
Enable Input Voltage (V_{EN}) ..... –0.3V to V_{IN}
LED Driver Voltage ($V_{D1, D2}$) ..... –0.3V to V_{IN}
Power Dissipation ..... Internally Limited
ESD Rating ..... 2 kV

Operating Ratings ‡

Supply Voltage (V_{IN}) ..... +3.0 to +5.5V
Enable Input Voltage (V_{EN}) ..... 0V to V_{IN}
LED Driver Voltage ($V_{D1, D2}$) ..... 0V to V_{IN}

† Notice: Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability.

‡ Notice: The device is not guaranteed to function outside its operating ratings.

ELECTRICAL CHARACTERISTICS

Electrical Characteristics: $V_{IN} = V_{EN} = 3.6V$, $C_{IN} = 1\ \mu F$, $R_{SET} = 9.53\ k\Omega$; $V_{D1, D2} = 0.6V$; $T_J = +25°C$, **bold** values valid for $–40°C \le T_J \le +85°C$; unless noted.

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Current Accuracy		**27.18**	30.2	**33.22**	mA	Note 1
Matching		—	±0.5	±3	%	Note 2
Dropout ($V_{D1, D2}$)	V_{DROP}	—	52	**125**	mV	Where I_{LED} = 90% of LED current seen at $V_{DROPNOM}$ = 0.6V, 100% brightness level
Ground/Supply Bias Current	I_{GND}	—	0.7	**1.5**	mA	I_{OUT} = 30.2 mA
Shutdown Current	I_{SHDN}	—	0.01	1	µA	Current source leakage, V_{EN} = 0V
PWM Dimming						
Enable Input Voltage	V_{EN}	—	—	**0.4**	V	Logic low
		1.4	—	—		Logic high
Enable Input Current	I_{EN}	—	0.01	**1**	µA	$V_{EN} \ge 1.4V$
Current Source Delay (50% Levels)		—	32	80	µs	Shutdown to ON
		—	1.5	4		Standby to ON
		—	0.3	—		ON to Standby
Current Source Transient Time	t_{RISE}	—	0.7	—	µs	10% to 90%
	t_{FALL}	—	0.2	—		
ON-to-Shutdown Time	t_{ON_SD}	4	5.8	10	ms	V_{EN} = 0V

Note 1: As determined by the average current of all channels in use and all channels loaded.
2: The current through each LED meets the stated limits from the average current of all LEDs.

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TEMPERATURE SPECIFICATIONS

Parameters	Sym.	Min.	Typ.	Max.	Units	Conditions
Temperature Ranges						
Lead Temperature	T_{LEAD}	—	—	+260	°C	Soldering, 10 sec.
Junction Temperature Range	T_J	–40	—	+125	°C	—
Storage Temperature	T_S	–65	—	+150	°C	—
Package Thermal Resistances						
Thermal Resistance, SC-70 6-Ld	θ_{JA}	—	256	—	°C/W	—
Thermal Resistance, TSOT 6-Ld	θ_{JA}	—	177	—	°C/W	—

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2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

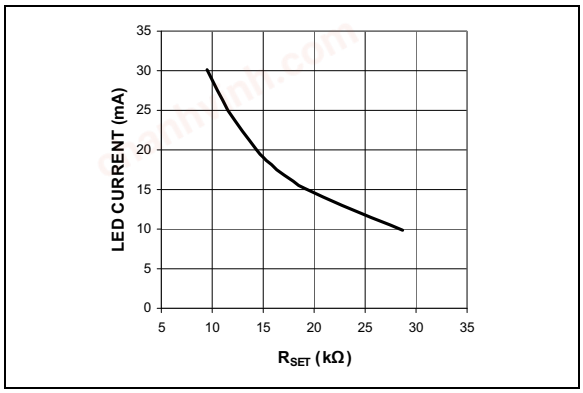


FIGURE 2-1: *Peak LED Current vs. R_{SET}.*

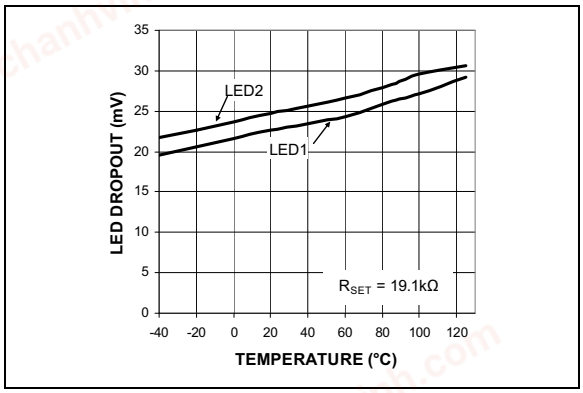


FIGURE 2-4: *Dropout Voltage vs. Temperature.*

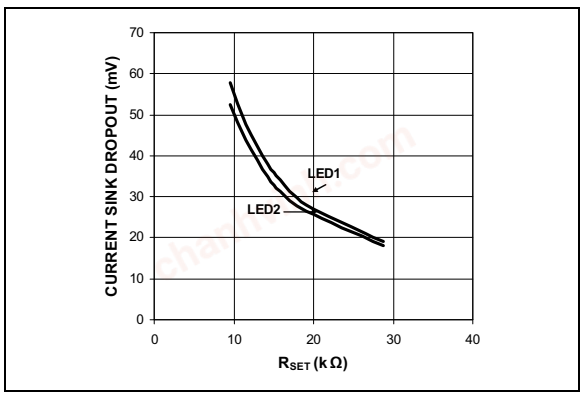


FIGURE 2-2: *Dropout Voltage vs. R_{SET}.*

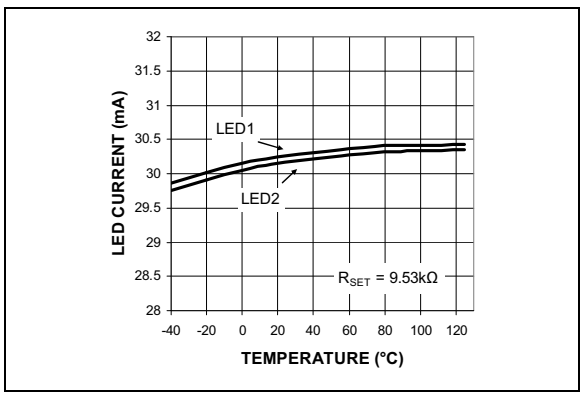


FIGURE 2-5: *LED Current vs. Temperature.*

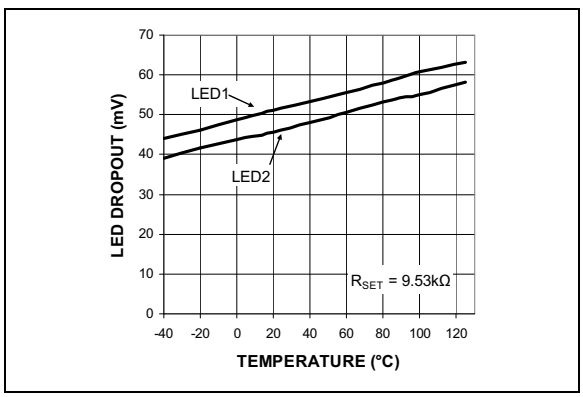


FIGURE 2-3: *Dropout Voltage vs. Temperature.*

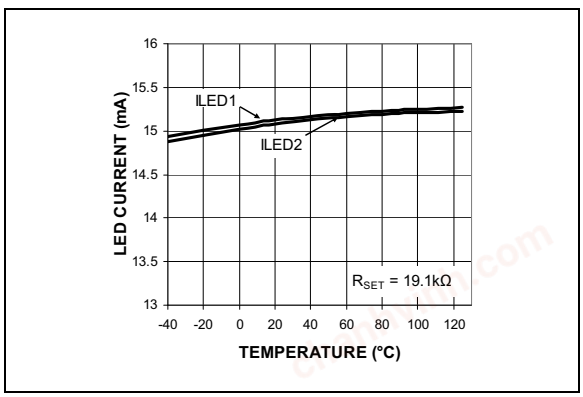


FIGURE 2-6: *LED Current vs. Temperature.*

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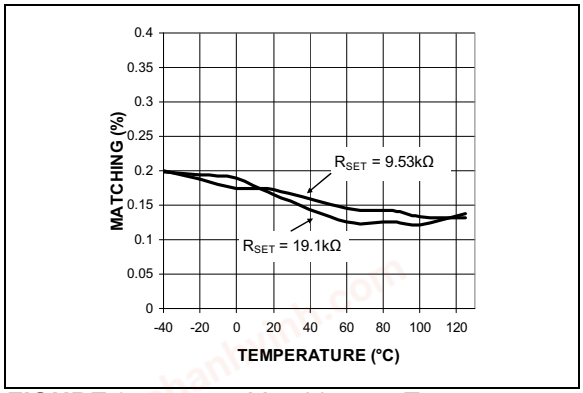


FIGURE 2-7: *Matching vs. Temperature.*

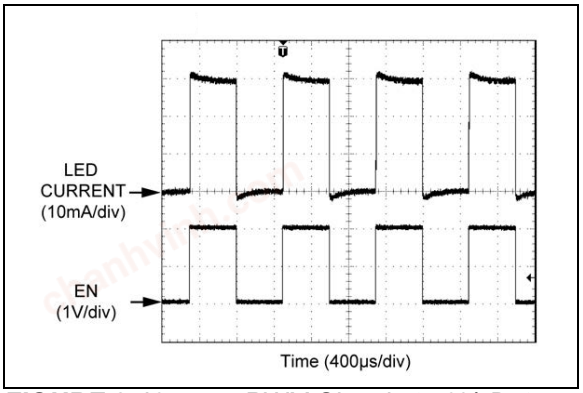


FIGURE 2-10: *PWM Signal at 50% Duty Cycle.*

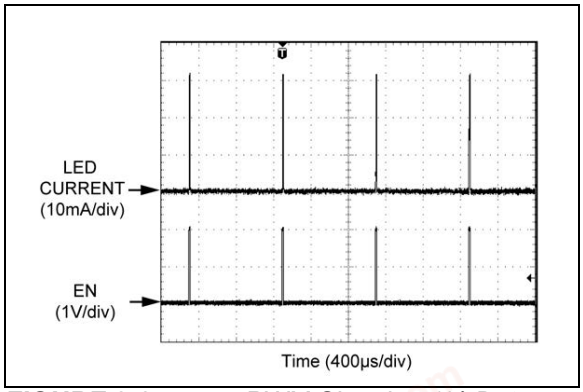


FIGURE 2-8: *PWM Signal at 1% Duty Cycle.*

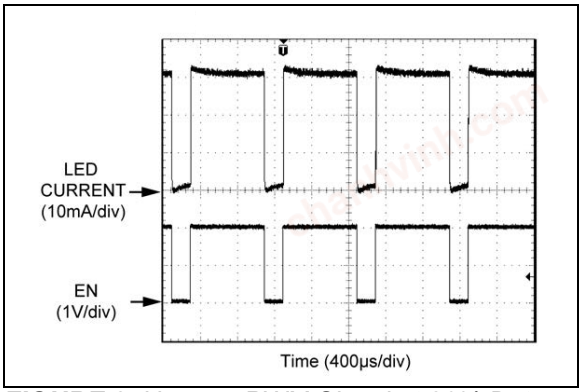


FIGURE 2-11: *PWM Signal at 80% Duty Cycle.*

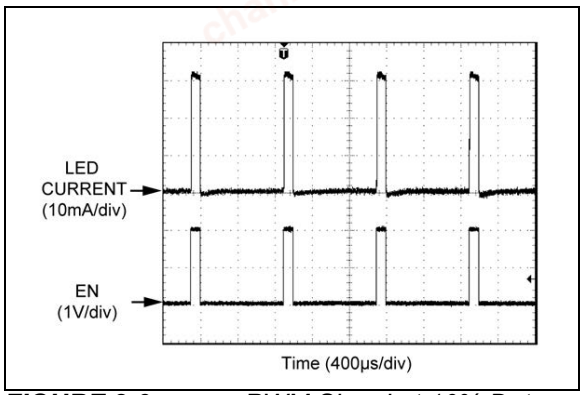


FIGURE 2-9: *PWM Signal at 10% Duty Cycle.*

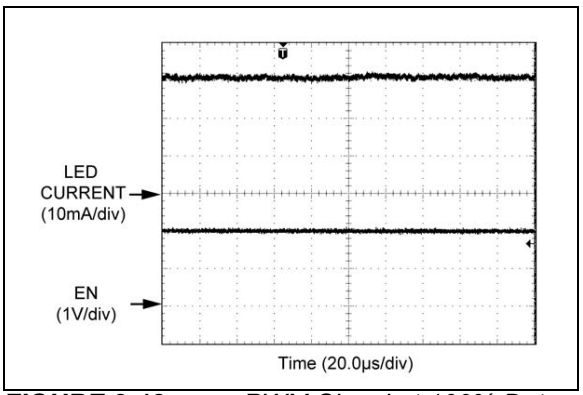


FIGURE 2-12: *PWM Signal at 100% Duty Cycle.*

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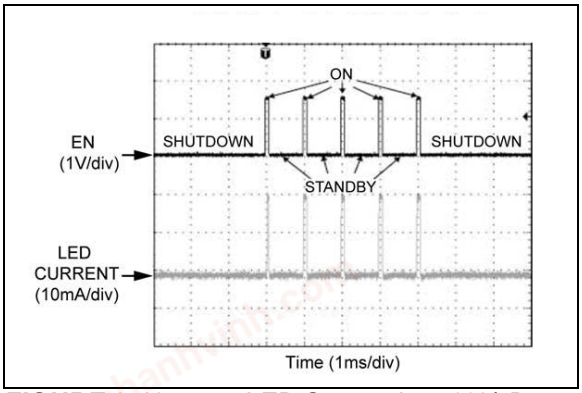


FIGURE 2-13: *LED Startup into 10% Duty Cycle.*

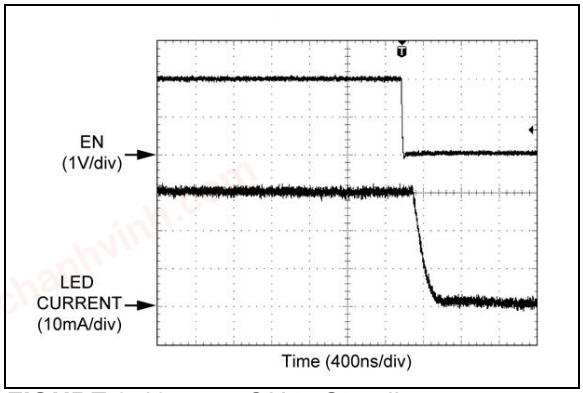


FIGURE 2-16: *ON to Standby.*

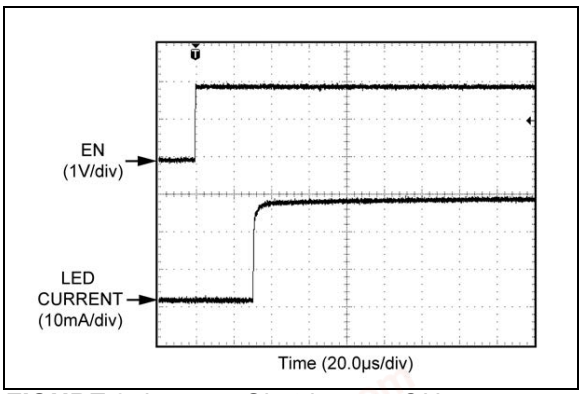


FIGURE 2-14: *Shutdown to ON.*

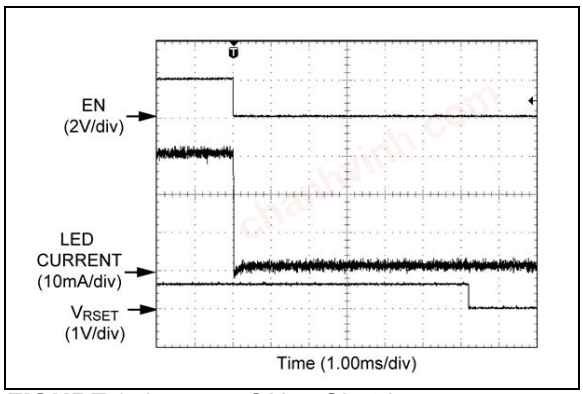


FIGURE 2-17: *ON to Shutdown.*

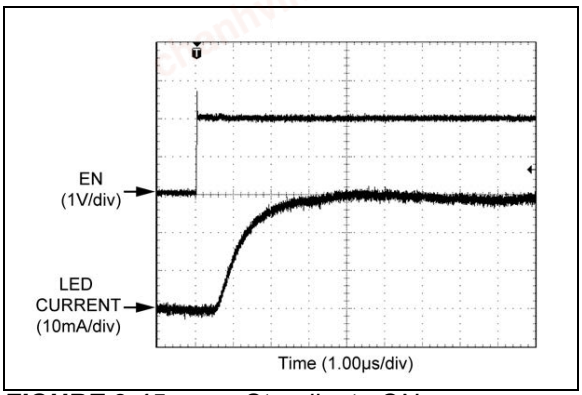


FIGURE 2-15: *Standby to ON.*

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3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 3-1.

TABLE 3-1: PIN FUNCTION TABLE

Pin Number	Pin Name	Description
1	EN	PWM Control Pin. This pin is used as a PWM input for dimming of WLEDs. Do not leave floating.
2	GND	Ground.
3	VIN	Voltage Input. Connect at least 1 µF ceramic capacitor between VIN and GND.
4	D2	LED2 driver. Connect LED anode to VIN and cathode to this pin. Do not leave floating.
5	RSET	An internal 1.27V reference sets the nominal maximum WLED current. Example, apply a 9.53 kΩ resistor between RSET and GND to set LED current to 30.2 mA at 100% duty cycle.
6	D1	LED1 driver. Connect LED anode to VIN and cathode to this pin. Do not leave floating.

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4.0 FUNCTIONAL DESCRIPTION

The MIC2860-2P is a two channel WLED driver. The WLED driver is designed to maintain proper current regulation with LED current accuracy of 1% with a typical matching between the 2 channels of ±0.5%. The WLEDs are driven independently from the input supply and will maintain regulation with a dropout of 52 mV at 30.2 mA. The low dropout of the linear drivers allows the WLEDs to be driven directly from the battery voltage and eliminates the need for large and inefficient charge pumps. The peak WLED current for each channel is set via an external resistor. If dimming is desired the MIC2860-2P can dim via a PWM signal.

4.1 Block Diagram

As shown in the Functional Block Diagram, the MIC2860-2P consists of two current sinks with the peak current determined by R_{SET}. The linear drivers have a designated control block for enabling and dimming of the WLEDs. The MIC2860-2P is controlled by the PWM control block that receives PWM signals for dimming.

4.2 VIN

The input supply (VIN) provides power to the linear drivers and the control circuitry. The VIN operating range is 3V to 5.5V. Due to wire inductance, a minimum bypass capacitor of 1 µF should be placed close to input (VIN) pin and the ground (GND) pin.

4.3 EN

The EN pin enables the linear drivers. It can also be used for dimming with a PWM signal. See the PWM Dimming Interface section for details. Do not leave floating.

4.4 R_{SET}

The R_{SET} pin is used by connecting a R_{SET} resistor to ground to set the peak current of the linear drivers. The maximum LED current (EN = 100% Duty Cycle) set by the R_{SET} resistor is shown in the table below:

TABLE 4-1: MAX. LED CURRENT VS. R_{SET} RESISTOR VALUES

R_{SET}	I_{LED}
9.53 kΩ	30.2 mA
11.5 kΩ	25.0 mA
14.3 kΩ	20.1 mA
15.8 kΩ	18.2 mA
19.1 kΩ	15.1 mA
28.7 kΩ	10.0 mA

A plot of I_{LED} versus R_{SET} is shown in Figure 4-1.

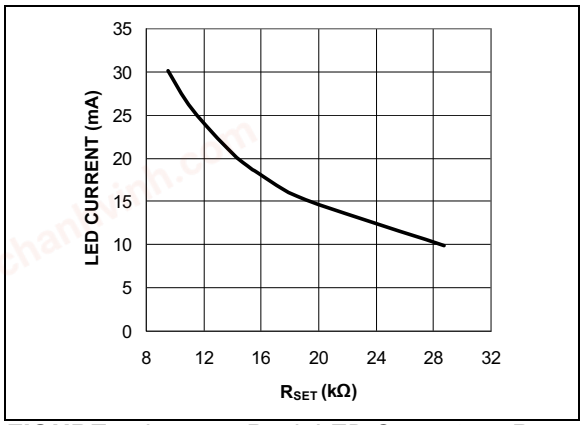


FIGURE 4-1: *Peak LED Current vs. R_{SET}.*

4.5 D1, D2

The D1 and D2 pins are the linear driver inputs for WLED 1 and 2, respectively. Connect the anodes of the WLEDs to VIN and each cathode of the WLEDs to D1 and D2. When operating with a single WLED, D1 and D2 should be connected to the WLED cathode to parallel the outputs for improved dropout performance. Paralleling these pins can also be done to drive a higher current through a single WLED. Do not leave these pins floating.

4.6 GND

The ground pin is the ground path for the linear drivers. The current loop for the ground should be as small as possible. The ground of the input capacitor should be routed with low impedance traces to the GND pin and made as short as possible.

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5.0 APPLICATION INFORMATION

5.1 PWM Dimming Interface

The MIC2860-2P can receive PWM signals from the EN pin for WLED dimming. Dimming is generated by pulsing the WLEDs on and off in synchronization with the PWM signal. The MIC2860-2P incorporates an internal shutdown delay to ensure that the internal control circuitry remains active during PWM dimming for optimum performance.

The lower PWM frequency range is recommended at 250 Hz due to the minimum standby to shutdown time of 4 ms. Because the period of 250 Hz is 4 ms, a 1% duty cycle would have an on time of 40 µs and an off time of 3.96 ms. To support operation down to 1% duty cycle, the maximum off time must not exceed 4 ms or the drivers may go into the low I_Q shutdown state.

With PWM frequencies higher than 500 Hz the t_{RISE} (0.7 µs) and t_{FALL} (0.2 µs) times will have a greater effect on the accuracy of the outputs. An upper frequency of 500 Hz is recommended to maintain output accuracy with duty cycles down to 1%. For systems that do not require a duty cycle below 1%, the frequency of the PWM signal may be increased. For example, with a minimum duty cycle of 10% (3.02 mA with R_{SET} = 9.53 kΩ) the PWM frequency can be increased to 10 kHz and still maintain accuracy.

5.2 Input Capacitor

The MIC2860-2P is a high performance, high bandwidth device. Stability can be maintained using a ceramic input capacitor of 1 µF. Low-ESR ceramic capacitors provide optimal performance with a minimum amount of space. Additional high frequency capacitors, such as small valued NPO dielectric type capacitors, help filter out high-frequency noise and are good practice in any noise-sensitive circuit. X5R or X7R dielectrics are recommended for the input capacitor. Y5V dielectrics lose most of their capacitance over temperature and are therefore, not recommended.

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6.0 TYPICAL APPLICATION SCHEMATIC

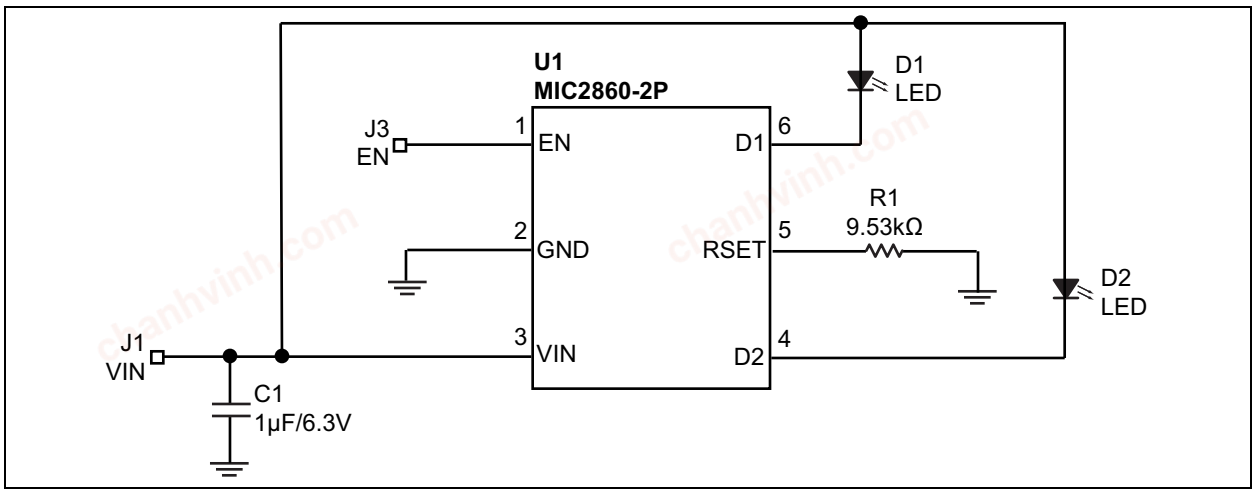


TABLE 6-1: BILL OF MATERIALS

Item	Part Number	Manufacturer	Description	Qty.
C1	C1608X5R0J105K	TDK	1 µF Ceramic Capacitor, 6.3V, X5R, Size 0603	1
R1	CRCW06032052FT1	Vishay	9.53 kΩ, 1%, Size 0603	1
U1	MIC2860-2PYC6	Microchip	2-Channel PWM Linear WLED Driver, SC-70	1
	MIC2860-2PYD6		2-Channel PWM Linear WLED Driver, SOT-23	

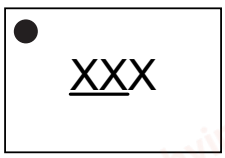
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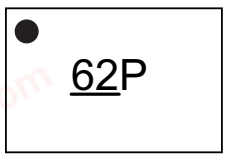
7.0 PACKAGING INFORMATION

7.1 Package Marking Information

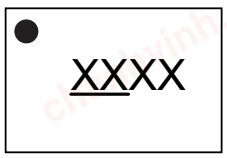
6-Lead SC-70* (Front)



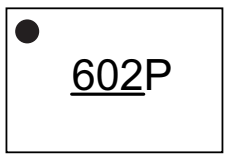
Example



6-Lead TSOT-23* (Front)



Example



6-Lead SC-70* (Back)



Example



6-Lead TSOT-23* (Back)



Example



Legend:	XX...X	Product code or customer-specific information
	Y	Year code (last digit of calendar year)
	YY	Year code (last 2 digits of calendar year)
	WW	Week code (week of January 1 is week ‘01’)
	NNN	Alphanumeric traceability code
	(e3)	Pb-free JEDEC® designator for Matte Tin (Sn)
	*	This package is Pb-free. The Pb-free JEDEC designator ((e3)) can be found on the outer packaging for this package.
	●, ▲, ▼	Pin one index is identified by a dot, delta up, or delta down (triangle mark).
Note:		In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information. Package may or may not include the corporate logo. Underbar (_) symbol may not be to scale.

Note: If the full seven-character YYWWNNN code cannot fit on the package, the following truncated codes are used based on the available marking space:
6 Characters = YWWNNN; 5 Characters = WWNNN; 4 Characters = WNNN; 3 Characters = NNN;
2 Characters = NN; 1 Character = N

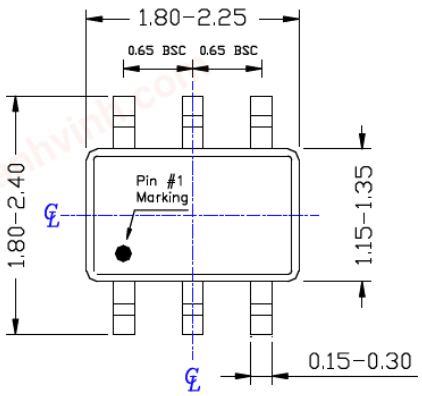
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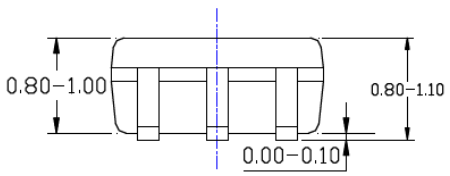
6-Lead SC-70 Package Outline and Recommended Land Pattern

TITLE
6 LEAD SC70 PACKAGE OUTLINE & RECOMMENDED LAND PATTERN

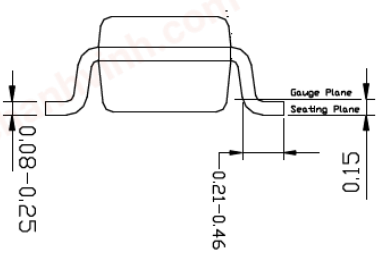
DRAWING #	SC70-6LD-PL-1	UNIT	MM



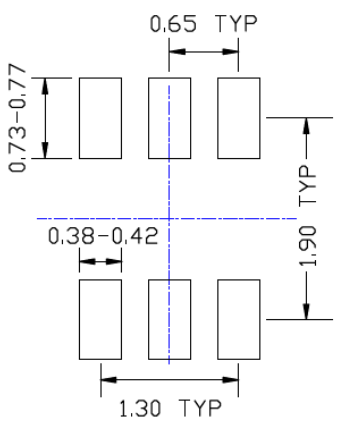
TOP VIEW



SIDE VIEW



END VIEW



RECOMMENDED
LAND PATTERN

NOTE:
1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSIONS ARE INCLUSIVE OF PLATING.
3. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH & METAL BURR.

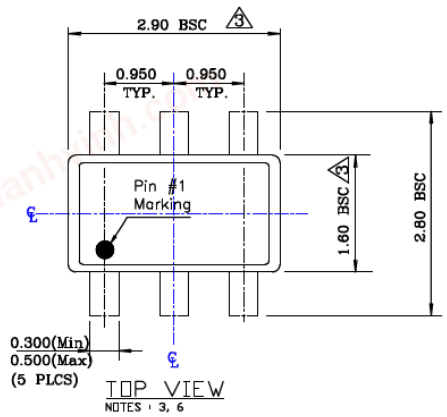
Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging.

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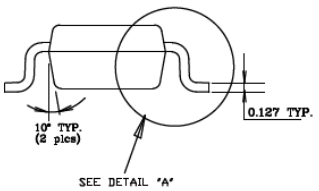
6-Lead Thin SOT-23 Package Outline and Recommended Land Pattern

TITLE
6 LEAD TSOT PACKAGE OUTLINE & RECOMMENDED LAND PATTERN

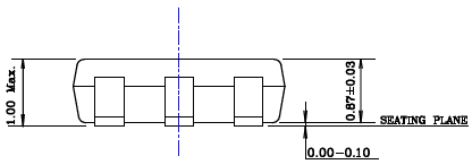
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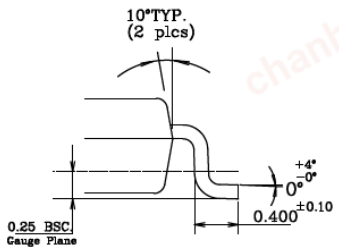
TOP VIEW
NOTES : 3, 6



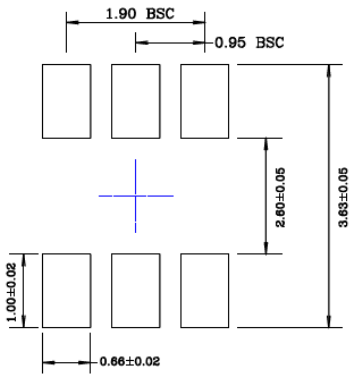
END VIEW
NOTES : 2, 4, 6



TOP VIEW
NOTES : 4, 6



DETAIL "A"



RECOMMENDED LAND PATTERN

NOTE:

1. Dimensions and tolerances are as per ANSI Y14.5M, 1994.
2. Die is facing up for mold. Die is facing down for trim/form, ie. reverse trim/form.
3. Dimensions are exclusive of mold flash and gate burr.
4. The footlength measuring is based on the gauge plane method.
5. All specification comply to Jedec Spec MO193 Issue C.
6. All dimensions are in millimeters.

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging.

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APPENDIX A: REVISION HISTORY

Revision A (June 2022)

- Converted Micrel document MIC2860-2P to Microchip data sheet DS20006695A.
- Minor text changes throughout.

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PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

Part Number	X	XX	-XX
Device	Temp. Range	Package	Media Type

Device:	MIC2860-2P:	High Efficiency 2-Channel WLED Driver with PWM Control
Temperature Range:	Y =	–40°C to +125°C
Package:	C6 =	6-Lead SC-70
	D6 =	6-Lead Thin SOT-23
Media Type:	TR =	3,000/Reel

Examples:

a) MIC2860-2PYC6-TR: MIC2860-2P, –40°C to +125°C Temp. Range, 6-Lead SC-70, 3,000/Reel

b) MIC2860-2PYD6-TR: MIC2860-2P, –40°C to +125°C Temp. Range, 6-Lead TSOT-23, 3,000/Reel

Note 1: Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option.

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