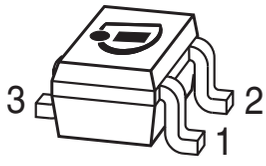
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BFR182W

Low Noise Silicon Bipolar RF Transistor



- For low noise, high-gain broadband amplifiers at collector currents from 1 mA to 20 mA
- f_T = 8 GHz, NF_{min} = 0.9 dB at 900 MHz
- Pb-free (RoHS compliant) and halogen-free package with visible leads
- Qualification report according to AEC-Q101 available

ESD (**E**lectro**s**tatic **d**ischarge) sensitive device, observe handling precaution!

Type	Marking	Pin Configuration			Package
BFR182W	RGs	1=B	2=E	3=C	SOT323

Maximum Ratings at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}	12	V
Collector-emitter voltage	V_{CES}	20	
Collector-base voltage	V_{CBO}	20	
Emitter-base voltage	V_{EBO}	2	
Collector current	I_C	35	mA
Base current	I_B	4	
Total power dissipation[1)] $T_S \leq 90$ °C	P_{tot}	250	mW
Junction temperature	T_J	150	°C
Ambient temperature	T_A	-65 ... 150	
Storage temperature	T_{Stg}	-65 ... 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point[2)]	R_{thJS}	240	K/W

[1] T_S is measured on the collector lead at the soldering point to the pcb

[2] For the definition of R_{thJS} please refer to Application Note AN077 (Thermal Resistance Calculation)

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Electrical Characteristics at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage I_C = 1 mA, I_B = 0	$V_{(BR)CEO}$	12	-	-	V
Collector-emitter cutoff current V_{CE} = 4 V, V_{BE} = 0 V_{CE} = 15 V, V_{BE} = 0 V, T_A = 85 °C (verified by random sampling)	I_{CES}	 - -	 1 5	 30 70	nA
Collector-base cutoff current V_{CB} = 4 V, I_E = 0	I_{CBO}	-	1	30	
Emitter-base cutoff current V_{EB} = 1 V, I_C = 0	I_{EBO}	-	-	50	
DC current gain I_C = 10 mA, V_{CE} = 8 V, pulse measured	h_{FE}	70	100	140	-

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Electrical Characteristics at T_A = 25 °C, unless otherwise specified

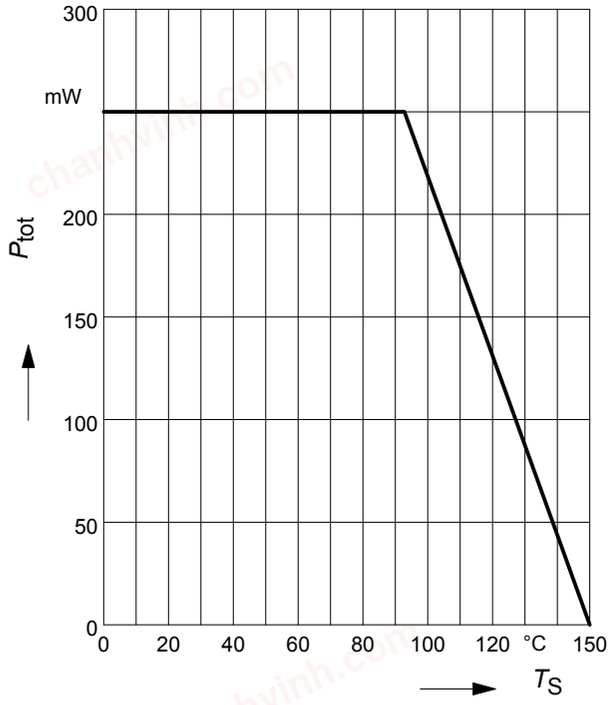
Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sampling)					
Transition frequency I_C = 15 mA, V_{CE} = 8 V, f = 500 MHz	f_T	6	8	-	GHz
Collector-base capacitance V_{CB} = 10 V, f = 1 MHz, V_{BE} = 0 , emitter grounded	C_{cb}	-	0.34	0.5	pF
Collector emitter capacitance V_{CE} = 10 V, f = 1 MHz, V_{BE} = 0 , base grounded	C_{ce}	-	0.26	-	
Emitter-base capacitance V_{EB} = 0.5 V, f = 1 MHz, V_{CB} = 0 , collector grounded	C_{eb}	-	0.8	-	
Minimum noise figure I_C = 3 mA, V_{CE} = 8 V, Z_S = Z_{Sopt}, f = 900 MHz I_C = 3 mA, V_{CE} = 8 V, Z_S = Z_{Sopt}, f = 1.8 GHz	NF_{min}	 - -	 0.9 1.3	 - -	dB
Power gain, maximum stable[1)] I_C = 10 mA, V_{CE} = 8 V, Z_S = Z_{Sopt}, Z_L = Z_{Lopt} , f = 900 MHz	G_{ms}	-	19	-	dB
Power gain, maximum available[2)] I_C = 10 mA, V_{CE} = 8 V, Z_S = Z_{Sopt}, Z_L = Z_{Lopt} , f = 1.8 GHz	G_{ma}	-	12.5	-	dB
Transducer gain I_C = 10 mA, V_{CE} = 8 V, Z_S = Z_L = 50 Ω, f = 900 MHz I_C = 10 mA, V_{CE} = 8 V, Z_S = Z_L = 50 Ω, f = 1.8 GHz	$\lvert S_{21e}\rvert^2$	 - -	 15.5 10	 - -	dB

[1] $G_{ms} = |S_{21} / S_{12}|$

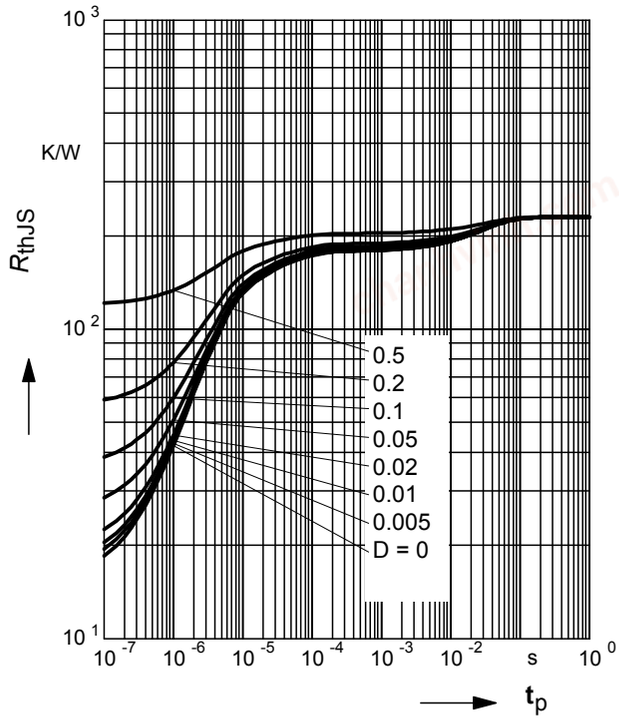
[2] $G_{ma} = |S_{21e} / S_{12e}| \, (k-(k^2-1)^{1/2})$

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Total power dissipation $P_{tot} = f(T_S)$

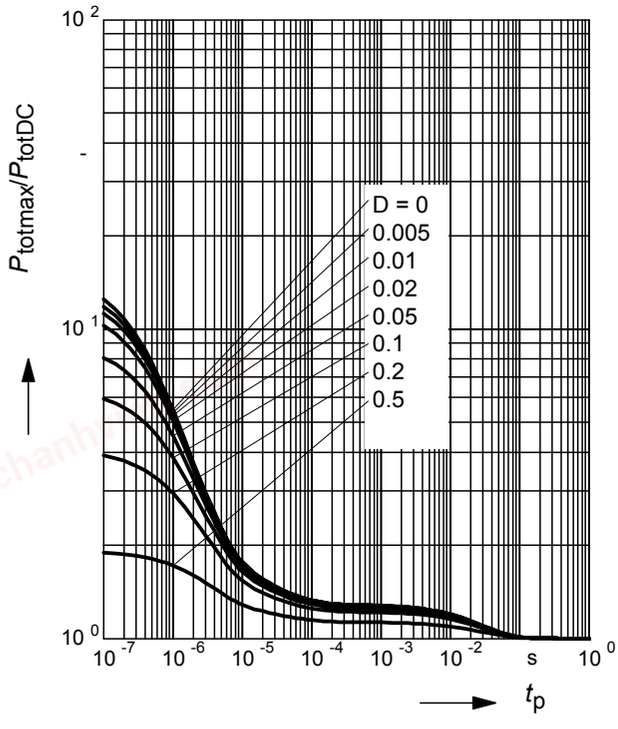


Permissible Pulse Load $R_{thJS} = f(t_p)$



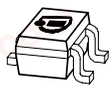
Permissible Pulse Load

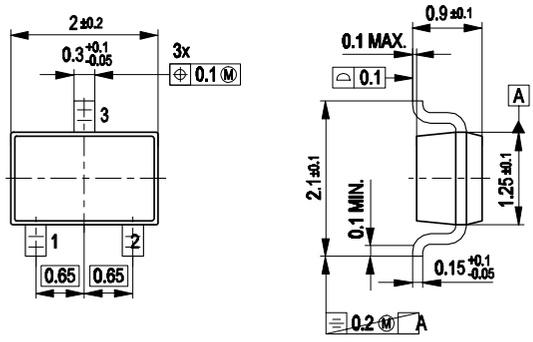
$P_{totmax}/P_{totDC} = f(t_p)$



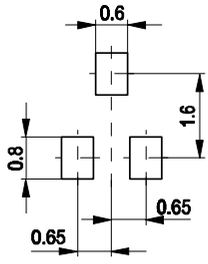
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Package Outline

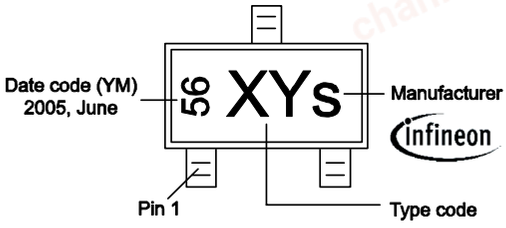




Foot Print

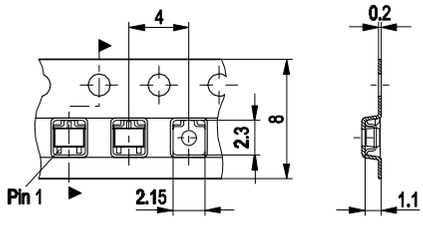


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel
Reel ø330 mm = 10.000 Pieces/Reel



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